

International IR Rectifier

- Generation V Technology
- Ultra Low On-Resistance
- Dual N and P Channel MOSFET
- Surface Mount
- Very Low Gate Charge and Switching Losses
- Fully Avalanche Rated

Description

Fifth Generation HEXFETs from International Rectifier utilize advanced processing techniques to achieve extremely low on-resistance per silicon area. This benefit, combined with the fast switching speed and ruggedized device design that HEXFET Power MOSFETs are well known for, provides the designer with an extremely efficient and reliable device for use in a wide variety of applications.

The SO-8 has been modified through a customized leadframe for enhanced thermal characteristics and multiple-die capability making it ideal in a variety of power applications. With these improvements, multiple devices can be used in an application with dramatically reduced board space. The package is designed for vapor phase, infra red, or wave soldering techniques.

	Symbol	Maximum		Units
		N-Channel	P-Channel	
Drain-Source Voltage	V _{DS}	30		V
Gate-Source Voltage	V _{GGS}	± 20		
Continuous Drain Current ^⑤	T _A = 25°C	I _D	3.5	A
	T _A = 70°C		2.8	
Pulsed Drain Current	I _{PD}	16	-10	
Continuous Source Current (Diode Conduction)	I _S	1.7	-1.3	
Maximum Power Dissipation ^⑤	T _A = 25°C	P _D	2.0	W
	T _A = 70°C		1.3	
Single Pulse Avalanche Energy	E _{AS}	44	57	mJ
Avalanche Current	I _{AR}	2.0	-1.3	A
Repetitive Avalanche Energy	E _{AR}	0.25		mJ
Peak Diode Recovery dv/dt ^②	dv/dt	5.0	-5.0	V/ns
Junction and Storage Temperature Range	T _J , T _{STG}	-55 to + 150 °C		

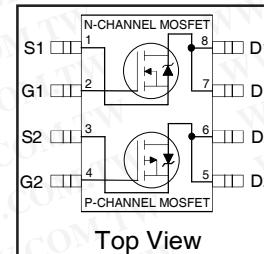
Thermal Resistance Ratings

Parameter	Symbol	Limit	Units
Maximum Junction-to-Ambient ^⑥	R _{θJA}	62.5	°C/W

PD - 91561B

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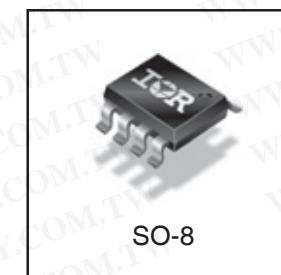
HEXFET® Power MOSFET



	N-Ch	P-Ch
V _{DSS}	30V	-30V
R _{DS(on)}	0.10Ω	0.25Ω

Recommended upgrade: IRF7309 or IRF7319

Lower profile/smaller equivalent: IRF7509



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Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(\text{BR})\text{DSS}}$	Drain-to-Source Breakdown Voltage	N-Ch 30	—	—	V	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$
		P-Ch -30	—	—		$V_{GS} = 0\text{V}, I_D = -250\mu\text{A}$
$\Delta V_{(\text{BR})\text{DSS}/\Delta T_J}$	Breakdown Voltage Temp. Coefficient	N-Ch —	0.015	—	V/ $^\circ\text{C}$	Reference to 25°C , $I_D = 1\text{mA}$
		P-Ch —	0.015	—		Reference to 25°C , $I_D = -1\text{mA}$
$R_{DS(\text{ON})}$	Static Drain-to-Source On-Resistance	N-Ch —	0.08	0.10	Ω	$V_{GS} = 10\text{V}, I_D = 2.2\text{A}$ ④
		—	0.12	0.15		$V_{GS} = 4.5\text{V}, I_D = 1.0\text{A}$ ④
		—	0.165	0.250		$V_{GS} = -10\text{V}, I_D = -1.0\text{A}$ ④
		—	0.290	0.400		$V_{GS} = -4.5\text{V}, I_D = -0.50\text{A}$ ④
$V_{GS(\text{th})}$	Gate Threshold Voltage	N-Ch 1.0	—	—	V	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$
		P-Ch -1.0	—	—		$V_{DS} = V_{GS}, I_D = -250\mu\text{A}$
g_{fs}	Forward Transconductance	N-Ch —	12	—	S	$V_{DS} = 15\text{V}, I_D = 3.5\text{A}$ ④
		P-Ch —	2.4	—		$V_{DS} = -15\text{V}, I_D = -2.3\text{A}$ ④
I_{DSS}	Drain-to-Source Leakage Current	N-Ch —	—	2.0	μA	$V_{DS} = 24\text{V}, V_{GS} = 0\text{V}$
		P-Ch —	—	-2.0		$V_{DS} = -24\text{V}, V_{GS} = 0\text{V}$
		N-Ch —	—	25		$V_{DS} = 24\text{V}, V_{GS} = 0\text{V}, T_J = 125^\circ\text{C}$
		P-Ch —	—	-25		$V_{DS} = -24\text{V}, V_{GS} = 0\text{V}, T_J = 125^\circ\text{C}$
I_{GSS}	Gate-to-Source Forward Leakage	N-P —	—	± 100	nA	$V_{GS} = \pm 20\text{V}$
Q_g	Total Gate Charge	N-Ch —	6.9	14	nC	N-Channel
		P-Ch —	6.1	12		$I_D = 1.8\text{A}, V_{DS} = 10\text{V}, V_{GS} = 10\text{V}$
Q_{gs}	Gate-to-Source Charge	N-Ch —	1.0	2.0	nC	P-Channel
		P-Ch —	1.7	3.4		$I_D = -2.3\text{A}, V_{DS} = -10\text{V}, V_{GS} = -10\text{V}$
Q_{gd}	Gate-to-Drain ("Miller") Charge	N-Ch —	1.8	3.5	nC	
		P-Ch —	1.1	2.2		
$t_{d(on)}$	Turn-On Delay Time	N-Ch —	6.2	12	ns	N-Channel
		P-Ch —	9.7	19		$V_{DD} = 10\text{V}, I_D = 1.0\text{A}, R_G = 6.0\Omega, R_D = 10\Omega$
t_r	Rise Time	N-Ch —	8.8	18	ns	P-Channel
		P-Ch —	14	28		$V_{DD} = -10\text{V}, I_D = -1.0\text{A}, R_G = 6.0\Omega, R_D = 10\Omega$
$t_{d(off)}$	Turn-Off Delay Time	N-Ch —	13	26	ns	
		P-Ch —	20	40		
t_f	Fall Time	N-Ch —	3.0	6.0	ns	
		P-Ch —	6.9	14		
C_{iss}	Input Capacitance	N-Ch —	190	—	pF	N-Channel
		P-Ch —	190	—		$V_{GS} = 0\text{V}, V_{DS} = 15\text{V}, f = 1.0\text{MHz}$
C_{oss}	Output Capacitance	N-Ch —	120	—	pF	P-Channel
		P-Ch —	110	—		$V_{GS} = 0\text{V}, V_{DS} = -15\text{V}, f = 1.0\text{MHz}$
C_{rss}	Reverse Transfer Capacitance	N-Ch —	61	—	pF	
		P-Ch —	54	—		

Source-Drain Ratings and Characteristics

	Parameter	Min.	Typ.	Max.	Units	Conditions
I_S	Continuous Source Current (Body Diode)	N-Ch —	—	1.7	A	
		P-Ch —	—	-1.3		
I_{SM}	Pulsed Source Current (Body Diode) ④	N-Ch —	—	16		
		P-Ch —	—	16		
V_{SD}	Diode Forward Voltage	N-Ch —	0.82	1.2	V	$T_J = 25^\circ\text{C}, I_S = 1.25\text{A}, V_{GS} = 0\text{V}$ ④
		P-Ch —	-0.82	-1.2		$T_J = 25^\circ\text{C}, I_S = -1.25\text{A}, V_{GS} = 0\text{V}$ ④
t_{rr}	Reverse Recovery Time	N-Ch —	27	53	ns	N-Channel
		P-Ch —	27	54		$T_J = 25^\circ\text{C}, I_F = 1.25\text{A}, di/dt = 100\text{A}/\mu\text{s}$
Q_{rr}	Reverse Recovery Charge	N-Ch —	28	57	nC	P-Channel
		P-Ch —	31	62		$T_J = 25^\circ\text{C}, I_F = -1.25\text{A}, di/dt = 100\text{A}/\mu\text{s}$

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature. (See fig. 23)
- ② N-Channel $I_{SD} \leq 2.0\text{A}$, $di/dt \leq 100\text{A}/\mu\text{s}$, $V_{DD} \leq V_{(\text{BR})\text{DSS}}$, $T_J \leq 150^\circ\text{C}$
 P-Channel $I_{SD} \leq -1.3\text{A}$, $di/dt \leq 84\text{A}/\mu\text{s}$, $V_{DD} \leq V_{(\text{BR})\text{DSS}}$, $T_J \leq 150^\circ\text{C}$
- ③ N-Channel Starting $T_J = 25^\circ\text{C}$, $L = 22\text{mH}$ $R_G = 25\Omega$, $I_{AS} = 2.0\text{A}$. (See Figure 12)
 P-Channel Starting $T_J = 25^\circ\text{C}$, $L = 67\text{mH}$ $R_G = 25\Omega$, $I_{AS} = -1.3\text{A}$.
- ④ Pulse width $\leq 300\mu\text{s}$; duty cycle $\leq 2\%$.
- ⑤ Surface mounted on FR-4 board, $t \leq 10\text{sec}$.

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N-Channel

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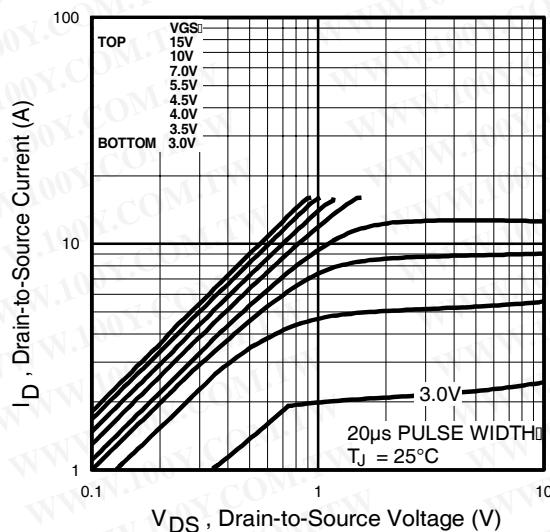


Fig 1. Typical Output Characteristics

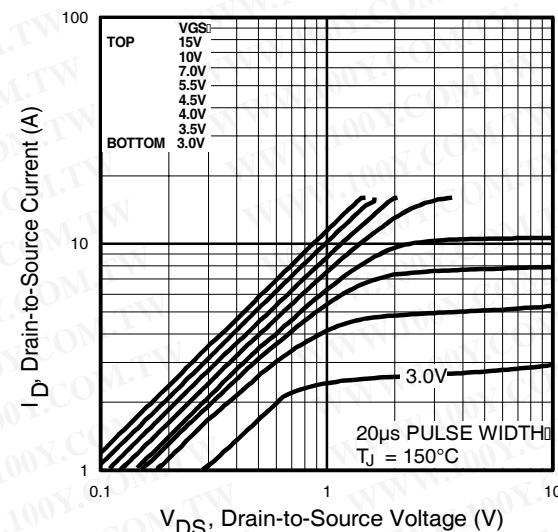


Fig 2. Typical Output Characteristics

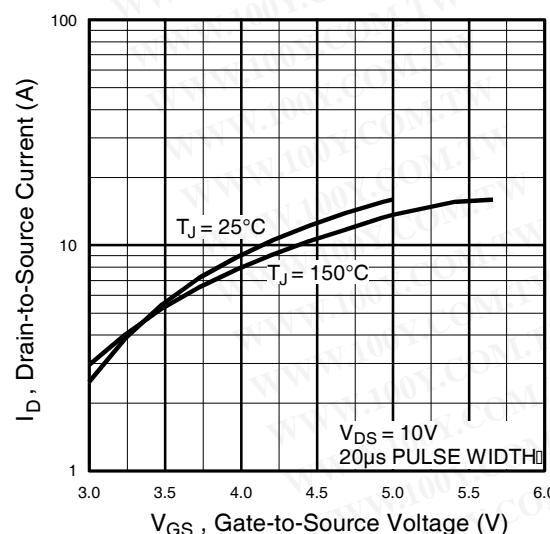


Fig 3. Typical Transfer Characteristics

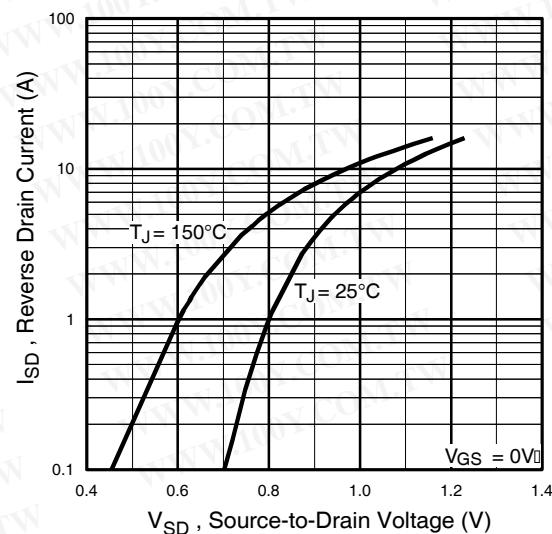


Fig 4. Typical Source-Drain Diode Forward Voltage

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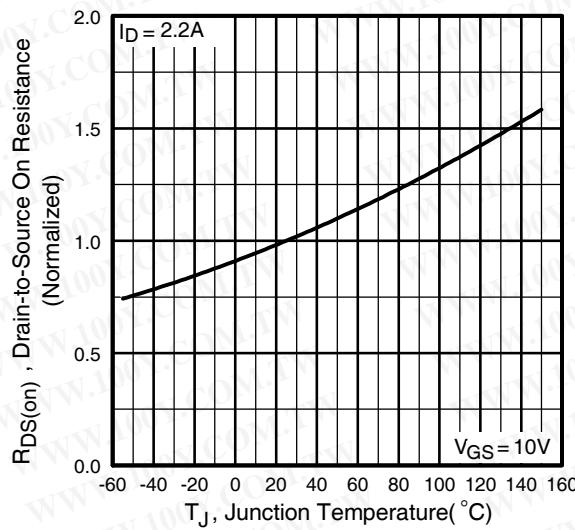


Fig 5. Normalized On-Resistance Vs. Temperature

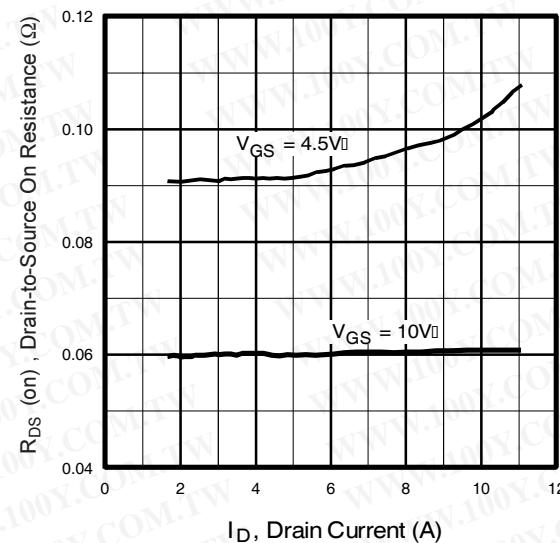


Fig 6. Typical On-Resistance Vs. Drain Current

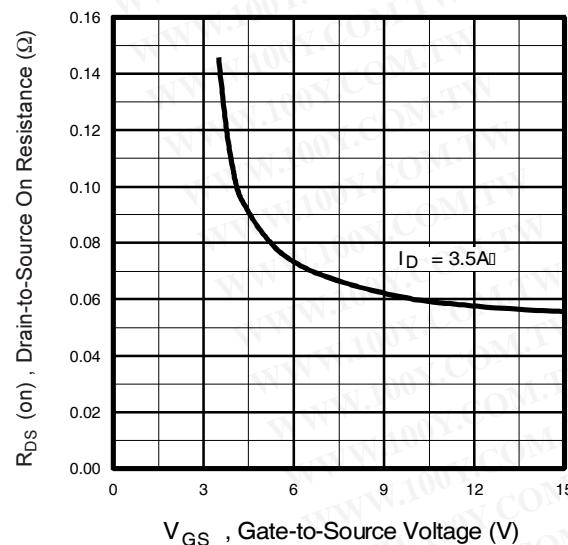


Fig 7. Typical On-Resistance Vs. Gate Voltage

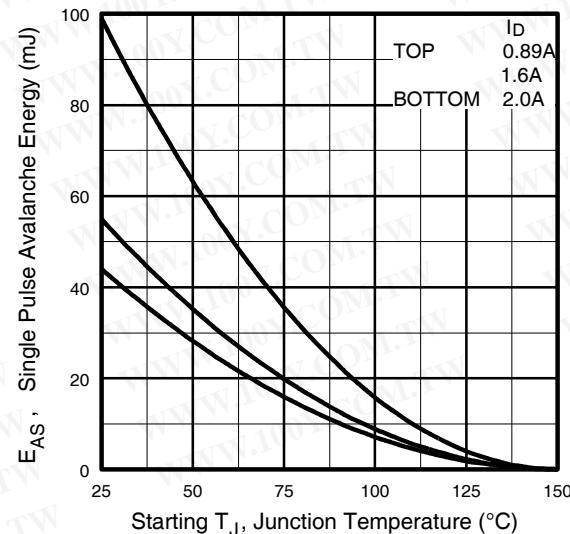


Fig 8. Maximum Avalanche Energy Vs. Drain Current

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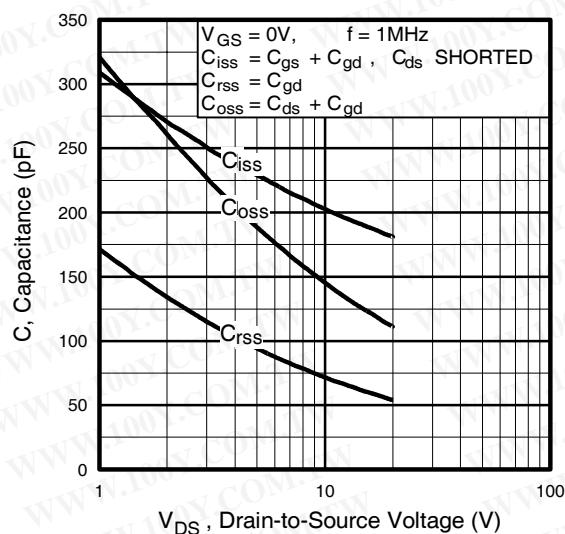


Fig 9. Typical Capacitance Vs.
Drain-to-Source Voltage

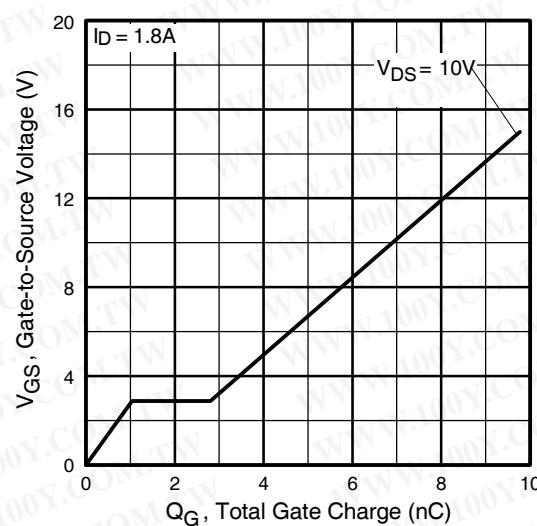


Fig 10. Typical Gate Charge Vs.
Gate-to-Source Voltage

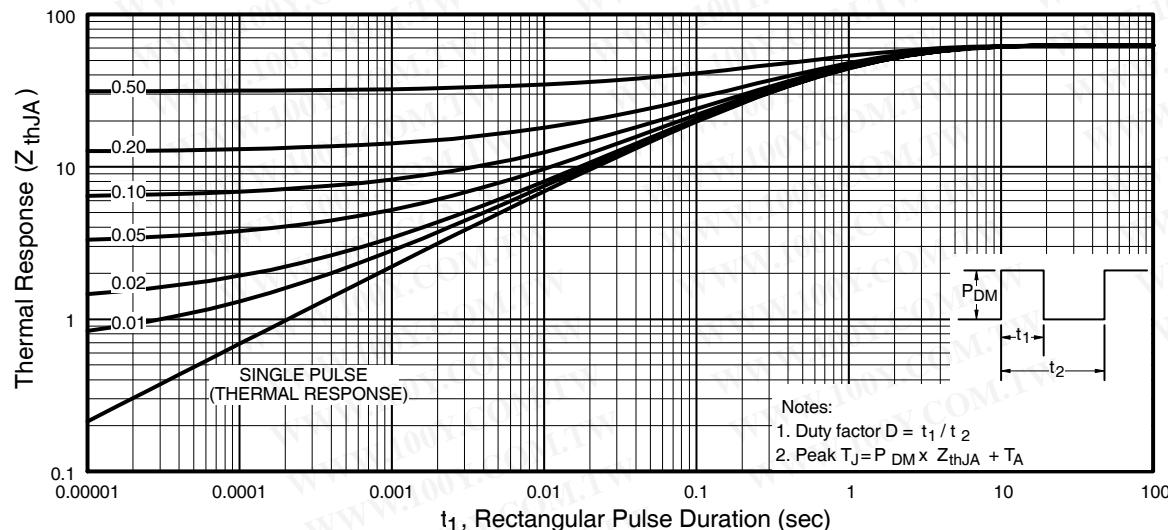


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

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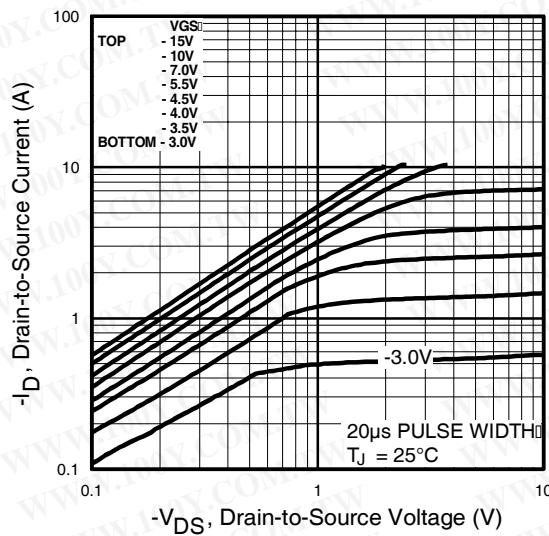


Fig 12. Typical Output Characteristics

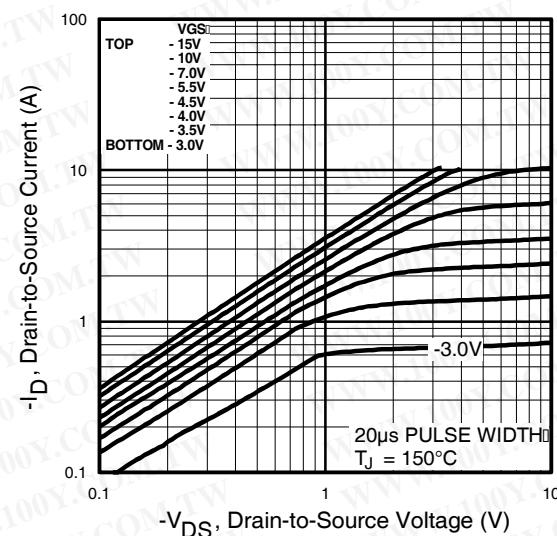


Fig 13. Typical Output Characteristics

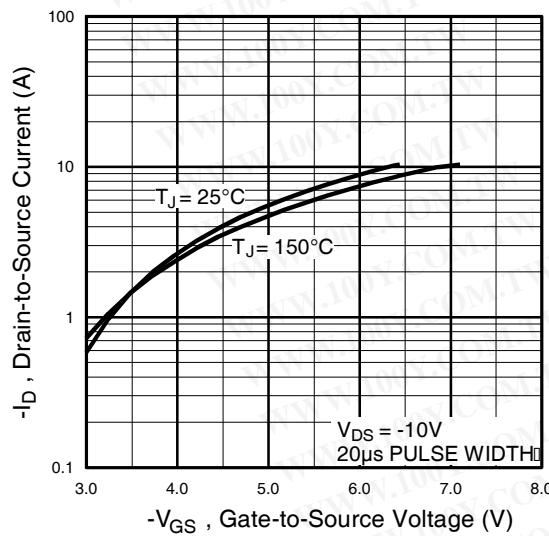


Fig 14. Typical Transfer Characteristics

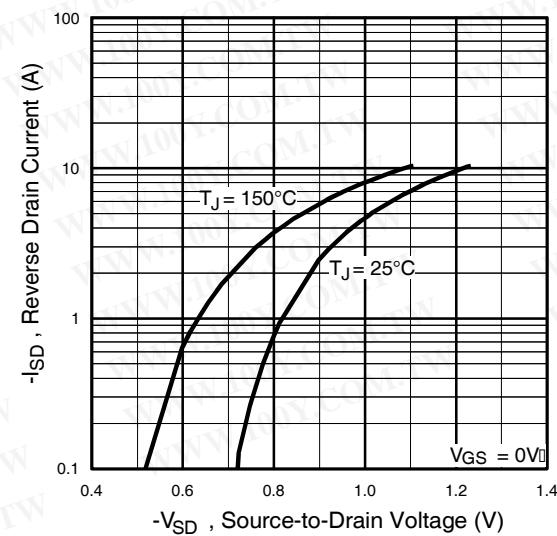


Fig 15. Typical Source-Drain Diode Forward Voltage

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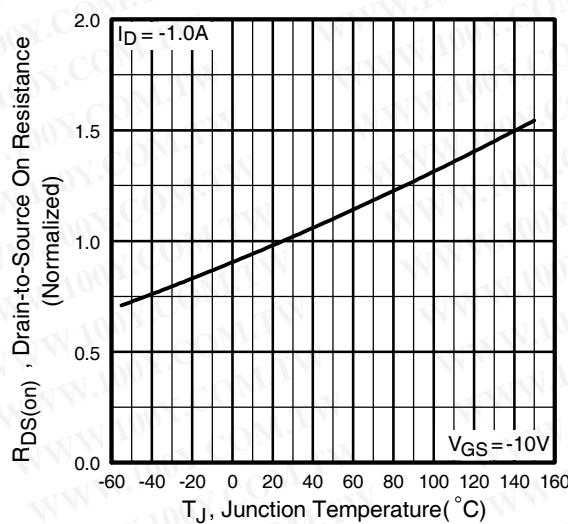


Fig 16. Normalized On-Resistance Vs. Temperature

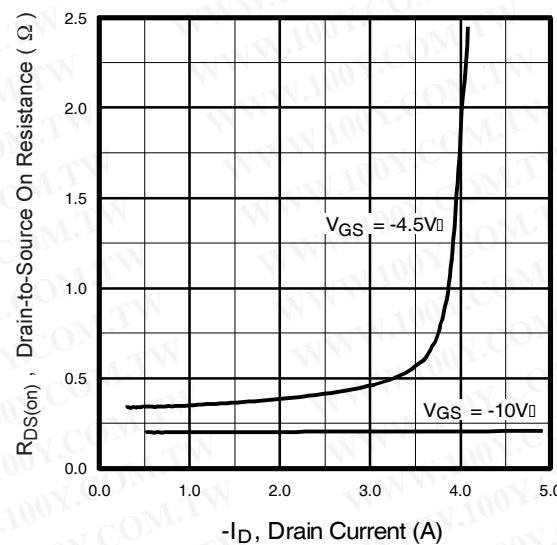


Fig 17. Typical On-Resistance Vs. Drain Current

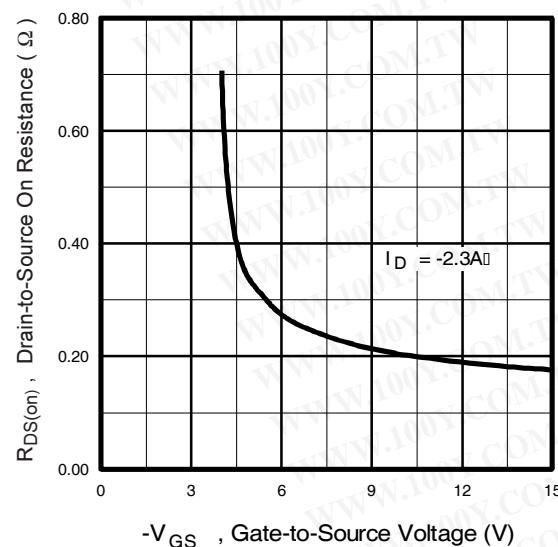


Fig 18. Typical On-Resistance Vs. Gate Voltage

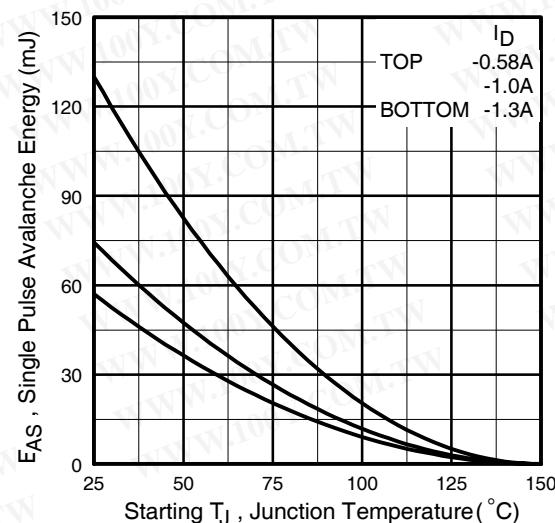


Fig 19. Maximum Avalanche Energy Vs. Drain Current

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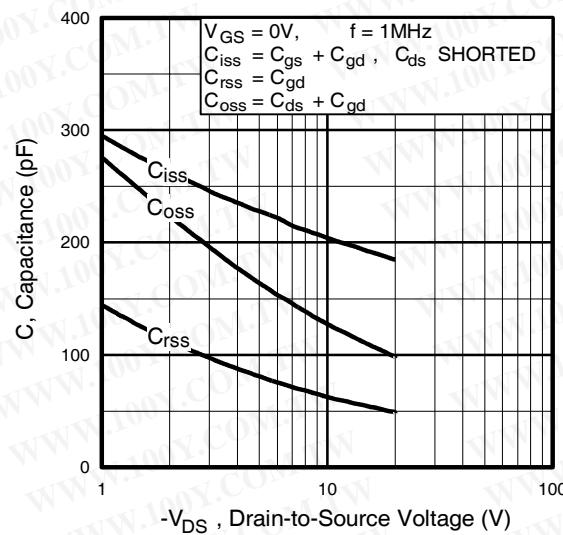


Fig 20. Typical Capacitance
 Vs.
 Drain-to-Source Voltage

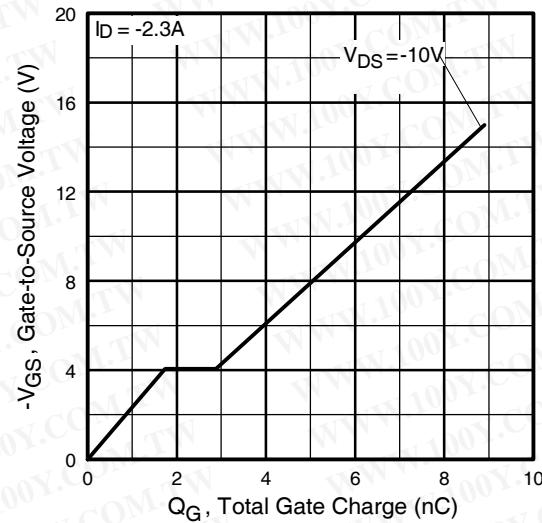


Fig 21. Typical Gate Charge Vs.
 Gate-to-Source Voltage

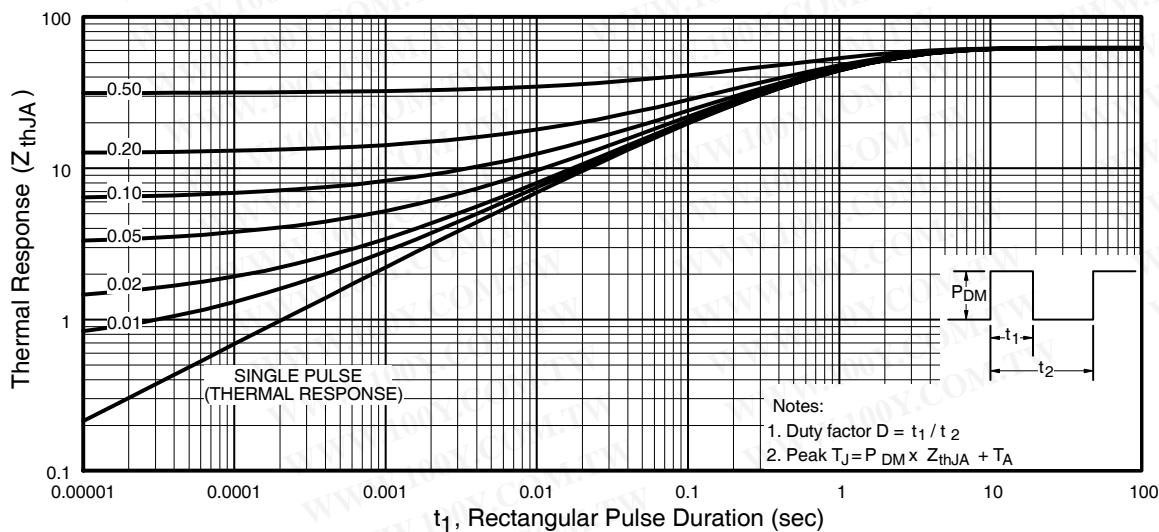
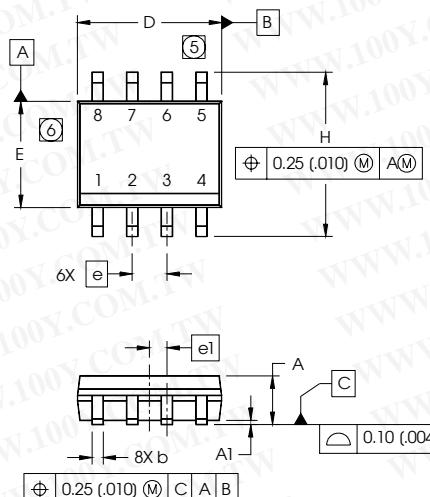


Fig 22. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

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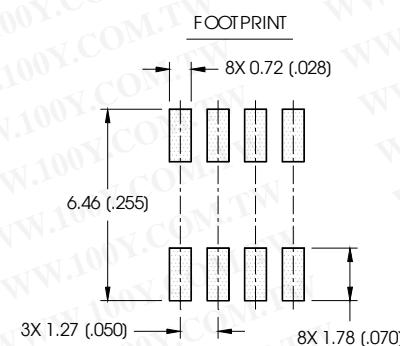
SO-8 Package Details



NOTES:

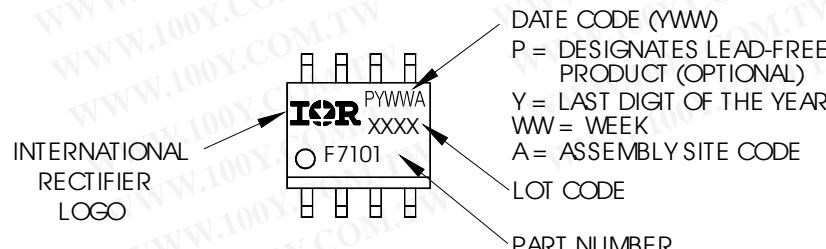
1. DIMENSIONING & TOLERANCING PER ASME Y14.5M-1994.
2. CONTROLLING DIMENSION: MILLIMETER
3. DIMENSIONS ARE SHOWN IN MILLIMETERS (INCHES).
4. OUTLINE CONFORMS TO JEDEC OUTLINE MS-012AA
5. DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS.
MOLD PROTRUSIONS NOT TO EXCEED 0.15 (.006).
6. DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS.
MOLD PROTRUSIONS NOT TO EXCEED 0.25 (.010).
7. DIMENSION IS THE LENGTH OF LEAD FOR SOLDERING TO
ASUBSTRATE.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.0532	.0688	1.35	1.75
A1	.0040	.0098	0.10	0.25
b	.013	.020	0.33	0.51
c	.0075	.0098	0.19	0.25
D	.189	.1968	4.80	5.00
E	.1497	.1574	3.80	4.00
e	.050	BASIC	1.27	BASIC
e1	.025	BASIC	0.635	BASIC
H	.2284	.2440	5.80	6.20
K	.0099	.0196	0.25	0.50
L	.016	.060	0.40	1.27
y	0°	8°	0°	8°



SO-8 Part Marking

EXAMPLE: THIS IS AN IRF7101 (MOSFET)



IRF9952

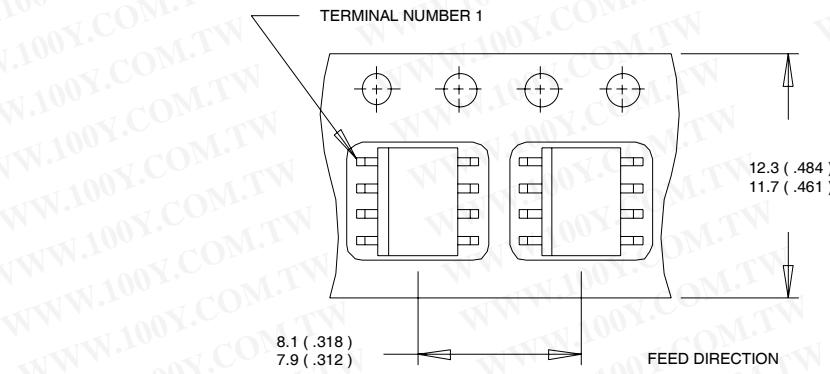
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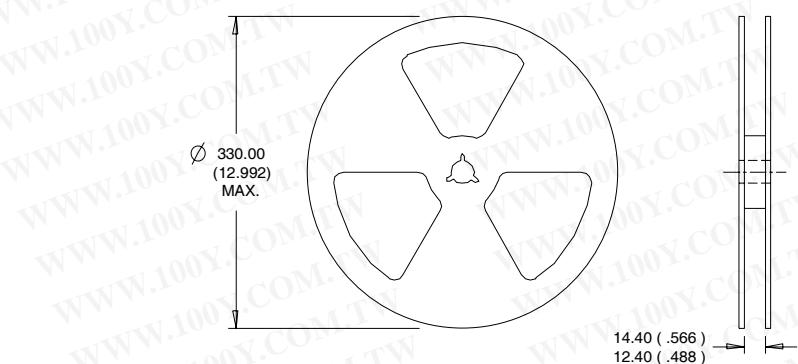
SO-8 Tape & Reel Information

Dimensions are shown in millimeters (inches)



NOTES:

1. CONTROLLING DIMENSION : MILLIMETER.
2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS(INCHES).
3. OUTLINE CONFORMS TO EIA-481 & EIA-541.



NOTES :

1. CONTROLLING DIMENSION : MILLIMETER.
2. OUTLINE CONFORMS TO EIA-481 & EIA-541.

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