August 2000

National Semiconductor

# LM119/LM219/LM319 High Speed Dual Comparator

# **General Description**

The LM119 series are precision high speed dual comparators fabricated on a single monolithic chip. They are designed to operate over a wide range of supply voltages down to a single 5V logic supply and ground. Further, they have higher gain and lower input currents than devices like the LM710. The uncommitted collector of the output stage makes the LM119 compatible with RTL, DTL and TTL as well as capable of driving lamps and relays at currents up to 25 mA.

The LM319A offers improved precision over the standard LM319, with tighter tolerances on offset voltage, offset current, and voltage gain.

Although designed primarily for applications requiring operation from digital logic supplies, the LM119 series are fully specified for power supplies up to  $\pm 15V$ . It features faster response than the LM111 at the expense of higher power dis勝 特 力 材 料 886-3-5753170 胜特力电子(上海) 86-21-54151736 胜特力电子(深圳) 86-755-83298787 Http://www.100y.com.tw

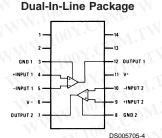
sipation. However, the high speed, wide operating voltage range and low package count make the LM119 much more versatile than older devices like the LM711.

The LM119 is specified from  $-55^{\circ}$ C to  $+125^{\circ}$ C, the LM219 is specified from  $-25^{\circ}$ C to  $+85^{\circ}$ C, and the LM319A and LM319 are specified from 0°C to  $+70^{\circ}$ C.

### Features

- Two independent comparators
- Operates from a single 5V supply
- Typically 80 ns response time at ±15V
- Minimum fan-out of 2 each side
- Maximum input current of 1 µA over temperature
- Inputs and outputs can be isolated from system ground
- High common mode slew rate

# **Connection Diagram**

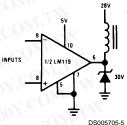


Top View Order Number LM119J, LM119J/883 (Note 1), LM219J, LM319J, LM319AM, LM319M, LM319AN or LM319N See NS Package Number J14A, M14A or N14A

Note 1: Also available per SMD# 8601401 or JM38510/10306

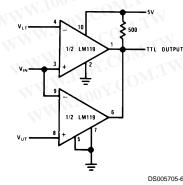
# Typical Applications (Note 2)

### Relay Driver



Note 2: Pin numbers are for metal can package.

Window Detector



### Absolute Maximum Ratings (Note 9)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Total Supply Voltage	36V
Output to Negative Supply Voltage	36V
Ground to Negative Supply Voltage	25V
Ground to Positive Supply Voltage	18V
Differential Input Voltage	±5V
Input Voltage (Note 3)	±15V
ESD rating (1.5 k $\Omega$ in series with	
100 pF)	800V
Power Dissipation (Note 4)	500 mW
Output Short Circuit Duration	10 sec
Storage Temperature Range	–65°C to 150°C

Lead Temperature (Soldering, 10 sec.)	260°C
Soldering Information Dual-In-Line Package Soldering (10 seconds)	260°C
Small Outline Package Vapor Phase (60 seconds)	215°C
Infrared (15 seconds)	220°C
One ANI 450 #Ountere Merustine Methods and T	1 <b></b>

See AN-450 "Surface Mounting Methods and Their Effect on Product Reliability" for other methods of soldering surface mount devices.

### **Operating Temperature Range**

100X.COM

LM119	-55°C to 125°C
LM219	–25°C to 85°C

# Electrical Characteristics (Note 5)

Parameter	Conditions	1	LM119/LM219			
	WWW TUDOY.COM.TW	Min	Тур	Max	IN	
Input Offset Voltage (Note 6)	$T_A = 25^{\circ}C, R_S \le 5k$	1	0.7	4.0	mV	
Input Offset Current (Note 6)	$T_A = 25^{\circ}C$	N	30	75	nA	
Input Bias Current	$T_A = 25^{\circ}C$	1	150	500	nA	
Voltage Gain	$T_A = 25^{\circ}C$ (Note 8)	10	40	100 1.	V/mV	
Response Time (Note 7)	$T_{A} = 25^{\circ}C, V_{S} = \pm 15V$	WTN	80	11004.0	ns	
Saturation Voltage	$V_{IN} \le -5 \text{ mV}, I_{OUT} = 25 \text{ mA}$ $T_A = 25^{\circ}\text{C}$	M.TW	0.75	1.5		
Output Leakage Current	$V_{IN} \ge 5 \text{ mV}, V_{OUT} = 35V$ $T_A = 25^{\circ}C$	OM.TW	0.2	2	μΑ	
Input Offset Voltage (Note 6)	R <sub>s</sub> ≤ 5k	COM		7	mV	
Input Offset Current (Note 6)	MUN 1002	COM.1		100	nA	
Input Bias Current	TW WY 100	T.M.	N	1000	nA	
Input Voltage Range	$V_{S} = \pm 15V$ V <sup>+</sup> = 5V, V <sup>-</sup> = 0	-12	±13	+12	V V	
Saturation Voltage	$V^+ \ge 4.5V, V^- = 0$ $V_{IN} \le -6 \text{ mV}, I_{SINK} \le 3.2 \text{ mA}$ $T_A \ge 0^{\circ}\text{C}$	100Y.COM	0.23	0.4	1001 N.100X	
WWW.100	$T_A \le 0^{\circ}C$	100X.CO	0.23	0.4	V	
Output Leakage Current	$V_{\rm IN} \ge 5 \text{ mV}, V_{\rm OUT} = 35V,$ $V^- = V_{\rm GND} = 0V$	N 100 Y.C	M.1	10	μA	
Differential Input Voltage	V.COMP. WW	.Vo.	Vn	±5	V	
Positive Supply Current	$T_A = 25^{\circ}C, V^+ = 5V, V^- = 0$	N.100 -1	4.3		mA	
Positive Supply Current	$T_{A} = 25^{\circ}C, V_{S} = \pm 15V$	100x	8	11.5	mA	
Negative Supply Current	$T_{A} = 25^{\circ}C, V_{S} = \pm 15V$		3	4.5	mA	

Note 3: For supply voltages less than ±15V the absolute maximum input voltage is equal to the supply voltage.

Note 4: The maximum junction temperature of the LM119 is 150°C, while that of the LM219 is 110°C. For operating at elevated temperatures, devices in the H10 package must be derated based on a thermal resistance of 160°C/W, junction to ambient, or 19°C/W, junction to case. The thermal resistance of the J14 and N14 packages is 100°C/W, junction to ambient.

Note 5: These specifications apply for  $V_S = \pm 15V$ , and the Ground pin at ground, and  $-55^{\circ}C \le T_A \le +125^{\circ}C$ , unless otherwise stated. With the LM219, however, all temperature specifications are limited to -25°C ≤ T<sub>A</sub> ≤ +85°C. The offset voltage, offset current and bias current specifications apply for any supply voltage from a single 5V supply up to  $\pm 15V$  supplies. Do not operate the device with more than 16V from ground to V<sub>S</sub>.

Note 6: The offset voltages and offset currents given are the maximum values required to drive the output within a volt of either supply with a 1 mA load. Thus, these parameters define an error band and take into account the worst case effects of voltage gain and input impedance.

Note 7: The response time specified (see definitions) is for a 100 mV input step with 5 mV overdrive.

Note 8: Output is pulled up to 15V through a 1.4  $k\Omega$  resistor.

Note 9: Refer to RETS119X for LM119H/883 and LM119J/883 specifications.

www.national.com

2

勝特力材料 886-3-5753170 胜特力电子(上海) 86-21-54151736 胜特力电子(深圳) 86-755-83298787 Http://www. 100y. com. tw

## Absolute Maximum Ratings LM319A/319 (Note 9)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Total Supply Voltage	36V
Output to Negative Supply Voltage	36V
Ground to Negative Supply Voltage	25V
Ground to Positive Supply Voltage	18V
Differential Input Voltage	±5V
Input Voltage (Note 10)	±15V
Power Dissipation (Note 11)	500 mW
Output Short Circuit Duration	10 sec
ESD rating (1.5 k $\Omega$ in series with 100 pF)	800V

Storage Temperature Range	–65°C to 150°C
Lead Temperature (Soldering, 10 sec.)	260°C
Soldering Information	
Dual-In-Line Package	
Soldering (10 sec.)	260°C
Small Outline Package	
Vapor Phase (60 sec.)	215°C
Infrared (15 sec.)	220°C
See AN-450 "Surface Mounting Metho	ds and Their Effect

See AN-450 "Surface Mounting Methods and Their Effect on Product Reliability" for other methods of soldering surface mount devices.

**Operating Temperature Range** 

### LM319A, LM319

0°C to 70°C

### Electrical Characteristics (Note 12)

Parameter	Conditions	LM319A			LM319			Units
		Min	Тур	Max	Min	Тур	Max	M.T
Input Offset Voltage (Note 13)	$T_A = 25^{\circ}C, R_S \le 5k$	N.C.	0.5	1.0	N	2.0	8.0	mV
Input Offset Current (Note 13)	$T_A = 25^{\circ}C$		20	40		80	200	nA
Input Bias Current	$T_A = 25^{\circ}C$	00 2	150	500		250	1000	nA
Voltage Gain	$T_{A} = 25^{\circ}C$ (Note 15)	20	40	I.M.	8	40	N.100	V/mV
Response Time (Note 14)	$T_{A} = 25^{\circ}C, V_{S} = \pm 15V$	100	80	WT.		80	10	ns
Saturation Voltage	$V_{IN} \le -10$ mV, $I_{OUT} = 25$ mA $T_A = 25^{\circ}C$	N.100	0.75	1.5	- 1	0.75	1.5	v V
Output Leakage Current	$V_{IN} \ge 10 \text{ mV}, V_{OUT} = 35V,$ $V^{-} = {}^{V}GND = 0V, T_{A} = 25^{\circ}C$	N.N.10	0.2	10		0.2	10	μA
Input Offset Voltage (Note 13)	R <sub>s</sub> ≤ 5k	WW.		10	I		10	mV
Input Offset Current (Note 13)	1001. MITH		1002.	300			300	nA
Input Bias Current	WT N. CONTRACT	NN.	1100	1000	NT.N		1200	nA
Input Voltage Range	$V_{\rm S} = \pm 15 V$	WW	±13	V.CO.	77.5	±13	WV	V
	$V^{+} = 5V, V^{-} = 0$	1	W.100	3	1		3	V
Saturation Voltage	$V^+ \ge 4.5V, V^- = 0$		0.3	0.4	M	0.3	0.4	V
	$V_{IN} \leq -10 \text{ mV}, I_{SINK} \leq 3.2 \text{ mA}$	W		nov.C			V	
Differential Input Voltage	WW.ICONCON		MM.	±5	CO.	W	±5	V
Positive Supply Current	$T_A = 25^{\circ}C, V^+ = 5V, V^- = 0$		4.3	Too	CON	4.3		mA
Positive Supply Current	$T_{A} = 25^{\circ}C, V_{S} = \pm 15V$		8	12.5		8	12.5	mA
Negative Supply Current	$T_{A} = 25^{\circ}C, V_{S} = \pm 15V$		3	5	1.00	3	5	mA

Note 10: For supply voltages less than ±15 the absolute maximum input voltage is equal to the supply voltage.

Note 11: The maximum junction temperature of the LM319A and LM319 is 85°C. For operating at elevated temperatures, devices in the H10 package must be derated based on a thermal resistance of 160°C/W, junction to ambient, or 19°C/W, junction to case. The thermal resistance of the N14 and J14 package is 100°C/W, junction to ambient. The thermal resistance of the M14 package is 115°C/W, junction to ambient.

Note 12: These specifications apply for  $V_S = \pm 15V$ , and 0°C  $\leq T_A \leq 70$ °C, unless otherwise stated. The offset voltage, offset current and bias current specifications apply for any supply voltage from a single 5V supply up to  $\pm 15V$  supplies. Do not operate the device with more than 16V from ground to  $V_S$ .

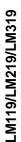
Note 13: The offset voltages and offset currents given are the maximum values required to drive the output within a volt of either supply with a 1 mA load. Thus, these parameters define an error band and take into account the worst case effects of voltage gain and input impedance.

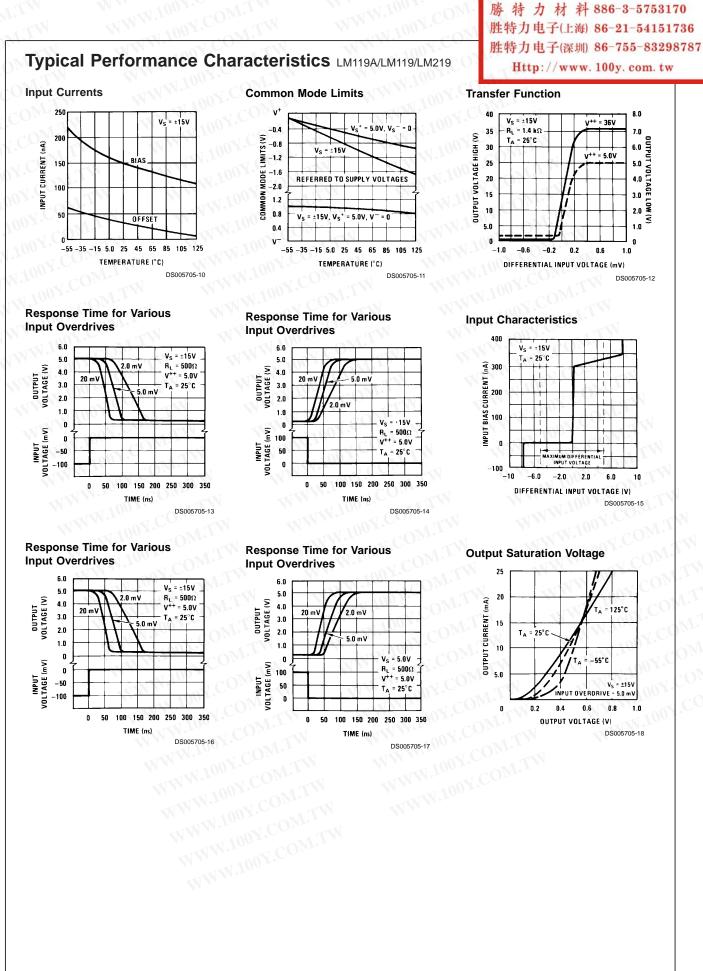
Note 14: The response time specified is for a 100 mV input step with 5 mV overdrive.

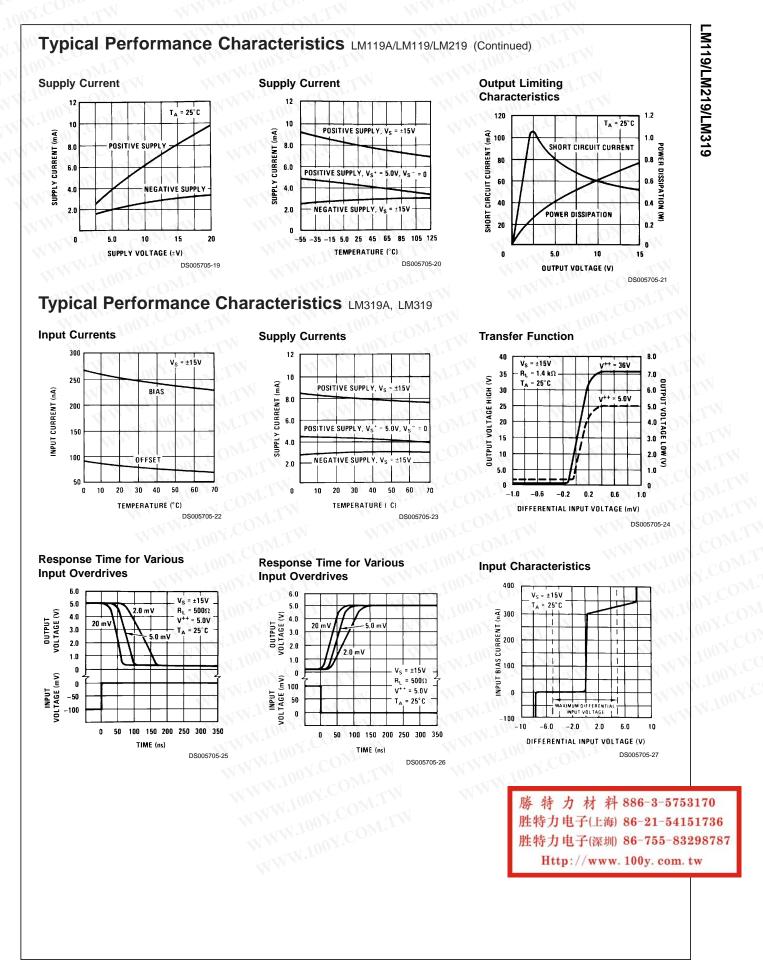
**Note 15:** Output is pulled up to 15V through a 1.4 k $\Omega$  resistor.

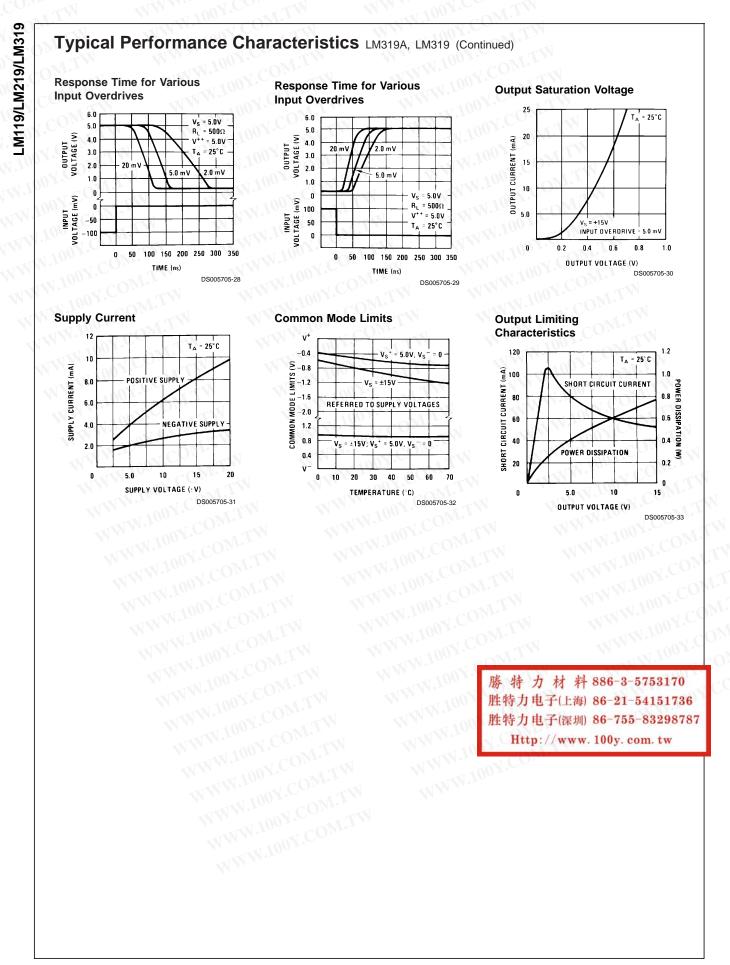
勝特力材料 886-3-5753170 胜特力电子(上海) 86-21-54151736 胜特力电子(深圳) 86-755-83298787 Http://www.100y.com.tw

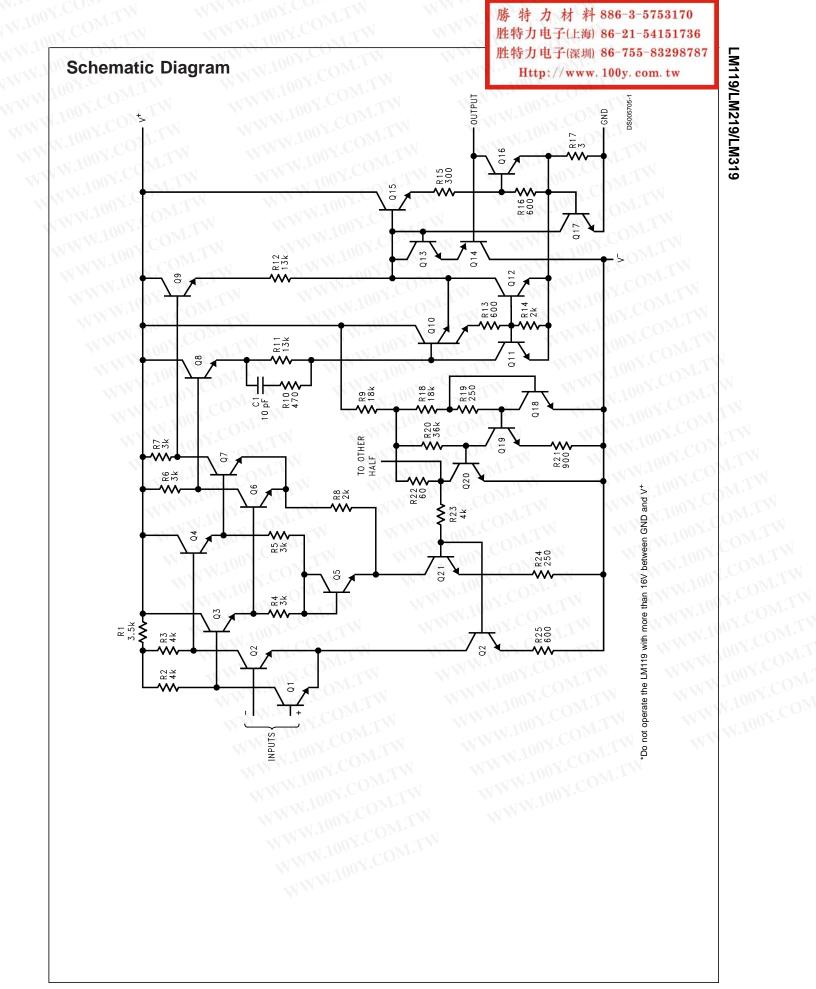
# LM119/LM219/LM319









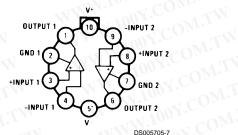


### **Connection Diagrams**



100Y.COM.TW

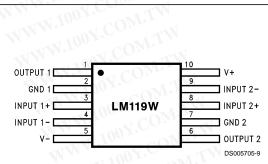
VT.MO



Case is connected to pin 5 (V<sup>-</sup>)

WWW.100Y.COM.TW

100X.COM.TW Top View Order Number LM119H, LM119H/883 (Note 1), or LM319H W.100Y.COM.TW WWW.100Y.COM.TW See NS Package Number H10C WW.100X.COM



100Y.COM.T

Order Number LM119W/883 See NS Package Number W10A WWW.100Y.COM.TW

WWW.100Y.COM.TW

WT.IN

勝 特力材料 886-3-5753170 胜特力电子(上海) 86-21-54151736 胜特力电子(深圳) 86-755-83298787 Http://www.100y.com.tw

00X.COM.TW

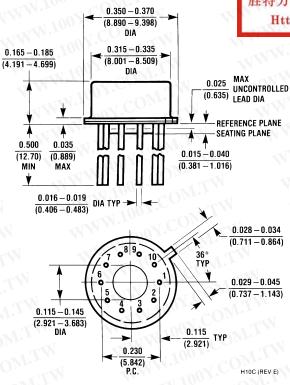
WWW.100Y.

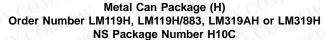
WWW.100Y.COM.TW

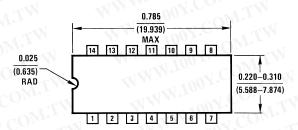
Physical Dimensions inches (millimeters) unless otherwise noted

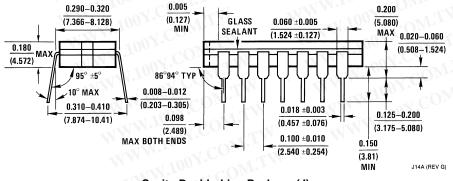
勝特力材料 886-3-5753170 胜特力电子(上海) 86-21-54151736 胜特力电子(深圳) 86-755-83298787 Http://www.100y.com.tw

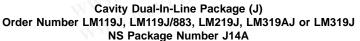
LM119/LM219/LM319

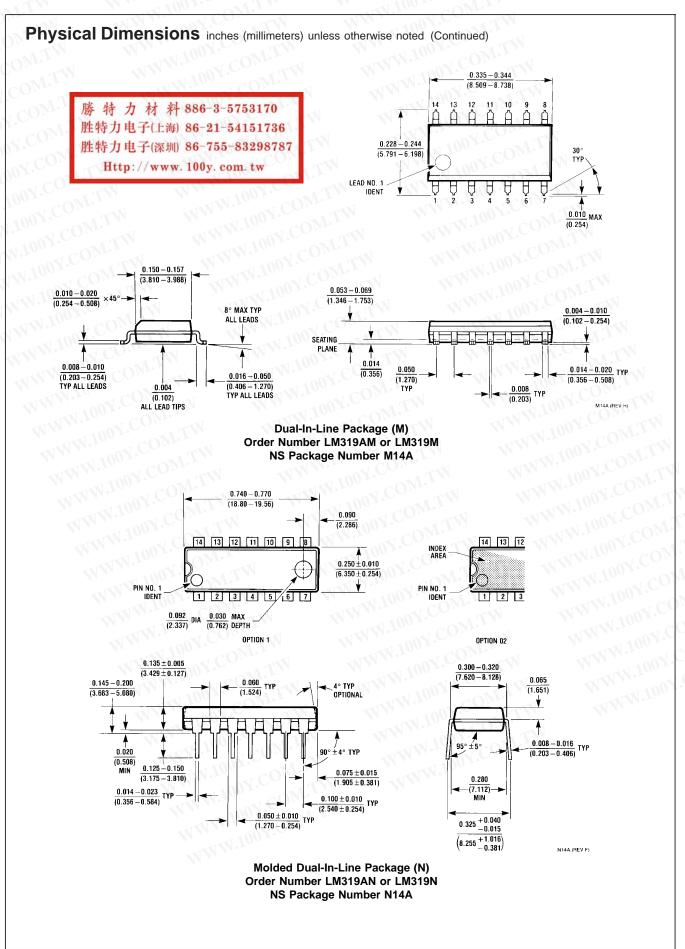


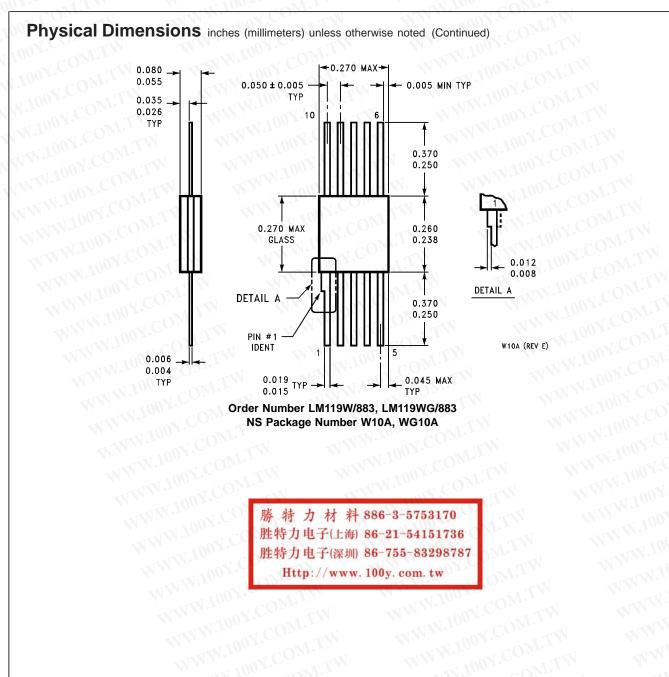












### LIFE SUPPORT POLICY

NATIONAL'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF THE PRESIDENT AND GENERAL COUNSEL OF NATIONAL SEMICONDUCTOR CORPORATION. As used herein:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
- 2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

National Semiconductor Corporation Americas Tel: 1-800-272-9959 Fax: 1-800-737-7018 Email: support@nsc.com www.national.com 
 National Semiconductor

 Europe
 Fax: +49 (0) 180-530 85 86

 Email:
 europe.support@nsc.com

 Deutsch Tel: +49 (0) 69 9508 6208
 English Tel: +44 (0) 870 24 0 2171

 Français Tel: +33 (0) 1 41 91 8790

National Semiconductor Asia Pacific Customer Response Group Tel: 65-2544466 Fax: 65-2504466 Email: ap.support@nsc.com National Semiconductor Japan Ltd. Tel: 81-3-5639-7560 Fax: 81-3-5639-7507 LM119/LM219/LM319 High

Speed

**Dual Comparator** 

National does not assume any responsibility for use of any circuitry described, no circuit patent licenses are implied and National reserves the right at any time without notice to change said circuitry and specifications.