

LM741 **Operational Amplifier General Description**

Connection Diagrams

The LM741 series are general purpose operational amplifiers which feature improved performance over industry standards like the LM709. They are direct, plug-in replacements for the 709C, LM201, MC1439 and 748 in most applications. The amplifiers offer many features which make their appli-

cation nearly foolproof: overload protection on the input and

ceeded, as well as freedom from oscillations.

the LM741C has their performance guaranteed over a 0°C to

Features

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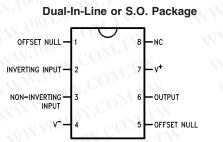
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Metal Can Package NC OFFSET NULL -OFFSET NULL INVERTING INPUT INVERTING INPUT OUTPUT NON-INVERTING INPUT NON-INVERTING INPUT OFFSET NULL 00934102 Note 1: LM741H is available per JM38510/10101 Order Number LM741H, LM741H/883 (Note 1) LM741AH/883 or LM741CH See NS Package Number H08C Ceramic Flatpak NC +OFFSET NULL -INPUT LM741W +INPUT OUTPUT -OFESET NULL v-00934106 Order Number LM741W/883 See NS Package Number W10A **Typical Application Offset Nulling Circuit** LM741 OUTPU 10 kΩ V-

output, no latch-up when the common mode range is ex-

The LM741C is identical to the LM741/LM741A except that +70°C temperature range, instead of -55°C to +125°C.



Order Number LM741J, LM741J/883, LM741CN See NS Package Number J08A, M08A or N08E

August 2000

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Absolute Maximum Ratings (Note 2)

If Military/Aerospace specified devices are required, VWW.100Y.COM.TW please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.y (Note 7)

ors for availability and specifications	100Y.COM.T			
	LM741A	LM741	LM741C	
Supply Voltage	±22V	±22V	±18V	
Power Dissipation (Note 3)	500 mW	500 mW	500 mW	
Differential Input Voltage	±30V	±30V	±30V	
Input Voltage (Note 4)	±15V	±15V	±15V	
Output Short Circuit Duration	Continuous	Continuous	Continuous	
Operating Temperature Range	–55°C to +125°C	-55°C to +125°C	0°C to +70°C	
Storage Temperature Range	-65°C to +150°C	-65°C to +150°C	-65°C to +150°C	
Junction Temperature	150°C	150°C	100°C	
Soldering Information				
N-Package (10 seconds)	260°C	260°C	260°C	
J- or H-Package (10 seconds)	300°C	300°C	300°C	
M-Package				
Vapor Phase (60 seconds)	215°C	215°C	215°C	
Infrared (15 seconds)	215°C	215°C	215°C	
See AN-450 "Surface Mounting Meth	ods and Their Effect	on Product Reliability"	for other methods of	
soldering				
surface mount devices.				
ESD Tolerance (Note 8)	400V	400V	400V	

surface mount devices. ESD Tolerance (Note 8)

Electrical Characteristics (Note 5)

Parameter	Conditions	N	LM741A			LM741			LM741C		
		Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	V.W.
Input Offset Voltage	$T_A = 25^{\circ}C$	1			100	1.0	M			N.	N.
	$R_{S} \le 10 \text{ k}\Omega$	W		NN.	10	1.0	5.0	WT	2.0	6.0	mV
	$R_{S} \le 50\Omega$		0.8	3.0	1.10		CO _N		1		mV
	$T_{AMIN} \le T_A \le T_{AMAX}$	VI.			W.1	00 -	cO ³	V. 7			W
	$R_S \le 50\Omega$	WT.		4.0	100	1007		T.M			mV
	$R_{S} \le 10 \text{ k}\Omega$	VT		N	N.M.	100	6.0		W	7.5	mV
Average Input Offset	WW.Ivo	0 11.	N	15	WW	1.10-	V.C	0	M		µV/°C
Voltage Drift	W 1001.0	T.Mo.				N.10	0 1.	No-		1	
Input Offset Voltage	$T_{A} = 25^{\circ}C, V_{S} = \pm 20V$	±10			111	±15	001.		±15		mV
Adjustment Range	WWW.LOON	COM	W		W	1 1		.CO	T	N	
Input Offset Current	$T_A = 25^{\circ}C$		3.0	30		20	200		20	200	nA
	$T_{AMIN} \le T_A \le T_{AMAX}$		1.1	70		85	500		M.	300	nA
Average Input Offset	WW 10	01.00	TI	0.5			10	01.0		TN	nA/°C
Current Drift	WWW.10	V.CC	TAT.	N/		WW	N	No	COM		
Input Bias Current	T _A = 25°C		30	80		80	500		80	500	nA
	$T_{AMIN} \le T_A \le T_{AMAX}$	1001.0	Mo	0.210			1.5			0.8	μA
Input Resistance	$T_{A} = 25^{\circ}C, V_{S} = \pm 20V$	1.0	6.0	WTN	0.3	2.0		0.3	2.0		MΩ
	$T_{AMIN} \le T_A \le T_{AMAX}$	0.5	CO	N 3-							MΩ
	$V_{S} = \pm 20V$	W.100									
Input Voltage Range	$T_A = 25^{\circ}C$							±12	±13		V
	$T_{AMIN} \le T_A \le T_{AMAX}$				±12	±13					V

Electrical Characteristics (Note 5) (Continued)						胜特力电子(上海) 86-21-54151736 胜特力电子(深圳) 86-755-83298787 Http://www.100y.com.tw					C.M
Parameter	Conditions	1.100	LM741	A	LM741		$\sqrt{1}$	M741	C-0)	Units	
	WW WILL	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	MT.W
arge Signal Voltage Gain	$T_A = 25^{\circ}C, R_L \ge 2 k\Omega$ $V_S = \pm 20V, V_O = \pm 15V$ $V_S = \pm 15V, V_O = \pm 10V$	50	07.C	CON.	50	200	A.	20	200	1.00 N.C	V/mV V/mV
	$\begin{split} T_{AMIN} &\leq T_A \leq T_{AMAX}, \\ R_L &\geq 2 \ k\Omega, \\ V_S &= \pm 20V, \ V_O &= \pm 15V \\ V_S &= \pm 15V, \ V_O &= \pm 10V \\ V_S &= \pm 5V, \ V_O &= \pm 2V \end{split}$	32 10	1001 1.100 1.100		25	LAN LAN		15	N.W.	101.5 1007 1007	V/mV V/mV V/mV
Output Voltage Swing	$V_{S} = \pm 20V$ $R_{L} \ge 10 \text{ k}\Omega$ $R_{L} \ge 2 \text{ k}\Omega$	±16 ±15	AN LA	100%	.CO2	A.T.	N N		NN NN	W.L	V V
	$V_{S} = \pm 15V$ $R_{L} \ge 10 \text{ k}\Omega$ $R_{L} \ge 2 \text{ k}\Omega$		NN NN	W.100	±12 ±10	±14 ±13	LM LM	±12 ±10	±14 ±13	NW NW	V V V
Dutput Short Circuit	T _A = 25°C	10	25	35	001.	25	N.T.		25		mA
urrent	$T_{AMIN} \le T_A \le T_{AMAX}$	10	N N	40	100		1.11			N.	mA
ommon-Mode ejection Ratio	$\begin{split} T_{AMIN} &\leq T_A \leq T_{AMAX} \\ R_S &\leq 10 \ \text{k}\Omega, \ V_{CM} = \pm 12V \\ R_S &\leq 50\Omega, \ V_{CM} = \pm 12V \end{split}$	80	95		70	90	OM	70	90	V	dB dB
upply Voltage Rejection atio	$\begin{split} & T_{AMIN} \leq T_{A} \leq T_{AMAX}, \\ & V_{S} = \pm 20 V \text{ to } V_{S} = \pm 5 V \\ & R_{S} \leq 50 \Omega \\ & R_{S} \leq 10 \; k \Omega \end{split}$	86	96	A M M	77	96	1.CO X.C	77	96		dB dB
ransient Response Rise Time Overshoot	$T_A = 25^{\circ}C$, Unity Gain	OM.T	0.25 6.0	0.8 20	N.N.	0.3 5	00X.	CO ₂	0.3 5	N	μs %
andwidth (Note 6)	T _A = 25°C	0.437	1.5		N		100		.1.1	N	MHz
ew Rate	$T_A = 25^{\circ}C$, Unity Gain	0.3	0.7		1	0.5		N.C	0.5	WT	V/µs
pply Current	$T_A = 25^{\circ}C$	- CO	Nr. 1	-		1.7	2.8	-	1.7	2.8	mA
wer Consumption	$T_A = 25^{\circ}C$ $V_S = \pm 20V$ $V_S = \pm 15V$	07.C	80	150		50	85	100X	50	85	mW mW
LM741A	$V_{\rm S} = \pm 20V$ $T_{\rm A} = T_{\rm AMIN}$	100%	CON	165				.100 N.10	N.C	DNI-	mW
LM741	$T_{A} = T_{AMAX}$ $V_{S} = \pm 15V$ $T_{A} = T_{AMIN}$	V.100	N.C	135	N	60	100	N.V.	700X	.CO2	mW
	$T_A = T_{AMAX}$	1	NY.		WT	45	75		100	1.00	mW

Note 2: "Absolute Maximum Ratings" indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits.

LM741

Electrical Characteristics (Note 5) (Continued)

Note 3: For operation at elevated temperatures, these devices must be derated based on thermal resistance, and T_j max. (listed under "Absolute Maximum Ratings"). $T_j = T_A + (\theta_{jA} P_D)$.

Thermal Resistance	Cerdip (J)	DIP (N)	HO8 (H)	SO-8 (M)		
θ_{jA} (Junction to Ambient)	100°C/W	100°C/W	170°C/W	195°C/W		
θ_{jC} (Junction to Case)	N/A	N/A	25°C/W	N/A		

Note 4: For supply voltages less than ±15V, the absolute maximum input voltage is equal to the supply voltage. Note 5: Unless otherwise specified, these specifications apply for $V_S = \pm 15V$, -55°C $\leq T_A \leq +125$ °C (LM741/LM741A). For the LM741C/LM741E, these

Note 5: Unless otherwise specified, these specifications apply for $V_S = \pm 15V$, $-55C \le T_A \le +125C$ (LM/41/LM/41A). For the LM/41C/LM/41E, these specifications are limited to $0^{\circ}C \le T_A \le +70^{\circ}C$.

Note 6: Calculated value from: BW (MHz) = 0.35/Rise Time(μ s).

Note 7: For military specifications see RETS741X for LM741 and RETS741AX for LM741A.

Note 8: Human body model, 1.5 k Ω in series with 100 pF.

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Schematic Diagram

