

# **3-Pin Microprocessor Reset Circuits**

### **General Description**

The MAX803/MAX809/MAX810 are microprocessor (µP) supervisory circuits used to monitor the power supplies in µP and digital systems. They provide excellent circuit reliability and low cost by eliminating external components and adjustments when used with +5V, +3.3V, +3.0V, or +2.5V-powered circuits.

These circuits perform a single function: they assert a reset signal whenever the VCC supply voltage declines below a preset threshold, keeping it asserted for at least 140ms after VCC has risen above the reset threshold. Reset thresholds suitable for operation with a variety of supply voltages are available.

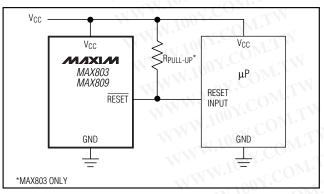
The MAX803 has an open-drain output stage, while the MAX809/MAX810 have push-pull outputs. The MAX803's open-drain RESET output requires a pull-up resistor that can be connected to a voltage higher than V<sub>CC</sub>. The MAX803/MAX809 have an active-low RESET output, while the MAX810 has an active-high RESET output. The reset comparator is designed to ignore fast transients on VCC, and the outputs are guaranteed to be in the correct logic state for VCC down to 1V.

Low supply current makes the MAX803/MAX809/ MAX810 ideal for use in portable equipment. The MAX803 is available in a 3-pin SC70 package, and the MAX809/MAX810 are available in 3-pin SC70 or SOT23 packages.

### **Applications**

Computers Controllers Intelligent Instruments Critical µP and µC Power Monitoring Portable/Battery-Powered Equipment Automotive

# Typical Operating Circuit



### Features

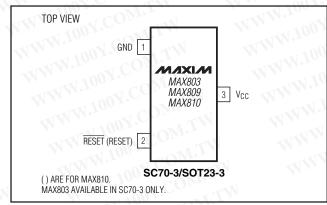
- ♦ Precision Monitoring of +2.5V, +3V, +3.3V, and +5V Power-Supply Voltages
- **♦ Fully Specified Over Temperature**
- ♦ Available in Three Output Configurations Open-Drain RESET Output (MAX803) Push-Pull RESET Output (MAX809) **Push-Pull RESET Output (MAX810)**
- 140ms min Power-On Reset Pulse Width
- ♦ 12µA Supply Current
- ♦ Guaranteed Reset Valid to V<sub>CC</sub> = +1V
- ♦ Power Supply Transient Immunity
- ♦ No External Components
- ♦ 3-Pin SC70 and SOT23 Packages

### **Ordering Information**

PART	TEMP. RANGE	PIN-PACKAGE
MAX803_EXR-T	-40°C to +105°C	3 SC70-3
MAX803_EXR-T10	-40°C to +105°C	3 SC70-3
MAX809_EXR-T	-40°C to +105°C	3 SC70-3
MAX809_EXR-T10	-40°C to +105°C	3 SC70-3
MAX809_EUR-T	-40°C to +105°C	3 SOT23-3
MAX809_EUR-T10	-40°C to +105°C	3 SOT23-3
MAX810_EXR-T	-40°C to +105°C	3 SC70-3
MAX810_EXR-T10	-40°C to +105°C	3 SC70-3
MAX810_EUR-T	-40°C to +105°C	3 SOT23-3
MAX810_EUR-T10	-40°C to +105°C	3 SOT23-3

Note: These parts are offered in 2.5k or 10k reels, and must be ordered in 2.5k or 10k increments. Order MAX803\_EXR-T for 2.5k reels and MAX803 EXR-T10 for 10k reels. Insert the desired suffix letter from the Selector Guide into the blank to complete the part number. All versions of these products may not be available at the time of announcement. Contact factory for availability.

# Pin Configuration



MIXIM

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# **3-Pin Microprocessor Reset Circuits**

### **ABSOLUTE MAXIMUM RATINGS**

Terminal Voltage (with respect to GND)	) ONLY
V <sub>CC</sub>	0.3V to +6.0V
RESET, RESET (push-pull)	0.3V to (V <sub>CC</sub> + 0.3V)
RESET (open drain)	0.3V to +6.0V
Input Current, VCC	20mA
Output Current, RESET, RESET	20mA
Rate of Rise, VCC	100V/µs

Continuous Power Dissipation (T <sub>A</sub> = +70°C)	
3-Pin SC70 (derate 2.17mW/°C above +70°C)	174mW
3-Pin SOT23 (derate 4mW/°C above +70°C)	320mW
Operating Temperature Range40°C to	5 +105°C
Storage Temperature Range65°C to	o +150°C
Lead Temperature (soldering, 10s)	+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### **ELECTRICAL CHARACTERISTICS**

( $V_{CC}$  = full range,  $T_A$  = -40°C to +105°C, unless otherwise noted. Typical values are at  $T_A$  = +25°C,  $V_{CC}$  = 5V for L/M/J versions,  $V_{CC}$  = 3.3V for T/S versions,  $V_{CC}$  = 3V for R version, and  $V_{CC}$  = 2.5V for Z version.) (Note 1)

PARAMETER	SYMBOL	M. O.Y.C.	CONDITIONS	MIN	TYP	MAX	UNITS
WWBCOM	1 -1	$T_A = 0^{\circ}\text{C to } +70^{\circ}\text{C}$ $T_A = -40^{\circ}\text{C to } +105^{\circ}\text{C}$		1.0	'A' CO	5.5	V
V <sub>CC</sub> Range	N AN			1.2	0.00	5.5	V
WW. JONE CONTRACTOR	W Y	$T_A = -40^{\circ}C$ to	V <sub>CC</sub> < 5.5V, MAX8J/L/M	-x11	24	60	N N
O I. O (OOTOO)	- VV	+85°C	V <sub>CC</sub> < 3.6V, MAX8R/S/T	11111	17	50	
Supply Current (SOT23)	Icc	$T_A = +85^{\circ}C$ to	V <sub>CC</sub> < 5.5V, MAX8L/M	WW.	10v	100	
	T	+105°C	V <sub>CC</sub> < 3.6V, MAX8R/S/T	- XIV	100	100	
M.M. TOON CO.	W	$T_A = -40^{\circ}C$ to	V <sub>C</sub> C < 5.5V, MAX8L/M	MAA	16	35	μΑ
Cumply Current (CC70)	Nr.	+85°C	V <sub>CC</sub> < 3.6V, MAX8R/S/T/Z	WW	12	30	TV
Supply Current (SC70)	Icc	$T_A = +85^{\circ}C$ to	V <sub>CC</sub> < 5.5V, MAX8L/M	-x1V	M.In.	75	OM.TV
	ONLTW	+105°C	V <sub>CC</sub> < 3.6V, MAX8R/S/T/Z	44	MW.10	75	
MALA	WT	MM	T <sub>A</sub> = +25°C	4.56	4.63	4.70	M
	COMP	MAX8L	$T_A = -40^{\circ}\text{C to } +85^{\circ}\text{C}$	4.50	MAIN	4.75	
	COMIT		$T_A = +85^{\circ}C \text{ to } +105^{\circ}C$	4.40	WW	4.86	
	TIME	MAX8M	T <sub>A</sub> = +25°C	4.31	4.38	4.45	
	Y.COM		$T_A = -40$ °C to $+85$ °C	4.25	MAN	4.50	
	COM		$T_A = +85^{\circ}C \text{ to } +105^{\circ}C$	4.16	WW	4.56	
	M. COM.	MAX809J (SOT only)	T <sub>A</sub> = +25°C	3.93	4.00	4.06	
	00 X.		$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	3.89	44	4.10	
	Y.CO.		$T_A = +85^{\circ}C \text{ to } +105^{\circ}C$	3.80		4.20	
	John CO	MAX8T	T <sub>A</sub> = +25°C	3.04	3.08	3.11	
Reset Threshold	V <sub>TH</sub>		$T_A = -40$ °C to $+85$ °C	3.00		3.15	
	100X.C		$T_A = +85^{\circ}C \text{ to } +105^{\circ}C$	2.92		3.23	
	W. CONT.C	TW	T <sub>A</sub> = +25°C	2.89	2.93	2.96	- x1 1(
	NN JOS	MAX8S	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	2.85	N	3.00	MAN:
	. 100 r.		$T_A = +85^{\circ}C \text{ to } +105^{\circ}C$	2.78	-1	3.08	
	1003	MAX8R	T <sub>A</sub> = +25°C	2.59	2.63	2.66	
	WWW.		$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	2.55	TW	2.70	
	WW.100	COM.	$T_A = +85^{\circ}C \text{ to } +105^{\circ}C$	2.50	W	2.76	WW
	W 10	MAX8Z (SC70 only)	T <sub>A</sub> = +25°C	2.28	2.32	2.35	WW
	WW		$T_A = -40$ °C to $+85$ °C	2.25	M.I.M	2.38	
	WWW.	(GG7 G Grilly)	$T_A = +85^{\circ}C \text{ to } +105^{\circ}C$	2.21	TT	2.45	1

# **3-Pin Microprocessor Reset Circuits**

# **ELECTRICAL CHARACTERISTICS (continued)**

(VCC = full range,  $T_A = -40^{\circ}$ C to +105°C, unless otherwise noted. Typical values are at  $T_A = +25^{\circ}$ C,  $V_{CC} = 5V$  for L/M/J versions,  $V_{CC} = 3.3V$  for T/S versions,  $V_{CC} = 3V$  for R version, and  $V_{CC} = 2.5V$  for Z version.) (Note 1)

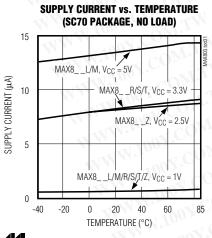
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS	
Reset Threshold Tempco	100	N. 100	Mo	30		ppm/°C	
V <sub>CC</sub> to Reset Delay (Note 2)	MM.	V <sub>CC</sub> = V <sub>TH</sub> to (V <sub>TH</sub> - 100mV)	N.Co.	20		μs	
Reset Active Timeout Period	TANN.	$T_A = -40$ °C to $+85$ °C	140	240	560 ms		
(SOT23)		$T_A = +85^{\circ}C \text{ to } +105^{\circ}C$	100	Mir	840	1115	
Reset Active Timeout Period	MM.	$T_A = -40^{\circ}\text{C to } +85^{\circ}\text{C}$	140	240	460	- ms	
(SC70)	WWW	$T_A = +85^{\circ}C \text{ to } +105^{\circ}C$	100	117	840		
RESET Output Voltage Low (push-pull active low and open- drain active-low, MAX803 and MAX809)	WW	V <sub>CC</sub> = V <sub>TH</sub> min, I <sub>SINK</sub> = 1.2mA, MAX803R/S/T/Z, MAX809R/S/T/Z	V.100Y.	COM	0.3	V	
	V <sub>OL</sub>	V <sub>CC</sub> = V <sub>TH</sub> min, I <sub>SINK</sub> = 3.2mA, MAX803L/M, MAX809J/L/M	W.1003	V.COI	0.4		
		V <sub>CC</sub> > 1.0V, I <sub>SINK</sub> = 50μA	MM. Inc	- <1 C.O	0.3		
RESET Output Voltage High (push-pull active low, MAX809)	Vон	V <sub>CC</sub> > V <sub>TH</sub> max, I <sub>SOURCE</sub> = 500μA, MAX809R/S/T/Z	0.8V <sub>CC</sub>	ON.C	$O_{M',I}$	W.	
		V <sub>CC</sub> > V <sub>TH</sub> max, I <sub>SOURCE</sub> = 800μA, MAX809J/L/M	V <sub>CC</sub> - 1.5		V		
RESET Output Voltage Low	V/OI	V <sub>CC</sub> = V <sub>TH</sub> max, I <sub>SINK</sub> = 1.2mA, MAX810R/S/T/Z	MMA	V.100 X	0.3	LIV	
(push-pull active high, MAX810)		V <sub>CC</sub> = V <sub>TH</sub> max, I <sub>SINK</sub> = 3.2mA, MAX810L/M	0.4		M.V		
RESET Output Voltage High (push-pull active high, MAX810)	Voh	1.8V < V <sub>CC</sub> < V <sub>TH</sub> min, I <sub>SOURCE</sub> = 150μA	0.8V <sub>CC</sub>	WW.1	007.	OV	
RESET Open-Drain Output Leakage Current (MAX803)	OMITY	V <sub>CC</sub> > V <sub>TH</sub> , RESET deasserted		NWW	1901	μΑ	

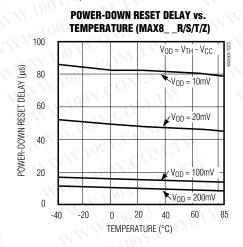
**Note 1:** Production testing done at  $T_A = +25^{\circ}C$ ; limits over temperature guaranteed by design only.

Note 2: RESET output for MAX803/MAX809; RESET output for MAX810.

# Typical Operating Characteristics

(V<sub>CC</sub> = full range,  $T_A = -40$ °C to +105°C, unless otherwise noted. Typical values are at  $T_A = +25$ °C,  $V_{CC} = +5$ V for L/M/J versions,  $V_{CC} = +3.3$ V for T/S versions,  $V_{CC} = +3.5$ V for R version, and  $V_{CC} = +2.5$ V for Z version.)

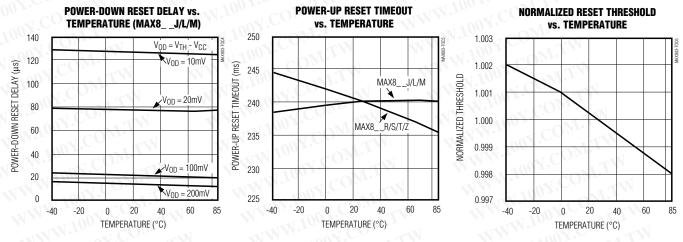




# **3-Pin Microprocessor Reset Circuits**

### **Typical Operating Characteristics (continued)**

 $(V_{CC} = \text{full range}, T_A = -40^{\circ}\text{C} \text{ to } +105^{\circ}\text{C}, \text{ unless otherwise noted}. \text{ Typical values are at } T_A = +25^{\circ}\text{C}, V_{CC} = +5\text{V for L/M/J versions}, V_{CC} = +3.3\text{V for T/S versions}, V_{CC} = +3.5\text{V for R version}, \text{ and } V_{CC} = +2.5\text{V for Z version}.)$ 



### **Selector Guide**

PART/SUFFIX	DECET TUDECHOLD (V)	OUTPUT TYPE	TOP MARK			
PAR I/SUFFIX	RESET THRESHOLD (V)	OUTPOTTIPE	SOT	SC70		
MAX803L	4.63	Open-Drain RESET	- IN	AAZ		
MAX803M	4.38	Open-Drain RESET		ABA		
MAX803T	3.08	Open-Drain RESET	TIN - W	ABB		
MAX803S	2.93	Open-Drain RESET	W - W	ABC		
MAX803R	2.63	Open-Drain RESET		ABD		
MAX803Z	2.32	Open-Drain RESET	William -	ABE		
MAX809L	4.63	Push-Pull RESET	AAAA	AAN		
MAX809M	4.38	Push-Pull RESET	ABAA	AAO		
MAX809J	4.00	Push-Pull RESET	CWAA	- 11/1 A 1100		
MAX809T	3.08	Push-Pull RESET	ACAA	AAP		
MAX809S	2.93	Push-Pull RESET	ADAA	AAQ		
MAX809R	2.63	Push-Pull RESET	AFAA	AAR		
MAX809Z	2.32	Push-Pull RESET	COM	AAS		
MAX810L	4.63	Push-Pull RESET	AGAA	AAT		
MAX810M	4.38	Push-Pull RESET	AHAA	AAU		
MAX810T	3.08	Push-Pull RESET	AJAA	AAV		
MAX810S	2.93	Push-Pull RESET	AKAA	AAX		
MAX810R	2.63	Push-Pull RESET	ALAA	AAW		
MAX810Z	2.32	Push-Pull RESET	A. Con	AAY		

# **3-Pin Microprocessor Reset Circuits**

### **Pin Description**

PIN	NAME	FUNCTION		
1	GND	Ground		
7.0 07.0 02 Y 100	RESET (MAX803/ MAX809)	RESET Output remains low while V <sub>CC</sub> is below the reset threshold, and for at least 140ms after V <sub>CC</sub> rises above the reset threshold.		
	RESET (MAX810)	RESET Output remains high while V <sub>CC</sub> is below the reset threshold, and for at least 140ms after V <sub>CC</sub> rises above the reset threshold.		
3	Vcc	Supply Voltage (+5V, +3.3V, +3.0V, or +2.5V)		
	ALL L.			

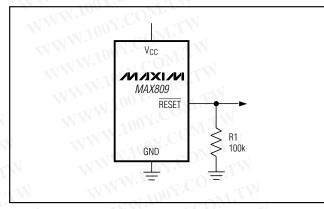


Figure 2. RESET Valid to VCC = Ground Circuit

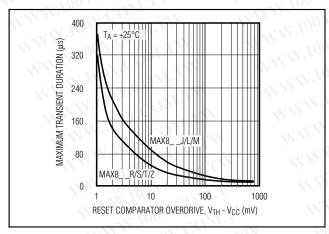


Figure 1. Maximum Transient Duration Without Causing a Reset Pulse vs. Reset Comparator Overdrive

# **Detailed Description**

A microprocessor's ( $\mu$ P's) reset input starts the  $\mu$ P in a known state. The MAX803/MAX809/MAX810 assert reset to prevent code-execution errors during power-up, power-down, or brownout conditions. They assert a reset signal whenever the V<sub>CC</sub> supply voltage declines below a preset threshold, keeping it asserted for at least 140ms after V<sub>CC</sub> has risen above the reset threshold. The MAX803 uses an open-drain output, and the MAX809/MAX810 have a push-pull output stage. Connect a pull-up resistor on the MAX803's RESET output to any supply between 0 and 6V.

### Applications Information

### **Negative-Going Vcc Transients**

In addition to issuing a reset to the  $\mu P$  during power-up, power-down, and brownout conditions, the MAX803/MAX809/MAX810 are relatively immune to short-duration negative-going VCC transients (glitches).

Figure 1 shows typical transient duration vs. reset comparator overdrive, for which the MAX803/MAX809/ MAX810 do **not** generate a reset pulse. The graph was generated using a negative-going pulse applied to VCC, starting 0.5V above the actual reset threshold and ending below it by the magnitude indicated (reset comparator overdrive). The graph indicates the maximum pulse width a negative-going VCC transient can have without causing a reset pulse. As the magnitude of the transient increases (goes farther below the reset threshold), the maximum allowable pulse width decreases. Typically, for the MAX8\_L and MAX8\_M, a VCC transient that goes 100mV below the reset threshold and lasts 20µs or less will not cause a reset pulse. A 0.1µF bypass capacitor mounted as close as possible to the VCC pin provides additional transient immunity.

# Ensuring a Valid Reset Output Down to Vcc = 0

When V<sub>CC</sub> falls below 1V, the MAX809 RESET output no longer sinks current—it becomes an open circuit. Therefore, high-impedance CMOS logic inputs connected to RESET can drift to undetermined voltages. This presents no problem in most applications since most  $\mu P$  and other circuitry is inoperative with V<sub>CC</sub> below 1V. However, in applications where RESET must be valid down to 0V, adding a pull-down resistor to RESET causes any stray leakage currents to flow to

# **3-Pin Microprocessor Reset Circuits**

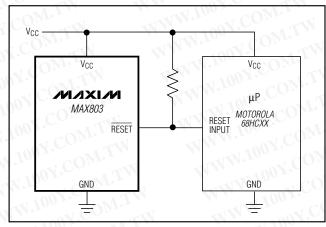


Figure 3. Interfacing to µPs with Bidirectional Reset I/O

ground, holding  $\overline{\text{RESET}}$  low (Figure 2). R1's value is not critical;  $100 k\Omega$  is large enough not to load  $\overline{\text{RESET}}$  and small enough to pull  $\overline{\text{RESET}}$  to ground.

A 100k $\Omega$  pull-up resistor to VCC is also recommended for the MAX810 if RESET is required to remain valid for VCC < 1V.

# Interfacing to µPs with Bidirectional Reset Pins

Since the  $\overline{\text{RESET}}$  output on the MAX803 is open drain, this device interfaces easily with  $\mu\text{Ps}$  that have bidirectional reset pins, such as the Motorola 68HC11. Connecting the  $\mu\text{P}$  supervisor's  $\overline{\text{RESET}}$  output directly to the microcontroller's ( $\mu\text{C}$ 's)  $\overline{\text{RESET}}$  pin with a single pull-up resistor allows either device to assert reset (Figure 3).

# MAX803 Open-Drain RESET Output Allows Use with Multiple Supplies

Generally, the pull-up connected to the MAX803 will connect to the supply voltage that is being monitored at the IC's V<sub>CC</sub> pin. However, some systems may use the open-drain output to level-shift from the monitored supply to reset circuitry powered by some other supply (Figure 4). Note that as the MAX803's V<sub>CC</sub> decreases below 1V, so does the IC's ability to sink current at RESET. Also, with any pull-up, RESET will be pulled high as V<sub>CC</sub> decays toward 0. The voltage where this occurs depends on the pull-up resistor value and the voltage to which it is connected.

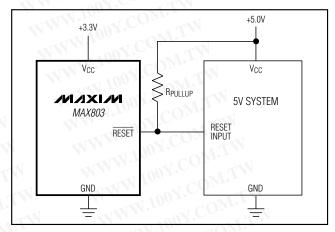


Figure 4. MAX803 Open-Drain RESET Output Allows Use with Multiple Supplies

### Benefits of Highly Accurate Reset Threshold

Most  $\mu P$  supervisor ICs have reset threshold voltages between 5% and 10% below the value of nominal supply voltages. This ensures a reset will **not** occur within 5% of the nominal supply, but **will** occur when the supply is 10% below nominal.

When using ICs rated at only the nominal supply  $\pm 5\%$ , this leaves a zone of uncertainty where the supply is between 5% and 10% low, and where the reset may or may not be asserted.

The MAX8\_L/T/Z use highly accurate circuitry to ensure that reset is asserted close to the 5% limit, and long before the supply has declined to 10% below nominal

\_Chip Information

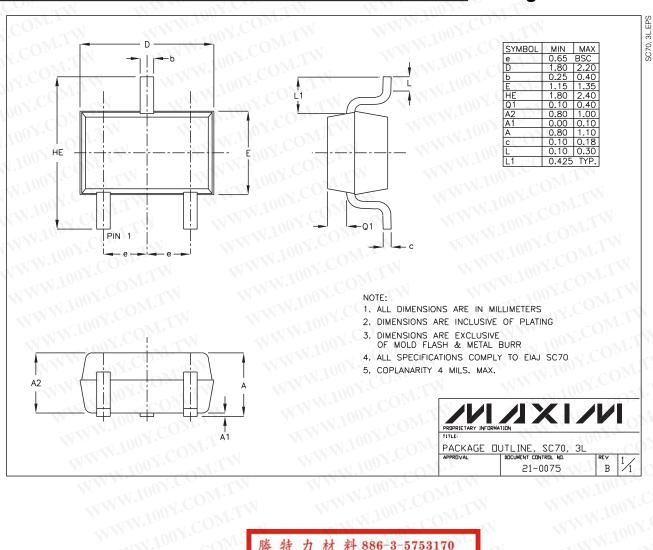
TRANSISTOR COUNT: 275 (SOT23) 380 (SC70)

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# /AX803L/M/R/S/T/Z, MAX809J/L

# Pin Microprocessor Reset Circuits

# **Package Information**



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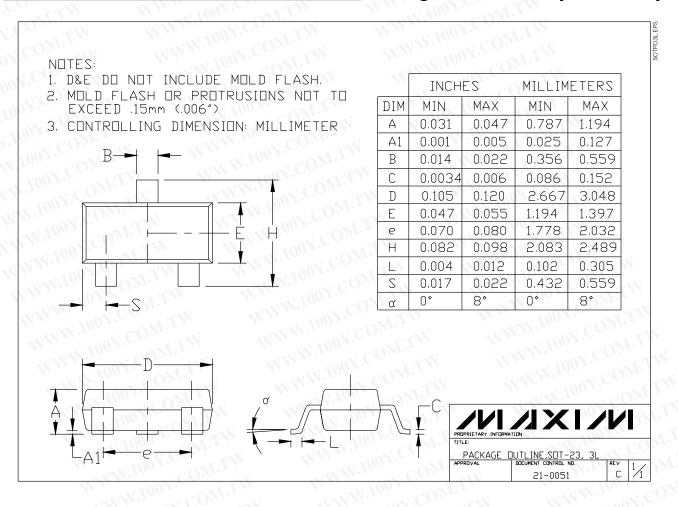
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# **3-Pin Microprocessor Reset Circuits**

# **Package Information (continued)**



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