



# 6-Pin DIP Random-Phase Optoisolators Triac Driver Output (400 Volts Peak)

The MOC3020 Series consists of gallium arsenide infrared emitting diodes, optically coupled to a silicon bilateral switch.

 To order devices that are tested and marked per VDE 0884 requirements, the suffix "V" must be included at end of part number. VDE 0884 is a test option.
 They are designed for applications requiring isolated triac triggering.

# Recommended for 115/240 Vac(rms) Applications:

- Solenoid/Valve Controls
- Lamp Ballasts
- Interfacing Microprocessors to 115 Vac Peripherals
- Motor Controls

- Static ac Power Switch
- Solid State Relays
- Incandescent Lamp Dimmers

# MOC3021 [IFT = 15 mA Max] MOC3022 [IFT = 10 mA Max] MOC3023 \* [IFT = 5 mA Max]



# **MAXIMUM RATINGS** ( $T_A = 25^{\circ}C$ unless otherwise noted)

Rating	Symbol	Value	Unit
NFRARED EMITTING DIODE	WW. 100X.Co	WELL	V
Reverse Voltage	V <sub>R</sub>	3	Volts
Forward Current — Continuous	J. Fox	60	mA
Total Power Dissipation @ T <sub>A</sub> = 25°C Negligible Power in Triac Driver Derate above 25°C	WW PD	100	mW mW/°C

# OUTPUT DRIVER

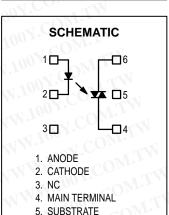
Off–State Output Terminal Voltage	VDRM	400	Volts
Peak Repetitive Surge Current (PW = 1 ms, 120 pps)	ITSM	ON TON	A
Total Power Dissipation @ T <sub>A</sub> = 25°C Derate above 25°C	PD	300 4	mW mW/°C

# **TOTAL DEVICE**

Isolation Surge Voltage <sup>(1)</sup> (Peak ac Voltage, 60 Hz, 1 Second Duration)	VISO	7500	Vac(pk)	
Total Power Dissipation @ T <sub>A</sub> = 25°C Derate above 25°C		330 4.4	mW mW/°C	
Junction Temperature Range	TJ	-40 to +100	√ °C	
Ambient Operating Temperature Range <sup>(2)</sup>		-40 to +85		
Storage Temperature Range(2)		-40 to +150	°C C	
Soldering Temperature (10 s)	TL	260	°C	

- 1. Isolation surge voltage, V<sub>ISO</sub>, is an internal device dielectric breakdown rating. For this test, Pins 1 and 2 are common, and Pins 4, 5 and 6 are common.
- 2. Refer to Quality and Reliability Section in Opto Data Book for information on test conditions.

**Preferred** devices are Motorola recommended choices for future use and best overall value. GlobalOptoisolator is a trademark of Motorola, Inc.



DO NOT CONNECT 6. MAIN TERMINAL

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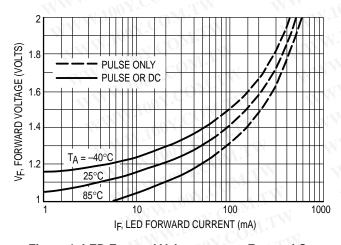
**ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
NPUT LED CONTROL OF THE PROPERTY OF THE PROPER	MW.	COP. TY		•	•
Reverse Leakage Current (V <sub>R</sub> = 3 V)	IN IR	$CO_{\overline{M},\underline{1}}$	0.05	100	μА
Forward Voltage (I <sub>F</sub> = 10 mA)	VF	N.COM	1.15	1.5	Volts
DUTPUT DETECTOR (I <sub>F</sub> = 0 unless otherwise noted)	MANTO	ON CON	TW	•	
Peak Blocking Current, Either Direction (Rated V <sub>DRM</sub> <sup>(1)</sup> )	IDRM	100 \rac{1}{2}CO	10	100	nA
Peak On–State Voltage, Either Direction (I <sub>TM</sub> = 100 mA Peak)	V <sub>TM</sub>	100±1.C	1.8	3	Volts
Critical Rate of Rise of Off–State Voltage (Figure 7, Note 2)	dv/dt	N.100	10		V/μs
COUPLED	11.4.	W.100 Y.	COMIT	V .	
LED Trigger Current, Current Required to Latch Output (Main Terminal Voltage = 3 V <sup>(3)</sup> )  MOC3021  MOC3022  MOC3023	IFT W	M <u>M</u> .100	√ (8 M) (1	15 10 5	mA
Holding Current, Either Direction	IH	TVEN.I	100	TN TN	μА

- 1. Test voltage must be applied within dv/dt rating.
- 2. This is static dv/dt. See Figure 7 for test circuit. Commutating dv/dt is a function of the load-driving thyristor(s) only.
- 3. All devices are guaranteed to trigger at an I<sub>F</sub> value less than or equal to max I<sub>F</sub>T. Therefore, recommended operating I<sub>F</sub> lies between max I<sub>F</sub>T (15 mA for MOC3021, 10 mA for MOC3022, 5 mA for MOC3023) and absolute max I<sub>F</sub> (60 mA).

# TYPICAL ELECTRICAL CHARACTERISTICS





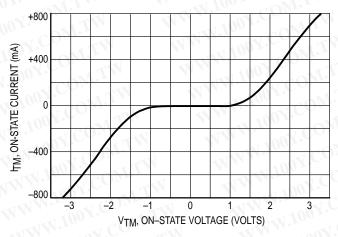


Figure 1. LED Forward Voltage versus Forward Current

Figure 2. On-State Characteristics

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# 

0.6 L -40

-20

0

T<sub>A</sub>, AMBIENT TEMPERATURE (°C)

Figure 3. Trigger Current versus Temperature

40

60

80

100

20

# 25 NORMALIZED TO: PW<sub>in</sub> ≥ 100 μs

MOC3021 MOC3022 MOC3023

Figure 4. LED Current Required to Trigger versus LED Pulse Width

PWin, LED TRIGGER WIDTH (μs)

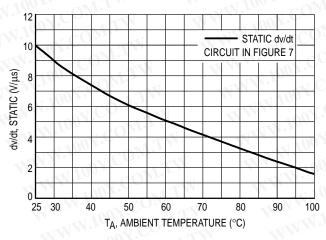


Figure 5. dv/dt versus Temperature

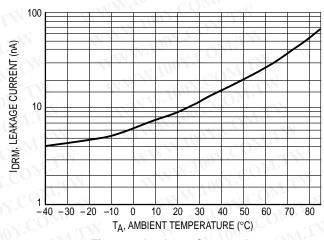


Figure 6. Leakage Current, IDRM versus Temperature

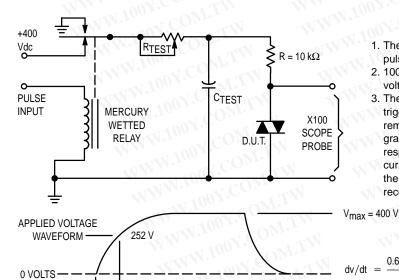
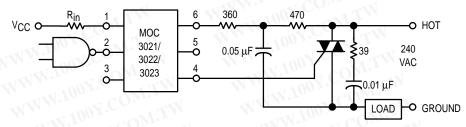


Figure 7. Static dv/dt Test Circuit

- The mercury wetted relay provides a high speed repeated pulse to the D.U.T.
- 100x scope probes are used, to allow high speeds and voltages.
- 3. The worst–case condition for static dv/dt is established by triggering the D.U.T. with a normal LED input current, then removing the current. The variable R<sub>TEST</sub> allows the dv/dt to be gradually increased until the D.U.T. continues to trigger in response to the applied voltage pulse, even after the LED current has been removed. The dv/dt is then decreased until the D.U.T. stops triggering. τ<sub>RC</sub> is measured at this point and recorded.

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τRC



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Additional information on the use of optically coupled triac drivers is available in Application Note AN-780A.

In this circuit the "hot" side of the line is switched and the load connected to the cold or ground side.

The 39 ohm resistor and 0.01 µF capacitor are for snubbing of the triac, and the 470 ohm resistor and  $0.05\,\mu F$  capacitor are for snubbing the coupler. These components may or may not be necessary depending upon the particular triac and load used.

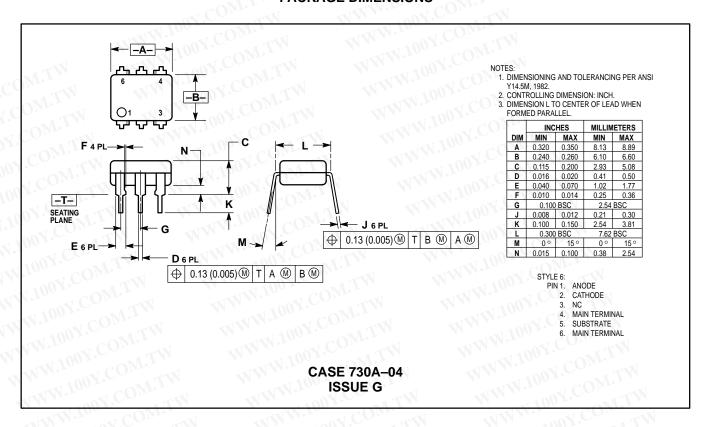
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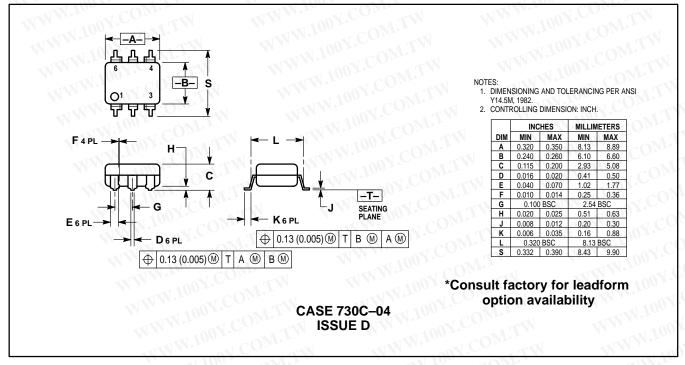
**Figure 8. Typical Application Circuit** 

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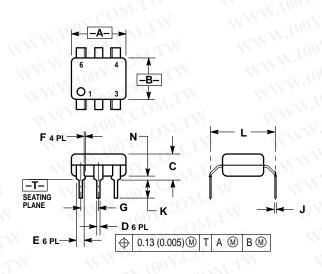
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## PACKAGE DIMENSIONS





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NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982
- CONTROLLING DIMENSION: INCH.
   DIMENSION L TO CENTER OF LEAD WHEN FORMED PARALLEL.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.320	0.350	8.13	8.89	
В	0.240	0.260	6.10	6.60	
С	0.115	0.200	2.93	5.08	
D	0.016	0.020	0.41	0.50	
E	0.040	0.070	1.02	1.77	
.F⊆	0.010	0.014	0.25	0.36	
G	0.100 BSC		2.54	BSC	
J	0.008	0.012	0.21	0.30	
K	0.100	0.150	2.54	3.81	
L	0.400	0.425	10.16	10.80	
N	0.015	0.040	0.38	1.02	

\*Consult factory for leadform option availability

**CASE 730D-05 ISSUE D** 

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### How to reach us:

USA/EUROPE: Motorola Literature Distribution: P.O. Box 20912: Phoenix. Arizona 85036, 1-800-441-2447

MFAX: RMFAX0@email.sps.mot.com - TOUCHTONE (602) 244-6609 INTERNET: http://Design-NET.com

JAPAN: Nippon Motorola Ltd.; Tatsumi-SPD-JLDC, Toshikatsu Otsuki, 6F Seibu-Butsuryu-Center, 3-14-2 Tatsumi Koto-Ku, Tokyo 135, Japan. 03-3521-8315

HONG KONG: Motorola Semiconductors H.K. Ltd.; 8B Tai Ping Industrial Park, 51 Ting Kok Road, Tai Po, N.T., Hong Kong. 852-26629298



