

SILICON POWER TRANSISTOR

2SD1588

NPN SILICON EPITAXIAL TRANSISTOR

FOR LOW-FREQUENCY POWER AMPLIFIERS AND LOW-SPEED SWITCHING

FEATURES

- Mold package that does not require an insulating board or insulation bushing
- Large current capacity in small dimension: $I_{C(DC)} = 7\text{ A}$
- Low collector saturation voltage: $V_{CE(sat)} = 0.5\text{ V MAX. (@5 A)}$
- Ideal for use in ramp drivers or inductance drivers
- Complementary transistor: 2SB1097

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$)

Parameter	Symbol	Ratings	Unit
Collector to base voltage	V_{CBO}	100	V
Collector to emitter voltage	V_{CEO}	60	V
Emitter to base voltage	V_{EBO}	7.0	V
Collector current (DC)	$I_{C(DC)}$	7.0	A
Collector current (Pulse)	$I_{C(pulse)}^*$	15	A
Base current (DC)	$I_{B(DC)}$	3.5	A
Total power dissipation	$P_T (T_C = 25^\circ\text{C})$	30	W
Total power dissipation	$P_T (T_A = 25^\circ\text{C})$	2.0	W
Junction temperature	T_J	150	$^\circ\text{C}$
Storage temperature	T_{stg}	-55 to +150	$^\circ\text{C}$

* $PW \leq 300\text{ }\mu\text{s}$, duty cycle $\leq 10\%$

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$)

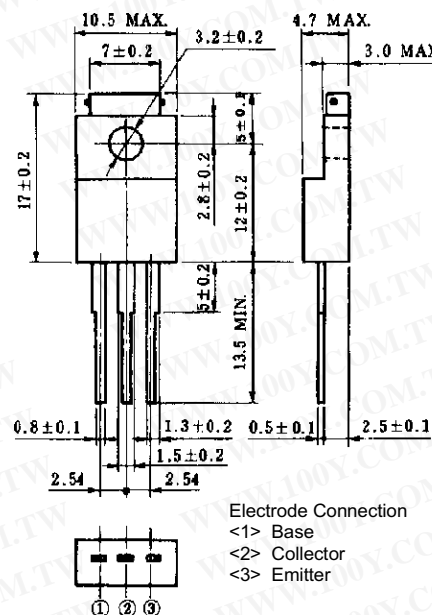
Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Collector cutoff current	I_{CBO}	$V_{CB} = 80\text{ V}, I_E = 0$			10	μA
Emitter cutoff current	I_{EBO}	$V_{EB} = 5.0\text{ V}, I_C = 0$			10	μA
DC current gain	h_{FE1}^{**}	$V_{CE} = 1.0\text{ V}, I_C = 3\text{ A}$	40		200	
DC current gain	h_{FE2}^{**}	$V_{CE} = 1.0\text{ V}, I_C = 5\text{ A}$	20			
Collector saturation voltage	$V_{CE(sat)}^{**}$	$I_C = 5\text{ A}, I_B = 0.5\text{ A}$			0.5	V
Base saturation voltage	$V_{BE(sat)}^{**}$	$I_C = 5\text{ A}, I_B = 0.5\text{ A}$			1.5	V

** Pulse test $PW \leq 350\text{ }\mu\text{s}$, duty cycle $\leq 2\%$ /per pulsed

h_{FE} CLASSIFICATION

Marking	M	L	K
h_{FE1}	40 to 80	60 to 120	100 to 200

PACKAGE DRAWING (UNIT: mm)



勝特力材料 886-3-5753170
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TOTAL POWER DISSIPATION vs. TEMPERATURE

2 mm aluminum board
silicon grease coating

With infinite heatsink
($T_c = 25^\circ\text{C}$)

500 cm²
200 cm²
100 cm²
50 cm²
20 cm²

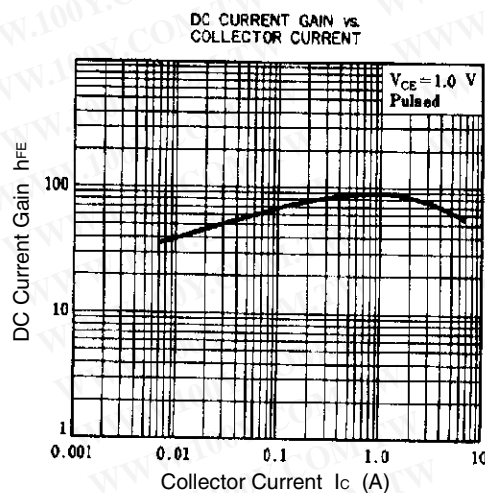
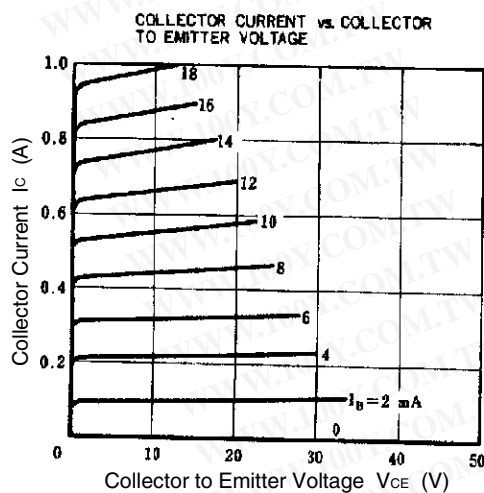
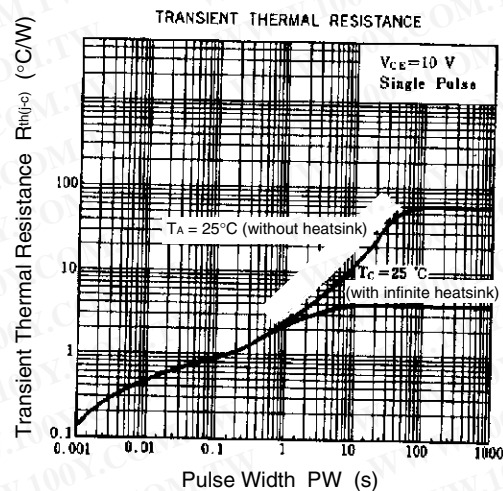
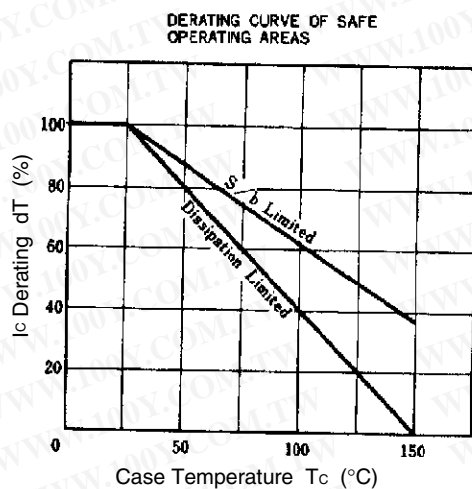
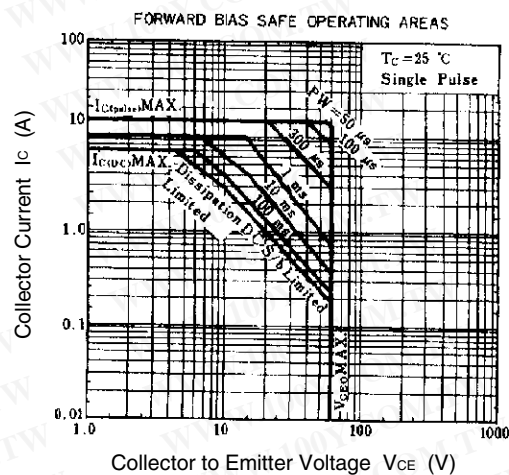
Without heatsink

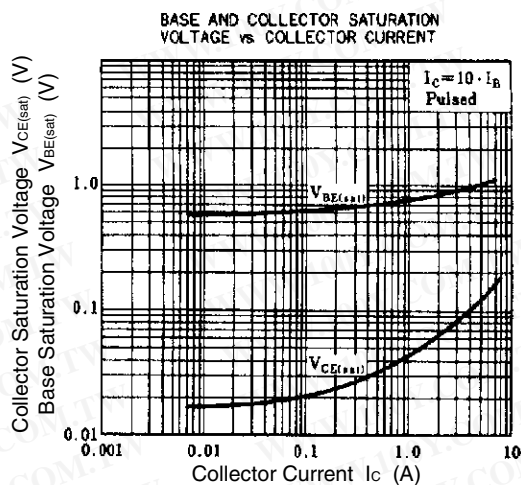
Total Power Dissipation P_T (W)

Temperature T ($^\circ\text{C}$)

Detailed description: This is a line graph showing the relationship between total power dissipation and temperature for different heatsink areas. The y-axis represents Total Power Dissipation P_T in Watts (W), ranging from 0 to 30 in increments of 5. The x-axis represents Temperature T in degrees Celsius ($^\circ\text{C}$), ranging from 0 to 175 in increments of 25. There are six linear data series, all originating from a common point at $T = 150^\circ\text{C}$ and $P_T = 0$ W. The series are labeled with their respective heatsink areas: 500 cm², 200 cm², 100 cm², 50 cm², 20 cm², and 'Without heatsink'. The 'Without heatsink' line is the steepest, while the 500 cm² line is the least steep. A horizontal line at $P_T = 20$ W is labeled 'With infinite heatsink ($T_c = 25^\circ\text{C}$)'. The graph also specifies '2 mm aluminum board' and 'silicon grease coating'.

Heatsink Area (cm ²)	Temperature T ($^\circ\text{C}$)	Total Power Dissipation P_T (W)
Without heatsink	150	0
Without heatsink	125	5
Without heatsink	100	10
Without heatsink	75	15
Without heatsink	50	20
Without heatsink	25	25
20 cm ²	150	0
20 cm ²	125	2.5
20 cm ²	100	5
20 cm ²	75	7.5
20 cm ²	50	10
20 cm ²	25	12.5
50 cm ²	150	0
50 cm ²	125	2
50 cm ²	100	4
50 cm ²	75	6
50 cm ²	50	8
50 cm ²	25	10
100 cm ²	150	0
100 cm ²	125	1.5
100 cm ²	100	3
100 cm ²	75	4.5
100 cm ²	50	6
100 cm ²	25	7.5
200 cm ²	150	0
200 cm ²	125	1
200 cm ²	100	2
200 cm ²	75	3
200 cm ²	50	4
200 cm ²	25	5
500 cm ²	150	0
500 cm ²	125	0.75
500 cm ²	100	1.5
500 cm ²	75	2.25
500 cm ²	50	3
500 cm ²	25	3.75





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