DATA SHEET

DARLINGTON POWER TRANSISTOR 2SD1592

NPN SILICON TRIPLE DIFFUSED TRANSISTOR (DARLINGTON CONNECTION) FOR HIGH-VOLTAGE LOW-SPEED SWITCHING

FEATURES

NEC

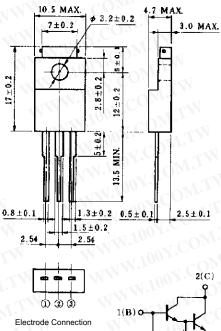
- High DC current gain due to Darlington connection
- Low collector saturation
- Reverse deterrence type
- · Ideal for use in devices such as pulse motor drivers and relay drivers of PC terminals, and ignitors of general-purpose engines.
- · Mold package that does not require an insulating board or insulation bushing

ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

Parameter	Symbol	Ratings	Unit
Collector to base voltage	Vсво	500	V
Collector to emitter voltage	VCEO	+300, -10	v
Emitter to base voltage	VEBO	10	00v .
Collector current	IC(DC)	5.0	A
Collector current	C(pulse)*	10	A
Base current	IB(DC)	0.5	Α
Total power dissipation	Р⊤ (Tc = 25°C)	30 🔨	w
Total power dissipation	P⊤ (Ta = 25°C)	1.5	W
Junction temperature	CTi	150 🧹	°C
Storage temperature	Tstg	-55 to +150	°C

* PW \leq 300 μ s, duty cycle \leq 10%

PACKAGE DRAWING (UNIT: mm)



1. Base 2. Collector 3. Emitter



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ELECTRICAL CHARACTERISTICS (Ta = 25° C)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Uni
Collector cutoff current	Ісво	V _{CB} = 400 V, I _E = 0		L'IN	10	μA
DC current gain	hfe1*	Vce = 2.0 V, lc = 2.0 A	400	M.T.W	3,000	
DC current gain	hfe2*	Vce = 2.0 V, Ic = 3.0 A	100	WIN		
Collector saturation voltage	V _{CE(sat)} *	Ic = 2 A, Iв = 5 mA	. NOY.C	1.0	1.5	V
Base saturation voltage	V _{BE(sat)} *	Ic = 2 A, Iв = 5 mA	.Voo	1.6	2.0	V
Turn-on time	ton	Ic = 3.0 A, I _{B1} = −I _{B2} = 30 mA R _L = 50 Ω, V _{CC} \cong 150 V	1.100	1.0	III	μs
Storage time	tstg		W.100 .	12		μs
Fall time	tr		W100	6	1.1	μs

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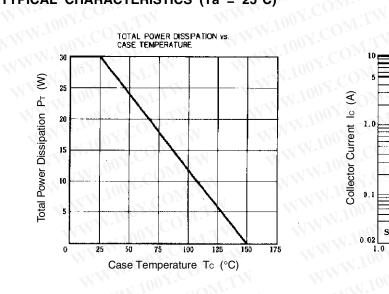
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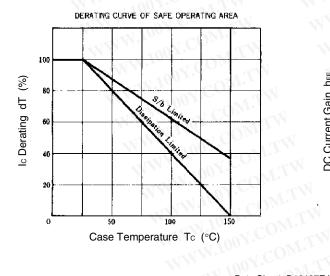
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he CLASSIFICATION

Marking	М	WVEN	K COK
hfe	400 to 800	600 to 1,200	1,000 to 3,000
1002	ON.TN	.W.	COM.

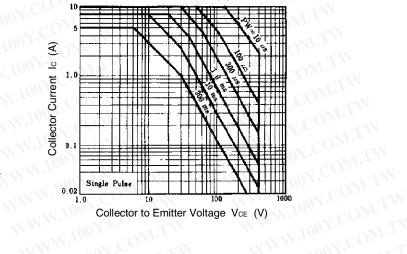
TYPICAL CHARACTERISTICS (Ta = 25°C)



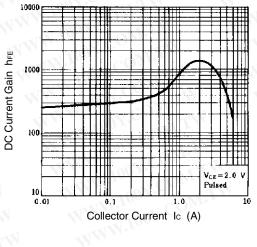


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FORWARD BIAS SAFE OPERATING AREA



WWW.100Y DC CURRENT GAIN vs. COLLECTOR CURRENT

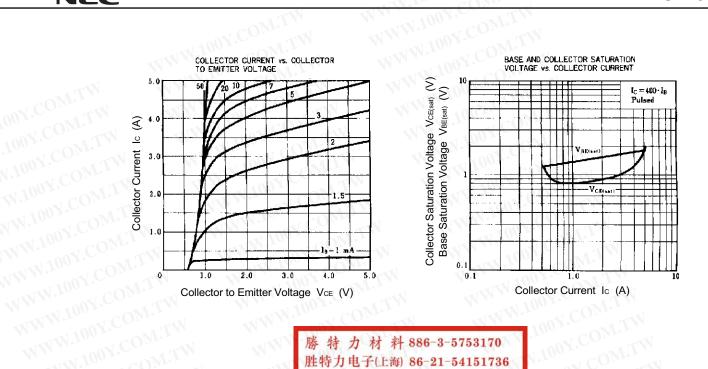


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