Preferred Device

勝 特 力 材 料 886-3-5753170 胜特力电子(上海) 86-21-54151736 胜特力电子(深圳) 86-755-83298787

Http://www. 100y. com. tw



# **Surface Mount Power Package**

This device employs the Schottky Barrier principle in a large area metal-to-silicon power diode. State-of-the-art geometry features epitaxial construction with oxide passivation and metal overlay contact. Ideally suited for low voltage, high frequency rectification, or as free wheeling and polarity protection diodes, in surface mount applications where compact size and weight are critical to the system.

### **Features**

- Very Low Forward Voltage Drop (0.395 Volts Max @ 1.0 A, T<sub>J</sub> = 25°C)
- Small Compact Surface Mountable Package with J-Bend Leads
- Highly Stable Oxide Passivated Junction
- Guard-Ring for Stress Protection
- Pb-Free Package is Available

### **Mechanical Characteristics**

- Case: Epoxy, Molded
- Weight: 95 mg (approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Cathode Polarity Band

### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit V	
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	30		
Average Rectified Forward Current  T <sub>L</sub> = 120°C  T <sub>L</sub> = 110°C	I <sub>F(AV)</sub>	1.0 2.0	A	
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I <sub>FSM</sub>	40	A	
Operating Junction Temperature	TJO	-65 to +125	°C	

# THERMAL CHARACTERISTICS

Rating	Symbol	Symbol Value		
Thermal Resistance, Junction–to–Lead (T <sub>1</sub> = 25°C)	$R_{ heta JL}$	12	°C/W	

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.



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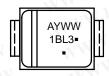
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# SCHOTTKY BARRIER RECTIFIER 1.0 AMPERE 30 VOLTS



SMB CASE 403A PLASTIC

#### MARKING DIAGRAM



1BL3 = Specific Device Code A = Assembly Location

Y = Year WW = Work Week Pb-Free Package

(Note: Microdot may be in either location)

### ORDERING INFORMATION

Device	Package	Shipping <sup>†</sup>		
MBRS130LT3	SMB	2500/Tape & Reel		
MBRS130LT3G	SMB (Pb-Free)	2500/Tape & Reel		

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

**Preferred** devices are recommended choices for future use and best overall value.

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# **ELECTRICAL CHARACTERISTICS**

Characteristic	Symbol	Value	Unit
Maximum Instantaneous Forward Voltage (Note 1)	$V_{F}$	WIT	V
$(i_F = 1.0 \text{ A}, T_J = 25^{\circ}\text{C})$	WY -100	0.395	
$(i_F = 2.0 \text{ A}, T_J = 25^{\circ}\text{C})$	1007.	0.445	
Maximum Instantaneous Reverse Current (Note 1)	IR	WT	mA
(Rated dc Voltage, T <sub>J</sub> = 25°C)	, 11W.100	1.0	
(Rated dc Voltage, T <sub>J</sub> = 100°C)	11/11/10/10	10	

<sup>1.</sup> Pulse Test: Pulse Width = 300 μs, Duty Cycle ≤ 2%.

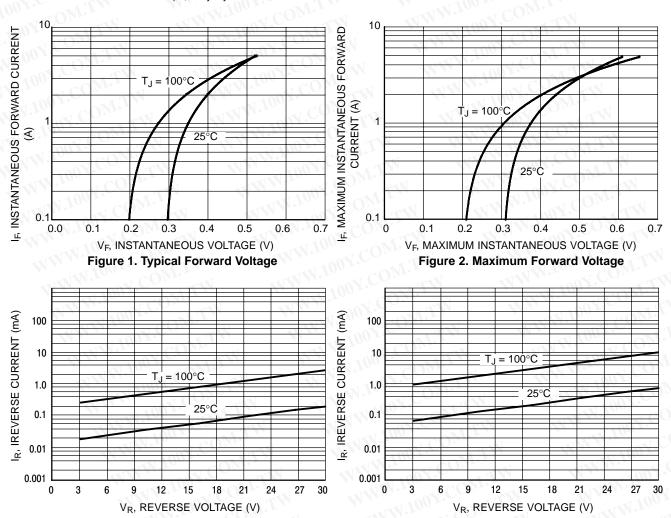
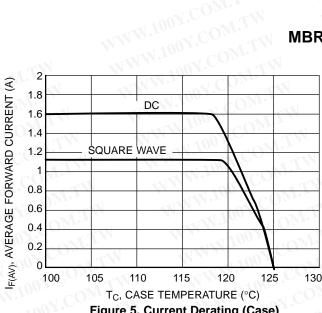


Figure 3. Typical Reverse Leakage Current

Figure 4. Typical Maximum Reverse Leakage Curent

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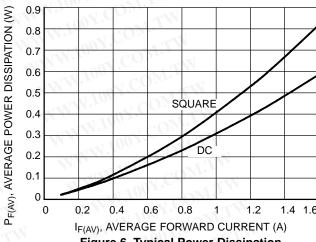


Figure 5. Current Derating (Case)



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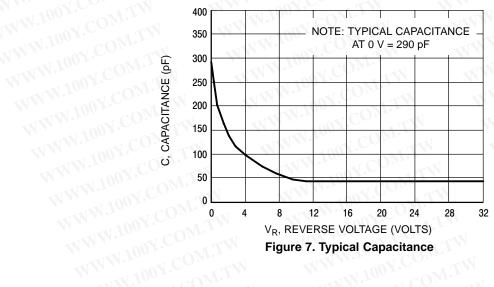


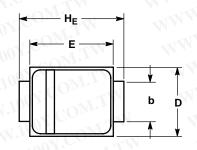
Figure 7. Typical Capacitance

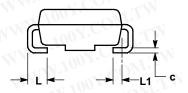
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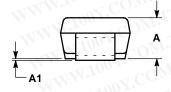
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# **PACKAGE DIMENSIONS**

## **SMB** PLASTIC PACKAGE CASE 403A-03 ISSUE E







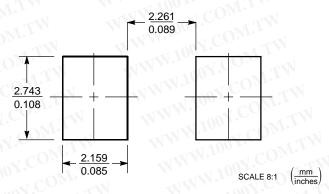
#### NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
   CONTROLLING DIMENSION: INCH.
- 3. D DIMENSION SHALL BE MEASURED WITHIN DIMENSION P.

-1	MILLIMETERS INCHES					
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	1.90	2.13	2.41	0.075	0.084	0.095
A1	0.05	0.10	0.15	0.002	0.004	0.006
b	1.96	2.03	2.11	0.077	0.080	0.083
С	0.15	0.23	0.30	0.006	0.009	0.012
D	3.30	3.56	3.81	0.130	0.140	0.150
E	4.06	4.32	4.57	0.160	0.170	0.180
HE	5.21	5.44	5.59	0.205	0.214	0.220
L	0.76	1.02	1.27	0.030	0.040	0.050
L1	0.51 REF			0.020 REF		

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### SOLDERING FOOTPRINT'



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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