MC33269, NCV33269

800 mA, Adjustable Output, Low Dropout Voltage Regulator

The MC33269/NCV33269 series are low dropout, medium current, fixed and adjustable, positive voltage regulators specifically designed for use in low input voltage applications. These devices offer the circuit designer an economical solution for precision voltage regulation, while keeping power losses to a minimum.

The regulator consists of a 1.0 V dropout composite PNP−NPN pass transistor, current limiting, and thermal shutdown.

Features

- 3.3 V, 5.0 V, 12 V and Adjustable Versions
  2.85 V version available as MC34268
- Space Saving DPAK, SO−8 and SOT−223 Power Packages
- 1.0 V Dropout
- Output Current in Excess of 800 mA
- Thermal Protection
- Short Circuit Protection
- Output Trimmed to 1.0% Tolerance
- Pb−Free Packages are Available
- NCV Prefix for Automotive and Other Applications Requiring Site and Control Changes

DEVICE TYPE/NOMINAL OUTPUT VOLTAGE

MC33269D	Adj	MC33269D−5.0	5.0 V
MC33269DT	Adj	MC33269DT−5.0	5.0 V
NCV33269DTRK*	Adj	MC33269T−5.0	5.0 V
MC33269T	Adj		
MC33269D−3.3	3.3 V	MC33269D−012	12 V
MC33269DT−3.3	3.3 V	MC33269DT−012	12 V
NCV33269DTRK−3.3*	3.3 V	NCV33269DTRK−012*	12 V
MC33269T−3.3	3.3 V	MC33269T−012	12 V
MC33269ST−3.3	3.3 V		

*NCV prefix is for automotive and other applications requiring site and change control.

ON Semiconductor®

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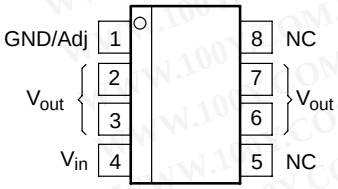
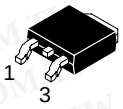
SO−8
D SUFFIX
CASE 751

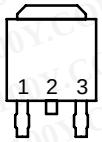


(Top View)



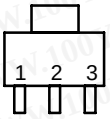
DPAK
DT SUFFIX
CASE 369C

1. GND/Adj
2. V_{out}
3. V_{in}



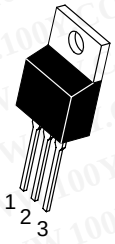
(Top View)

SOT−223
ST SUFFIX
CASE 318E



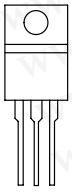
(Top View)

Heatsink surface (shown as terminal 4 in case outline drawing) is connected to Pin 2.



TO−220AB
T SUFFIX
CASE 221AB

1. GND/Adj
2. V_{out}
3. V_{in}



1 2 3
(Top View)

Heatsink surface (shown as terminal 4 in case outline drawing) is connected to Pin 2.

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 7 of this data sheet.

DEVICE MARKING INFORMATION

See general marking information in the device marking section on page 9 of this data sheet.

MAXIMUM RATINGS

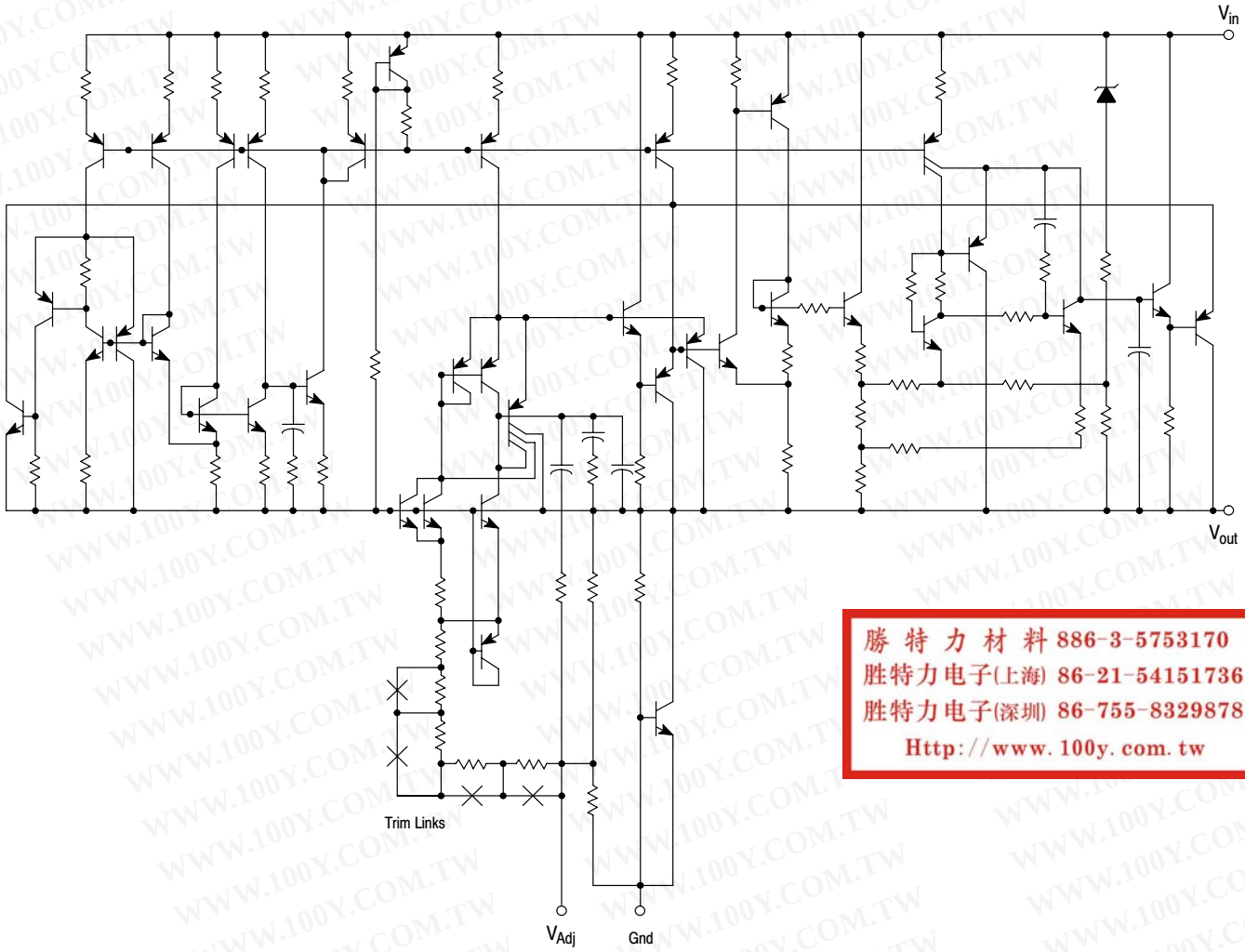
Rating		Symbol	Value	Unit
Power Supply Input Voltage		V_{in}	20	V
Power Dissipation				
Case 369C (DPAK)	T_A = 25°C	P_D	Internally Limited	W
	Thermal Resistance, Junction−to−Ambient	θ_{JA}	92	°C/W
	Thermal Resistance, Junction−to−Case	θ_{JC}	6.0	°C/W
Case 751 (SO−8)	T_A = 25°C	P_D	Internally Limited	W
	Thermal Resistance, Junction−to−Ambient	θ_{JA}	160	°C/W
	Thermal Resistance, Junction−to−Case	θ_{JC}	25	°C/W
Case 221A (TO−220)	T_A = 25°C	P_D	Internally Limited	W
	Thermal Resistance, Junction−to−Ambient	θ_{JA}	65	°C/W
	Thermal Resistance, Junction−to−Case	θ_{JC}	5.0	°C/W
Case 318E (SOT−223)	T_A = 25°C	P_D	Internally Limited	W
	Thermal Resistance, Junction−to−Ambient	θ_{JA}	156	°C/W
	Thermal Resistance, Junction−to−Case	θ_{JC}	15	°C/W
Operating Die Junction Temperature Range		T_J	−40 to +150	°C
Operating Ambient Temperature Range	MC33269	T_A	−40 to +125	°C
	NCV33269		−40 to +125	
Storage Temperature		T_{stg}	−55 to +150	°C
Electrostatic Discharge Sensitivity (ESD)	Human Body Model (HBM)	ESD	4000	V
	Machine Model (MM)		400	

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

ELECTRICAL CHARACTERISTICS (C_O = 10 μF, T_A = 25°C, for min/max values T_A = −40°C to +125°C, unless otherwise noted.)

Characteristic		Symbol	Min	Typ	Max	Unit
Output Voltage (I_{out} = 10 mA, T_A = 25°C)		V_O				V
	3.3 Suffix (V_{CC} = 5.3 V)		3.27	3.3	3.33	
	5.0 Suffix (V_{CC} = 7.0 V)		4.95	5.0	5.05	
	12 Suffix (V_{CC} = 14 V)		11.88	12	12.12	
Output Voltage (Line, Load and Temperature) (Note 1) ($1.25\ V \le V_{in} - V_{out} \le 15\ V$, I_{out} = 500 mA) ($1.35\ V \le V_{in} - V_{out} \le 10\ V$, I_{out} = 800 mA)		V_O				V
	3.3 Suffix		3.23	3.3	3.37	
	5.0 Suffix		4.9	5.0	5.1	
	12 Suffix		11.76	12	12.24	
Reference Voltage for Adjustable Voltage (I_{out} = 10 mA, $V_{in} - V_{out}$ = 2.0 V, T_A = 25°C)		V_{ref}	1.235	1.25	1.265	V
Reference Voltage (Line, Load and Temperature) (Note 1) for Adjustable Voltage ($1.25\ V \le V_{in} - V_{out} \le 15\ V$, I_{out} = 500 mA) ($1.35\ V \le V_{in} - V_{out} \le 10\ V$, I_{out} = 800 mA)		V_{ref}	1.225	1.25	1.275	V
Line Regulation	(I_{out} = 10 mA, V_{in} = [V_{out} + 1.5 V] to V_{in} = 20 V, T_A = 25°C)	Reg_{line}	–	–	0.3	%
Load Regulation	(V_{in} = V_{out} + 3.0 V, I_{out} = 10 mA to 800 mA, T_A = 25°C)	Reg_{load}	–	–	0.5	%
Dropout Voltage	(I_{out} = 500 mA)	$V_{in} - V_{out}$	–	1.0	1.25	V
	(I_{out} = 800 mA)		–	1.1	1.35	
Ripple Rejection	(10 V_{pp}, 120 Hz Sinewave; I_{out} = 500 mA)	RR	55	–	–	dB
Current Limit	($V_{in} - V_{out}$ = 10 V)	I_{Limit}	800	–	–	mA
Quiescent Current (Fixed Output)	($1.5\ V \le V_{out} \le 3.3\ V$)	I_Q	–	5.5	8.0	mA
	($5\ V \le V_{out} \le 12\ V$)		–	–	20	
Minimum Required Load Current	Fixed Output Voltage	I_{Load}	–	–	0	mA
	Adjustable Voltage		8.0	–	–	
Adjustment Pin Current		I_{Adj}	–	–	120	μA

1. The MC33269−12, $V_{in} - V_{out}$ is limited to 8.0 V maximum, because of the 20 V maximum rating applied to V_{in}.



This device contains 38 active transistors.

Figure 1. Internal Schematic

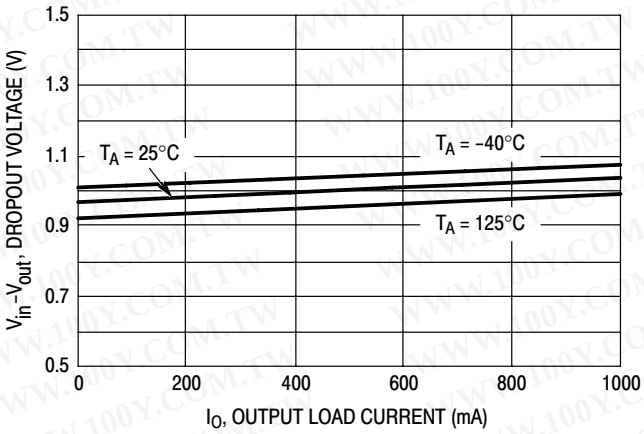


Figure 2. Dropout Voltage versus Output Load Current

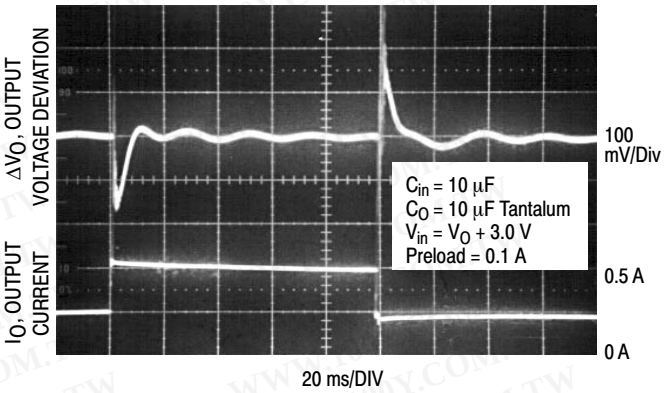


Figure 3. Transient Load Regulation

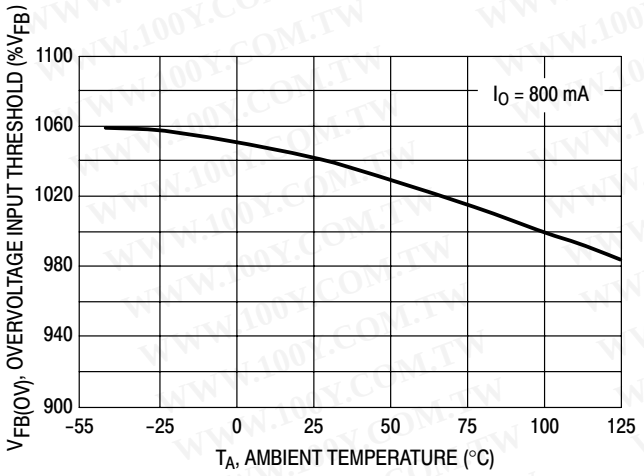


Figure 4. Dropout Voltage versus Temperature

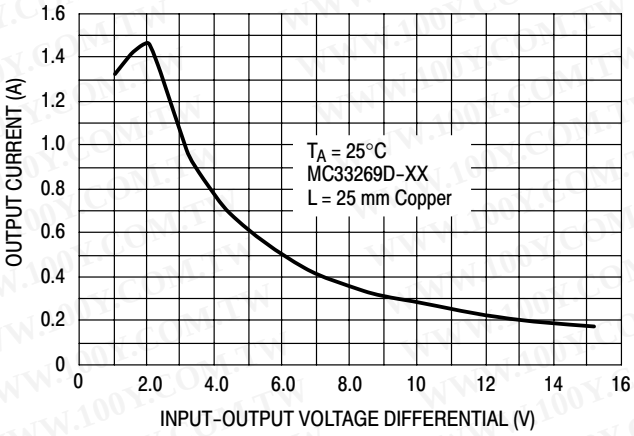


Figure 5. MC33269−XX Output DC Current versus Input−Output Differential Voltage

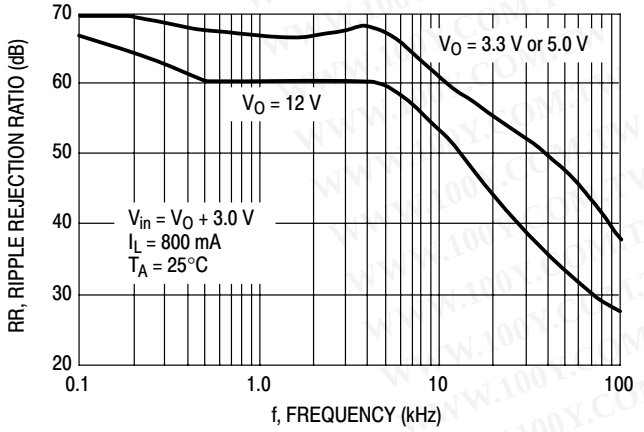


Figure 6. MC33269 Ripple Rejection versus Frequency

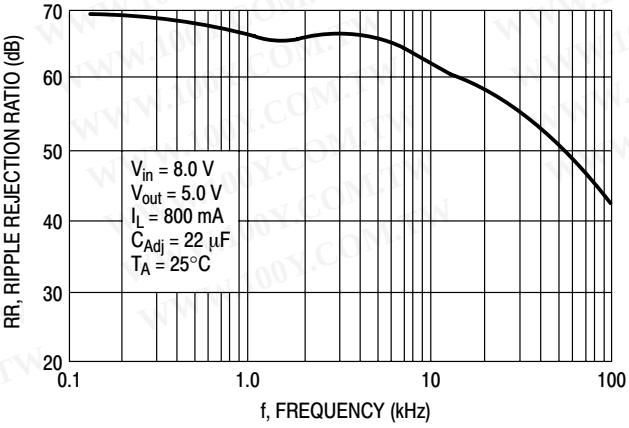


Figure 7. MC33269−ADJ Ripple Rejection versus Frequency

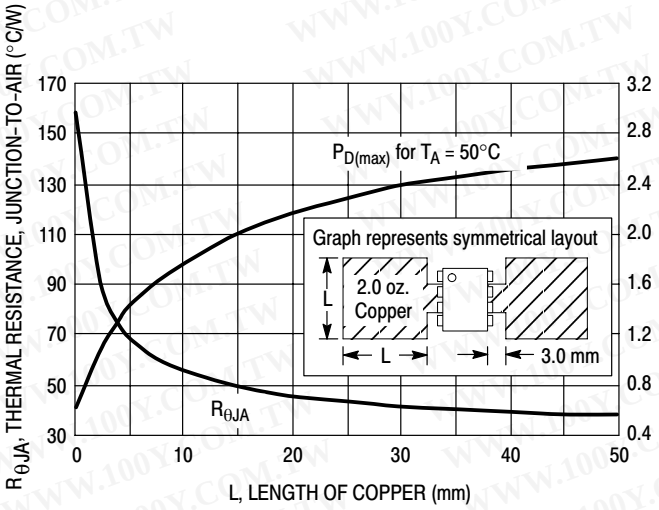


Figure 8. SOP−8 Thermal Resistance and Maximum Power Dissipation versus P.C.B. Copper Length

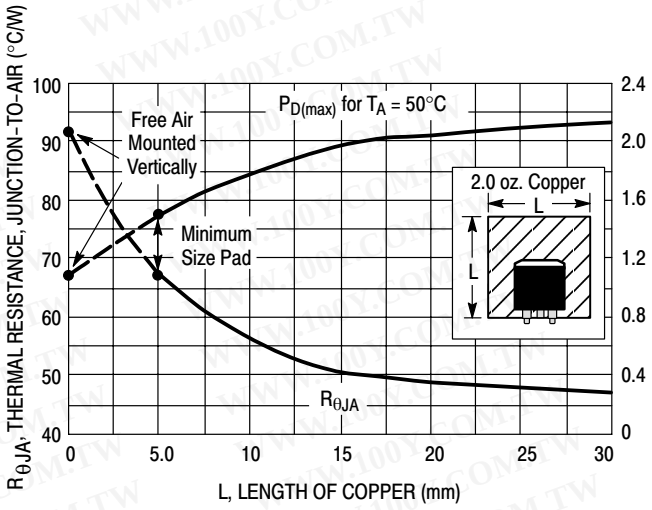


Figure 9. DPAK Thermal Resistance and Maximum Power Dissipation versus P.C.B. Copper Length

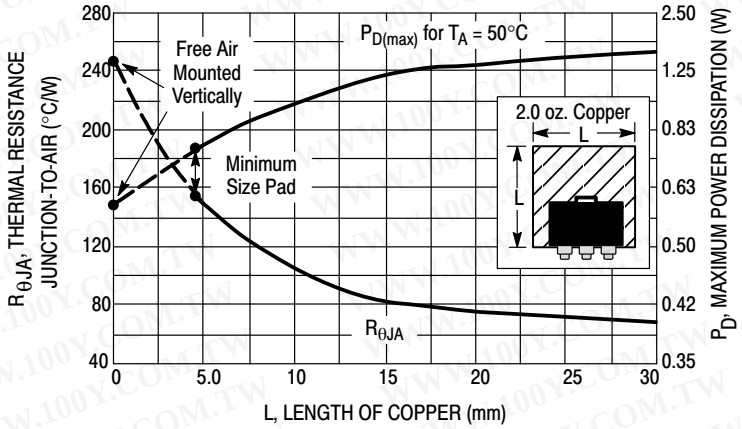


Figure 10. SOT−223 Thermal Resistance and Maximum Power Dissipation versus P.C.B. Copper Length

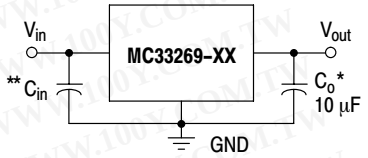
APPLICATIONS INFORMATION

Figures 11 through 15 are typical application circuits. The output current capability of the regulator is in excess of 800 mA, with a typical dropout voltage of less than 1.0 V. Internal protective features include current and thermal limiting.

* The MC33269 requires an external output capacitor for stability. The capacitor should be at least 10 μF with an equivalent series resistance (ESR) of less than 10 Ω but greater than 0.2 Ω over the anticipated operating temperature range. With economical electrolytic capacitors, cold temperature operation can pose a problem. As temperature decreases, the capacitance also decreases and the ESR increases, which could cause the circuit to oscillate. Also capacitance and ESR of a solid tantalum capacitor is more stable over temperature. The use of a low ESR ceramic capacitor placed within close proximity to the output of the device could cause instability.

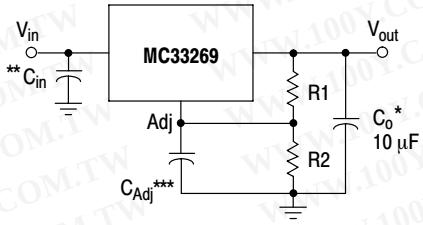
** An input bypass capacitor is recommended to improve transient response or if the regulator is connected to the supply input filter with long wire lengths. This will reduce the circuit's sensitivity to the input line impedance at high frequencies. A 0.33 μF or larger tantalum, mylar, ceramic, or other capacitor having low internal impedance at high frequencies should be chosen. The bypass capacitor should be mounted with shortest possible lead or track length directly across the regulator's input terminals. **Applications should be tested over all operating conditions to insure stability.**

Internal thermal limiting circuitry is provided to protect the integrated circuit in the event that the maximum junction temperature is exceeded. When activated, typically at 170°C, the output is disabled. There is no hysteresis built into the thermal limiting circuit. As a result, if the device is overheating, the output will appear to be oscillating. This feature is provided to prevent catastrophic failures from accidental device overheating. **It is not intended to be used as a substitute for proper heat−sinking.**



An input capacitor is not necessary for stability, however it will improve the overall performance.

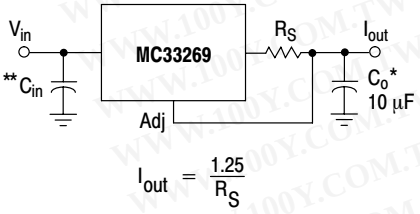
Figure 11. Typical Fixed Output Application



$$V_{out} = 1.25\left(1 + \frac{R2}{R1}\right) + I_{Adj}R2$$

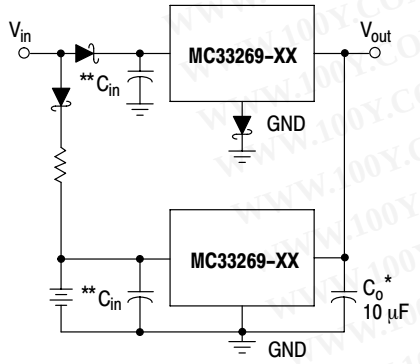
***C_{Adj} is optional, however it will improve the ripple rejection. The MC34269 develops a 1.25 V reference voltage between the output and the adjust terminal. Resistor R1, operates with constant current to flow through it and resistor R2. This current should be set such that the Adjust Pin current causes negligible drop across resistor R2. The total current with minimum load should be greater than 8.0 mA.

Figure 12. Typical Adjustable Output Application



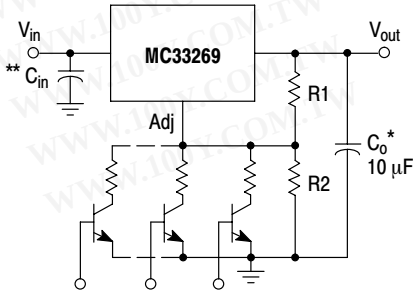
$$I_{out} = \frac{1.25}{R_S}$$

Figure 13. Current Regulator



The Schottky diode in series with the ground leg of the upper regulator shifts its output voltage higher by the forward voltage drop of the diode. This will cause the lower device to remain off until the input voltage is removed.

Figure 14. Battery Backed−Up Power Supply



R_2 sets the maximum output voltage. Each transistor reduces the output voltage when turned on.

Figure 15. Digitally Controlled Voltage Regulator

ORDERING INFORMATION

Device	Package	Shipping Information†
MC33269D	SO−8	98 Units / Rail
MC33269DG	SO−8 (Pb−Free)	
MC33269DR2	SO−8	2500 Units / Tape & Reel
MC33269DR2G	SO−8 (Pb−Free)	
MC33269DT	DPAK	75 Units / Rail
MC33269DTG	DPAK (Pb−Free)	
MC33269DTRK	DPAK	2500 Units / Tape & Reel
MC33269DTRKG	DPAK (Pb−Free)	
MC33269T	TO−220	50 Units / Rail
MC33269TG	TO−220 (Pb−Free)	
MC33269D−3.3	SO−8	98 Units / Rail
MC33269D−3.3G	SO−8 (Pb−Free)	
MC33269DR2−3.3	SO−8	2500 Units / Tape & Reel
MC33269DR2−3.3G	SO−8 (Pb−Free)	
MC33269DT−3.3	DPAK	75 Units / Rail
MC33269DT−3.3G	DPAK (Pb−Free)	
MC33269DTRK−3.3	DPAK	2500 Units / Tape & Reel
MC33269DTRK−3.3G	DPAK (Pb−Free)	
MC33269ST−3.3T3	SOT−223	4000 Units / Tape & Reel
MC33269ST−3.3T3G	SOT−223 (Pb−Free)	
MC33269T−3.3	TO−220	50 Units / Rail
MC33269T−3.3G	TO−220 (Pb−Free)	
MC33269D−5.0	SO−8	98 Units / Rail
MC33269D−5.0G	SO−8 (Pb−Free)	
MC33269DR2−5.0	SO−8	2500 Units / Tape & Reel
MC33269DR2−5.0G	SO−8 (Pb−Free)	
MC33269DT−5.0	DPAK	75 Units / Rail
MC33269DT−5.0G	DPAK (Pb−Free)	
MC33269DTRK−5.0	DPAK	2500 Units / Tape & Reel
MC33269DTRK−5.0G	DPAK (Pb−Free)	

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

*NCV prefix is for automotive and other applications requiring site and control changes.

ORDERING INFORMATION (continued)

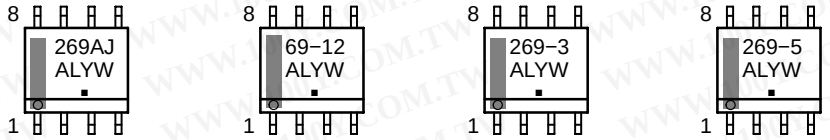
Device	Package	Shipping Information†
MC33269T−5.0	TO−220	50 Units / Rail
MC33269T−5.0G	TO−220 (Pb−Free)	
MC33269D−012	SO−8	98 Units / Rail
MC33269D−012G	SO−8 (Pb−Free)	
MC33269DR2−012	SO−8	2500 Units / Tape & Reel
MC33269DR2−012G	SO−8 (Pb−Free)	
MC33269DT−012	DPAK	75 Units / Rail
MC33269DT−012G	DPAK (Pb−Free)	
MC33269DTRK−012	DPAK	2500 Units / Tape & Reel
MC33269DTRK−012G	DPAK (Pb−Free)	
MC33269T−012	TO−220	50 Units / Rail
MC33269T−012G	TO−220 (Pb−Free)	
NCV33269DTRK*	DPAK	2500 Units / Tape & Reel
NCV33269DTRKG*	DPAK (Pb−Free)	
NCV33269DTRK−3.3*	DPAK	
NCV33269DTRK−3.3G*	DPAK (Pb−Free)	
NCV33269DTRK−012*	DPAK	
NCV33269DTRK−012G*	DPAK (Pb−Free)	

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

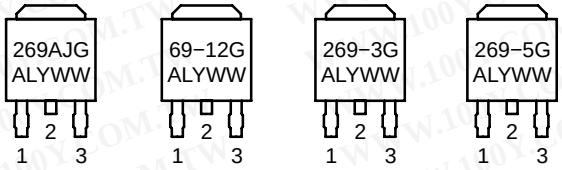
*NCV prefix is for automotive and other applications requiring site and control changes.

MARKING DIAGRAMS

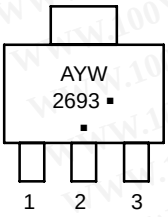
SO−8
D SUFFIX
CASE 751



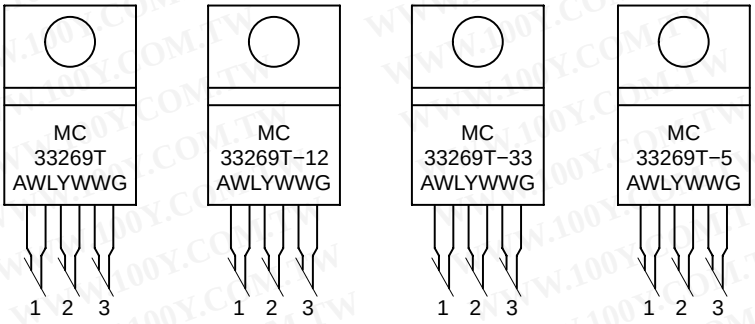
DPAK
DT SUFFIX
CASE 369C



SOT−223
ST SUFFIX
CASE 318E



TO−220AB
T SUFFIX
CASE 221A

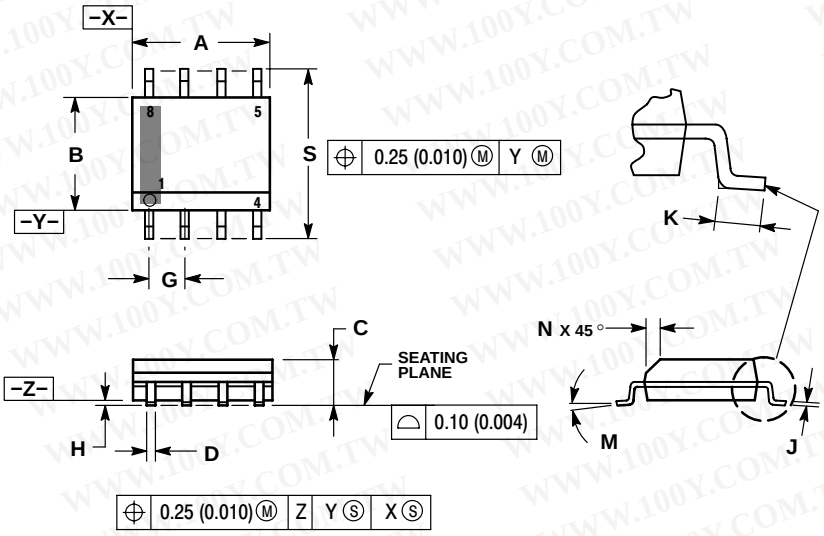


A	= Assembly Location
L, WL	= Wafer Lot
Y	= Year
W, WW	= Work Week
G	= Pb−Free Package
▪	= Pb−Free Package

(Note: Microdot may be in either location)

PACKAGE DIMENSIONS

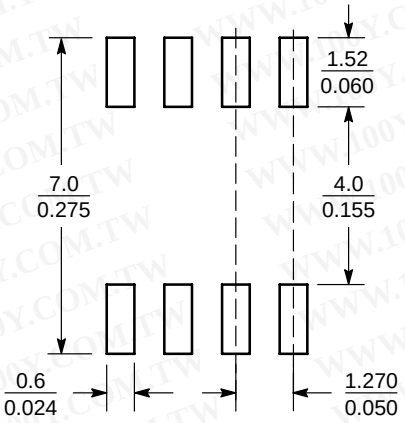
SO−8
D SUFFIX
CASE 751−07
ISSUE AH



NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.
3. DIMENSION A AND B DO NOT INCLUDE MOLD PROTRUSION.
4. MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.
5. DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.
6. 751−01 THRU 751−06 ARE OBSOLETE. NEW STANDARD IS 751−07.

	MILLIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
A	4.80	5.00	0.189	0.197
B	3.80	4.00	0.150	0.157
C	1.35	1.75	0.053	0.069
D	0.33	0.51	0.013	0.020
G	1.27 BSC		0.050 BSC	
H	0.10	0.25	0.004	0.010
J	0.19	0.25	0.007	0.010
K	0.40	1.27	0.016	0.050
M	0 °	8 °	0 °	8 °
N	0.25	0.50	0.010	0.020
S	5.80	6.20	0.228	0.244

SOLDERING FOOTPRINT*

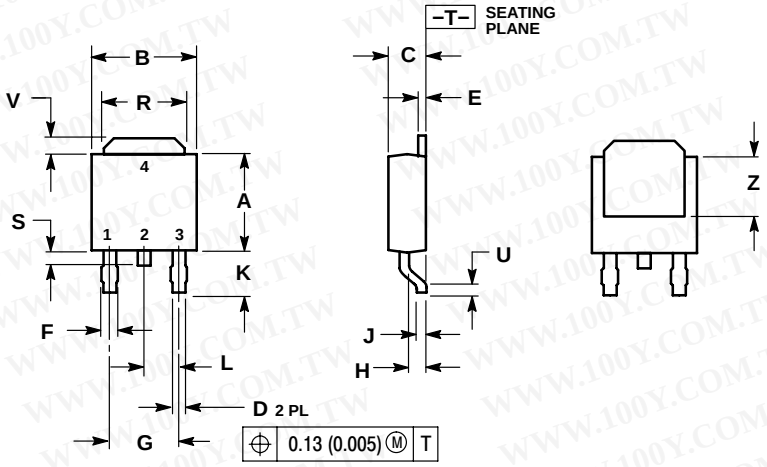


SCALE 6:1 $\left(\frac{\text{mm}}{\text{inches}}\right)$

*For additional information on our Pb−Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

PACKAGE DIMENSIONS

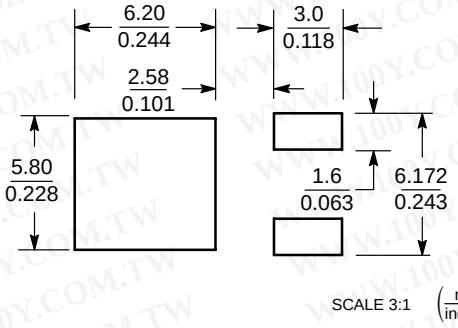
DPAK
DT SUFFIX
CASE 369C−01
ISSUE O



NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
A	0.235	0.245	5.97	6.22
B	0.250	0.265	6.35	6.73
C	0.086	0.094	2.19	2.38
D	0.027	0.035	0.69	0.88
E	0.018	0.023	0.46	0.58
F	0.037	0.045	0.94	1.14
G	0.180 BSC		4.58 BSC	
H	0.034	0.040	0.87	1.01
J	0.018	0.023	0.46	0.58
K	0.102	0.114	2.60	2.89
L	0.090 BSC		2.29 BSC	
R	0.180	0.215	4.57	5.45
S	0.025	0.040	0.63	1.01
U	0.020	–––	0.51	–––
V	0.035	0.050	0.89	1.27
Z	0.155	–––	3.93	–––

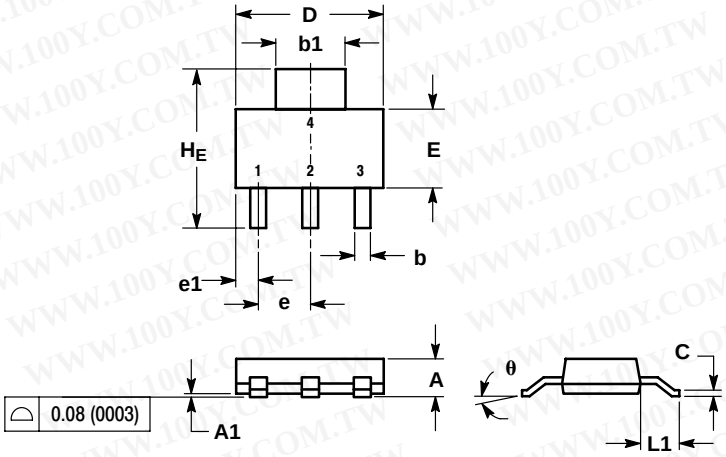
SOLDERING FOOTPRINT*



*For additional information on our Pb−Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

PACKAGE DIMENSIONS

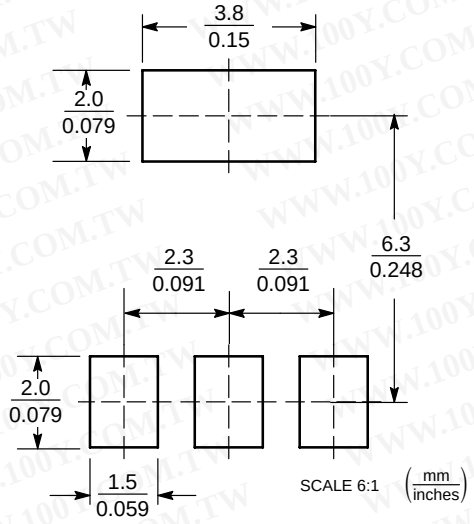
SOT−223
ST SUFFIX
CASE 318E−04
ISSUE L



NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
A	1.50	1.63	1.75	0.060	0.064	0.068
A1	0.02	0.06	0.10	0.001	0.002	0.004
b	0.60	0.75	0.89	0.024	0.030	0.035
b1	2.90	3.06	3.20	0.115	0.121	0.126
c	0.24	0.29	0.35	0.009	0.012	0.014
D	6.30	6.50	6.70	0.249	0.256	0.263
E	3.30	3.50	3.70	0.130	0.138	0.145
e	2.20	2.30	2.40	0.087	0.091	0.094
e1	0.85	0.94	1.05	0.033	0.037	0.041
L1	1.50	1.75	2.00	0.060	0.069	0.078
H_E	6.70	7.00	7.30	0.264	0.276	0.287
θ	0°	–	10°	0°	–	10°

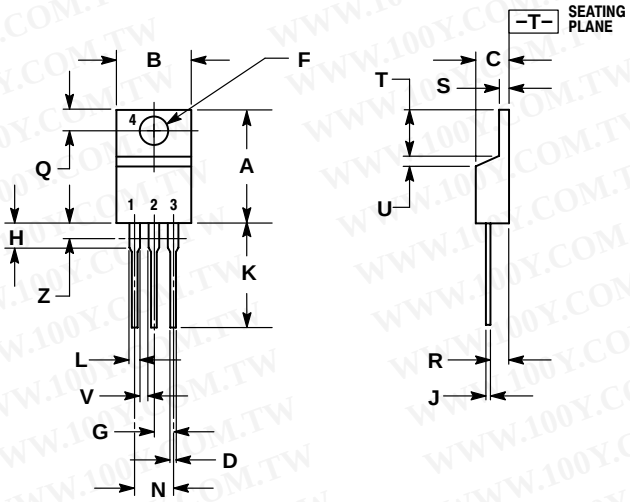
SOLDERING FOOTPRINT*



*For additional information on our Pb−Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

PACKAGE DIMENSIONS

TO−220AB, SINGLE GAUGE
T SUFFIX
CASE 221AB−01
ISSUE O



NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
A	0.570	0.620	14.48	15.75
B	0.380	0.405	9.66	10.28
C	0.160	0.190	4.07	4.82
D	0.025	0.035	0.64	0.88
F	0.142	0.147	3.61	3.73
G	0.095	0.105	2.42	2.66
H	0.110	0.155	2.80	3.93
J	0.018	0.025	0.46	0.64
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.020	0.055	0.508	1.39
T	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
V	0.045	---	1.15	---
Z	---	0.080	---	2.04

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