

勝特力材料 886-3-5753170
 勝特力电子(上海) 86-21-54151736
 勝特力电子(深圳) 86-755-83298787
[Http://www.100y.com.tw](http://www.100y.com.tw)

MMBT2907ALT1

General Purpose Transistors

PNP Silicon

Features

- Pb-Free Packages are Available



ON Semiconductor®

<http://onsemi.com>

MAXIMUM RATINGS

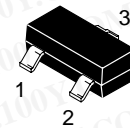
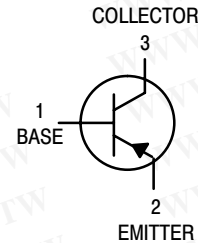
Rating	Symbol	2907A	Unit
Collector–Emitter Voltage	V_{CEO}	–60	Vdc
Collector–Base Voltage	V_{CBO}	–60	Vdc
Emitter–Base Voltage	V_{EBO}	–5.0	Vdc
Collector Current – Continuous	I_C	–600	mAdc

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

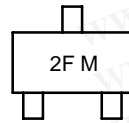
Characteristic	Symbol	Max	Unit
Total Device Dissipation FR–5 Board (Note 1) $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	225 1.8	mW mW/ $^\circ\text{C}$
Thermal Resistance, Junction–to–Ambient	$R_{\theta JA}$	556	$^\circ\text{C}/\text{W}$
Total Device Dissipation Alumina Substrate, (Note 2) $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	300 2.4	mW mW/ $^\circ\text{C}$
Thermal Resistance, Junction–to–Ambient	$R_{\theta JA}$	417	$^\circ\text{C}/\text{W}$
Junction and Storage Temperature	T_J, T_{stg}	–55 to +150	$^\circ\text{C}$

- FR–5 = $1.0 \times 0.75 \times 0.062$ in.
- Alumina = $0.4 \times 0.3 \times 0.024$ in. 99.5% alumina.



SOT–23 (TO–236AB)
 CASE 318
 STYLE 6

MARKING DIAGRAM



2F = Device Code
 M = Month Code

ORDERING INFORMATION

Device	Package	Shipping†
MMBT2907ALT1	SOT–23	3000 Units/Reel
MMBT2907ALT1G	SOT–23 (Pb–Free)	3000 Units/Reel
MMBT2907ALT3	SOT–23	3000 Units/Reel
MMBT2907ALT3G	SOT–23 (Pb–Free)	3000 Units/Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector–Emitter Breakdown Voltage (Note 3) (I _C = -10 mAdc, I _B = 0)	V _{(BR)CEO}	-60	-	Vdc
Collector–Base Breakdown Voltage (I _C = -10 μAdc, I _E = 0)	V _{(BR)CBO}	-60	-	Vdc
Emitter–Base Breakdown Voltage (I _E = -10 μAdc, I _C = 0)	V _{(BR)EBO}	-5.0	-	Vdc
Collector Cutoff Current (V _{CE} = -30 Vdc, V _{EB(off)} = -0.5 Vdc)	I _{CEX}	-	-50	nAdc
Collector Cutoff Current (V _{CB} = -50 Vdc, I _E = 0) (V _{CB} = -50 Vdc, I _E = 0, T _A = 125°C)	I _{CBO}	-	-0.010 -10	μAdc
Base Cutoff Current (V _{CE} = -30 Vdc, V _{EB(off)} = -0.5 Vdc)	I _{BL}	-	-50	nAdc

ON CHARACTERISTICS

DC Current Gain (I _C = -0.1 mAdc, V _{CE} = -10 Vdc) (I _C = -1.0 mAdc, V _{CE} = -10 Vdc) (I _C = -10 mAdc, V _{CE} = -10 Vdc) (I _C = -150 mAdc, V _{CE} = -10 Vdc) (I _C = -500 mAdc, V _{CE} = -10 Vdc) (Note 3)	h _{FE}	75 100 100 100 50	- - - 300 -	-
Collector–Emitter Saturation Voltage (Note 3) (I _C = -150 mAdc, I _B = -15 mAdc) (Note 3) (I _C = -500 mAdc, I _B = -50 mAdc)	V _{CE(sat)}	- -	-0.4 -1.6	Vdc
Base–Emitter Saturation Voltage (Note 3) (I _C = -150 mAdc, I _B = -15 mAdc) (I _C = -500 mAdc, I _B = -50 mAdc)	V _{BE(sat)}	- -	-1.3 -2.6	Vdc

SMALL-SIGNAL CHARACTERISTICS

Current–Gain – Bandwidth Product (Notes 3, 4) (I _C = -50 mAdc, V _{CE} = -20 Vdc, f = 100 MHz)	f _T	200	-	MHz
Output Capacitance (V _{CB} = -10 Vdc, I _E = 0, f = 1.0 MHz)	C _{obo}	-	8.0	pF
Input Capacitance (V _{EB} = -2.0 Vdc, I _C = 0, f = 1.0 MHz)	C _{ibo}	-	30	

SWITCHING CHARACTERISTICS

Turn–On Time	(V _{CC} = -30 Vdc, I _C = -150 mAdc, I _{B1} = -15 mAdc)	t _{on}	-	45	ns
Delay Time		t _d	-	10	
Rise Time		t _r	-	40	
Turn–Off Time	(V _{CC} = -6.0 Vdc, I _C = -150 mAdc, I _{B1} = I _{B2} = -15 mAdc)	t _{off}	-	100	
Storage Time		t _s	-	80	
Fall Time		t _f	-	30	

3. Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2.0%.

4. f_T is defined as the frequency at which |h_{fe}| extrapolates to unity.

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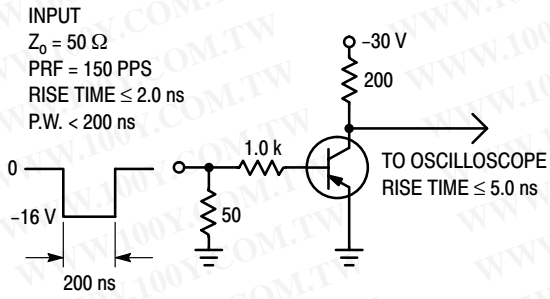


Figure 1. Delay and Rise Time Test Circuit

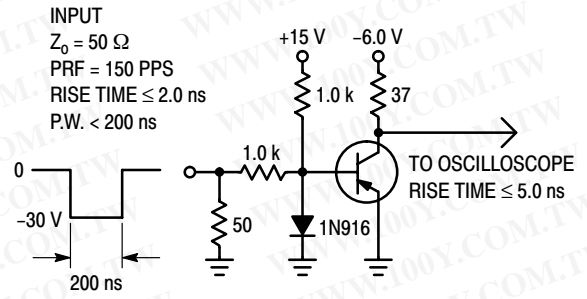


Figure 2. Storage and Fall Time Test Circuit

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TYPICAL CHARACTERISTICS

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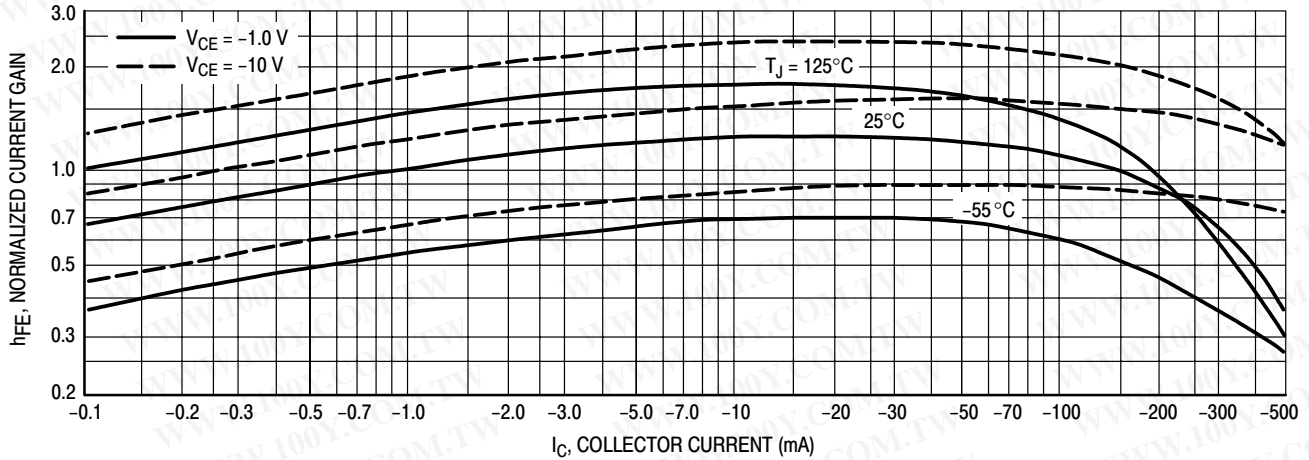


Figure 3. DC Current Gain

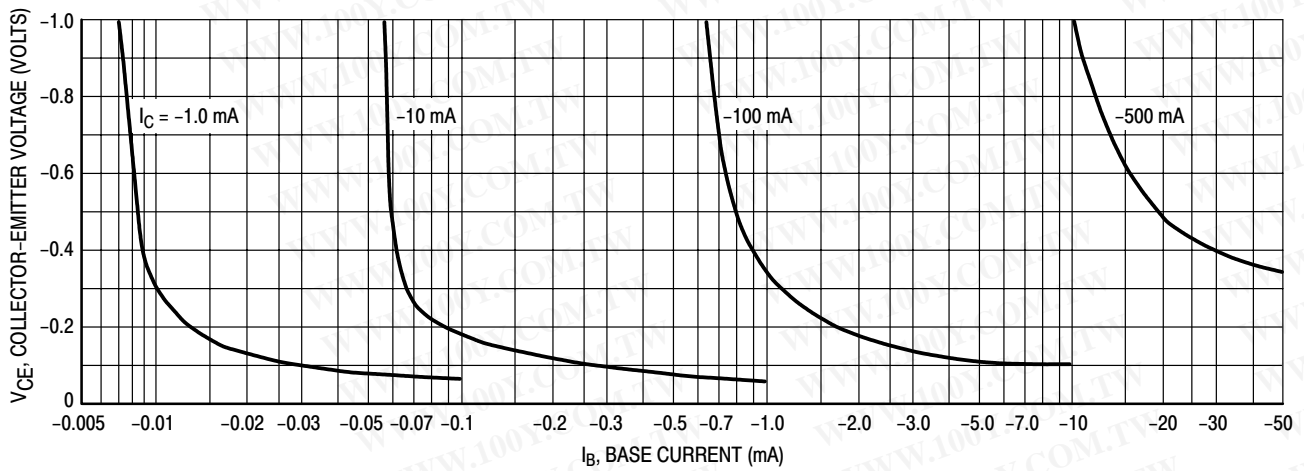


Figure 4. Collector Saturation Region

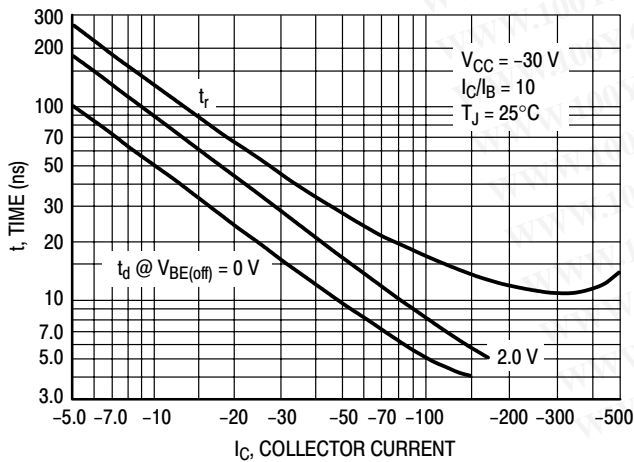


Figure 5. Turn-On Time

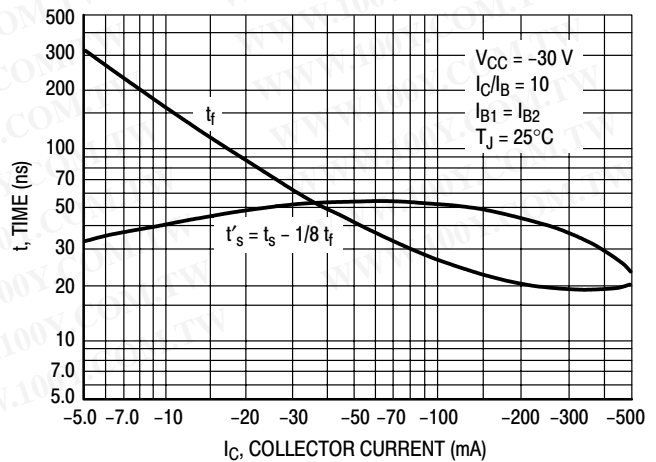


Figure 6. Turn-Off Time

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TYPICAL SMALL-SIGNAL Characteristics NOISE FIGURE

$V_{CE} = 10 \text{ Vdc}$, $T_A = 25^\circ\text{C}$

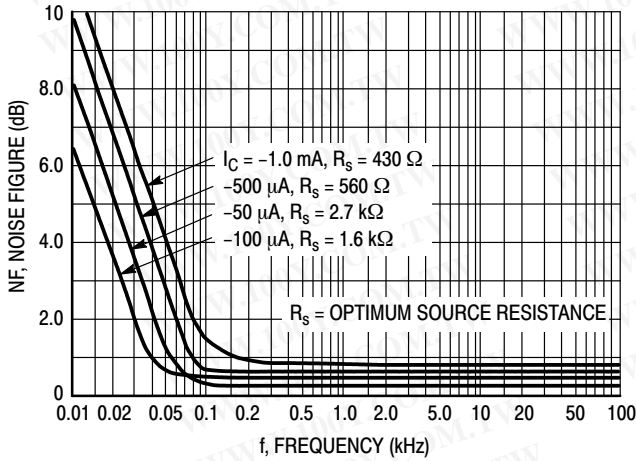


Figure 7. Frequency Effects

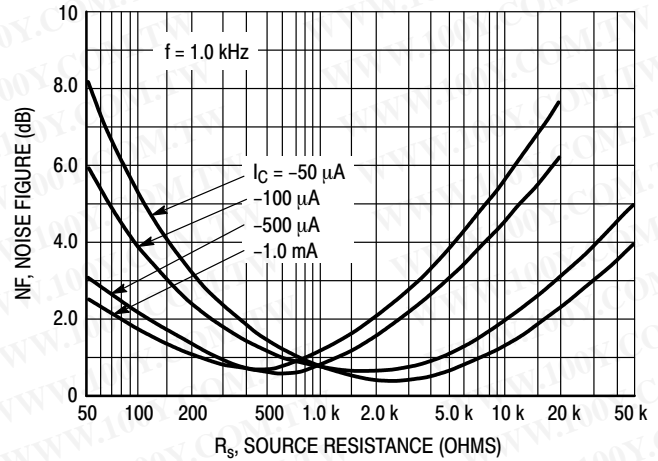


Figure 8. Source Resistance Effects

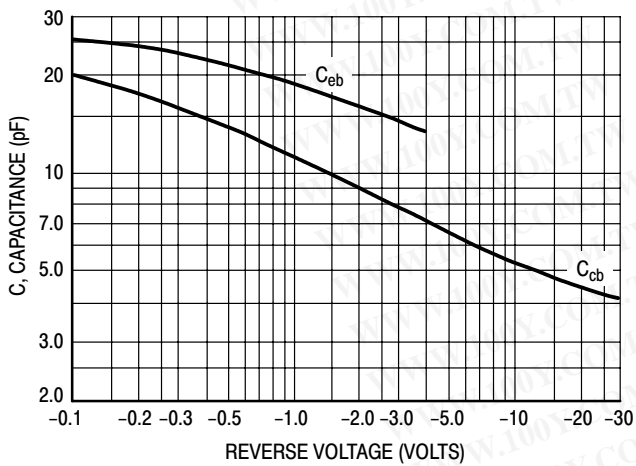


Figure 9. Capacitances

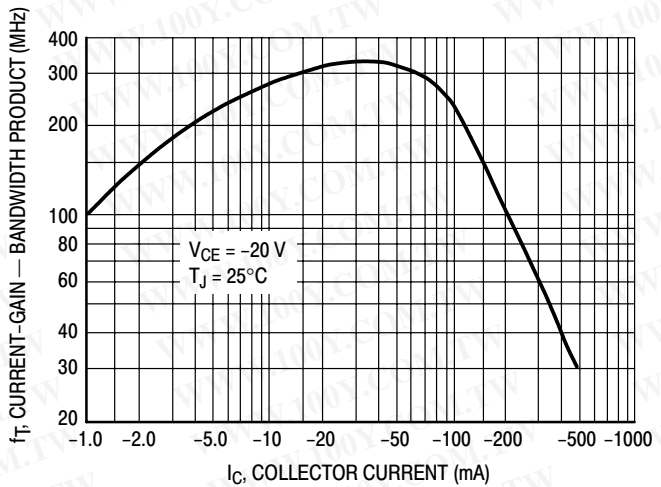


Figure 10. Current-Gain - Bandwidth Product

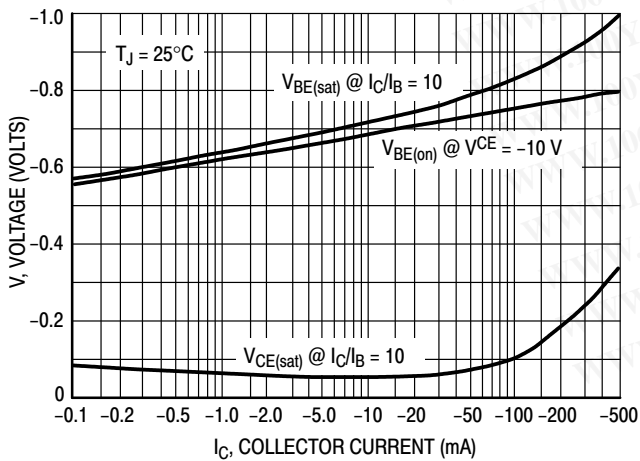


Figure 11. "On" Voltage

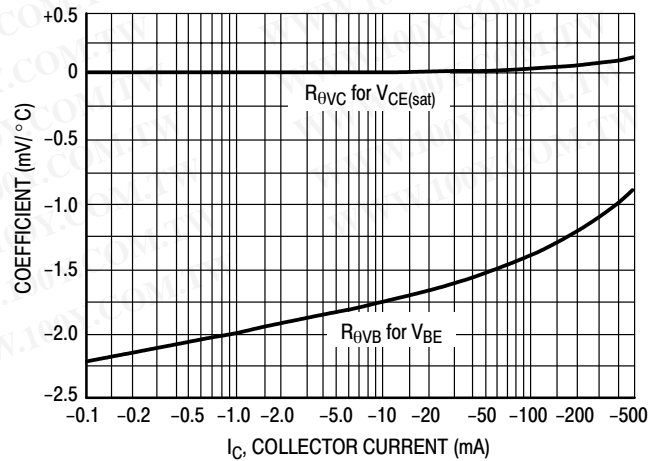


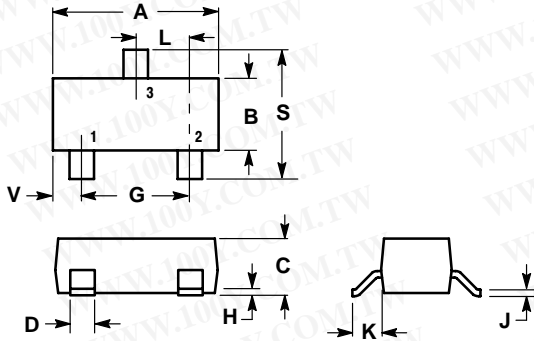
Figure 12. Temperature Coefficients

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PACKAGE DIMENSIONS

SOT-23 (TO-236AB)
CASE 318-08
ISSUE AK

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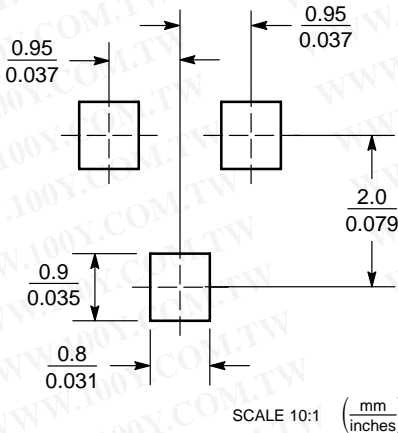


- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
 4. 318-01 THRU -07 AND -09 OBSOLETE, NEW STANDARD 318-08.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.1102	0.1197	2.80	3.04
B	0.0472	0.0551	1.20	1.40
C	0.0350	0.0440	0.89	1.11
D	0.0150	0.0200	0.37	0.50
G	0.0701	0.0807	1.78	2.04
H	0.0005	0.0040	0.013	0.100
J	0.0034	0.0070	0.085	0.177
K	0.0140	0.0285	0.35	0.69
L	0.0350	0.0401	0.89	1.02
S	0.0830	0.1039	2.10	2.64
V	0.0177	0.0236	0.45	0.60

- STYLE 6:
PIN 1. BASE
2. EMITTER
3. COLLECTOR

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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