SWITCHMODE™ Power Rectifiers

Ultrafast "E" Series with High Reverse Energy Capability

These state-of-the-art devices are designed for use in switching power supplies, inverters and as free wheeling diodes.

Features

- 20 mJ Avalanche Energy Guaranteed
- Excellent Protection Against Voltage Transients in Switching Inductive Load Circuits
- Ultrafast 75 Nanosecond Recovery Time
- 175°C Operating Junction Temperature
- Low Forward Voltage
- Low Leakage Current
- High Temperature Glass Passivated Junction
- Reverse Voltage to 1000 V
- These are Pb-Free Devices*

Mechanical Characteristics:

- Case: Epoxy, Molded
- Weight: 1.1 Gram (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Shipped in Plastic Bags, 5,000 per Bag
- Available Tape and Reel, 1,500 per Reel, by Adding a "RL" Suffix to the Part Number
- Polarity: Cathode indicated by Polarity Band

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage MUR480E MUR4100E	V _{RRM} V _{RWM} V _R	800 1000	
Average Rectified Forward Current (Square Wave; Mounting Method #3 Per Note 2)	I _{F(AV)}	4.0 @ T _A = 35°C	A
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I _{FSM}	70	AC
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-65 to +175	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



ON Semiconductor®

http://onsemi.com

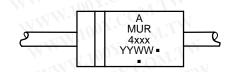
ULTRAFAST RECTIFIER 4.0 AMPERES, 800–1000 VOLTS





AXIAL LEAD CASE 267 STYLE 1

MARKING DIAGRAM



A = Assembly Location

MUR4xxx = Device Number (see page 2)

YY = Year
WW = Work Week
Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

勝 特 力 材 料 886-3-5753170 胜特力电子(上海) 86-21-54151736 胜特力电子(深圳) 86-755-83298787 Http://www.100y.com.tw

WWW.100Y.COM.TW THERMAL CHARACTERISTICS

MUR480E, MUR41	00E		
THERMAL CHARACTERISTICS			
Rating	Symbol	Value	Unit
Maximum Thermal Resistance, Junction-to-Ambient	$R_{ heta JA}$	See Note 2	°C/W

ELECTRICAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Maximum Instantaneous Forward Voltage (Note 1) $(i_F = 3.0 \text{ Amps}, T_J = 150^{\circ}\text{C})$ $(i_F = 3.0 \text{ Amps}, T_J = 25^{\circ}\text{C})$ $(i_F = 4.0 \text{ Amps}, T_J = 25^{\circ}\text{C})$	V _F	1.53 1.75 1.85	COM
Maximum Instantaneous Reverse Current (Note 1) (Rated dc Voltage, T_J = 150°C) (Rated dc Voltage, T_J = 25°C)	iR COLTIN	900 25	μΑ
Maximum Reverse Recovery Time ($I_F = 1.0$ Amp, di/dt = 50 Amp/ μ s) ($I_F = 0.5$ Amp, $I_R = 1.0$ Amp, $I_{REC} = 0.25$ Amp)	100X CC M t _{rr}	100 75	ns
Maximum Forward Recovery Time (I _F = 1.0 Amp, di/dt = 100 Amp/μs, Recovery to 1.0 V)	V. 100 tfr	75	ns
Controlled Avalanche Energy (See Test Circuit in Figure 6)	W _{AVAL}	20	mJ

^{1.} Pulse Test: Pulse Width = 300 μ s, Duty Cycle \leq 2.0%. WWW.100Y.COM.TV

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ORDERING INFORMATION

		WWW.	胜特力电子(深圳) 86-755-83298787 Http://www.100y.com.tw
DRDERING INFORMATION Device	Marking	Package	Shipping [†]
MUR420E	WW. 100Y.COM	Axial Lead*	500 Units / Bulk
MUR420EG	MANN TO ON COMP.	Axial Lead*	500 Units / Bulk
MUR420ERL	MUR420E	Axial Lead*	1500 / Tape & Reel
MUR420ERLG	TWW.100 TCON	Axial Lead*	1500 / Tape & Reel
MUR420ES	TOWN TOO CON	Axial Lead*	500 Units / Bulk
MUR420ESG	MUR420ES	Axial Lead*	500 Units / Bulk
MUR4100E	WW. 1007.0	Axial Lead*	500 Units / Bulk
MUR4100EG	MUDAAOOF	Axial Lead*	500 Units / Bulk
MUR4100ERL	MUR4100E	Axial Lead*	1500 / Tape & Reel
MUR4100ERLG	WWW.IO	Axial Lead*	1500 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging WWW.100Y.COM. WWW.100Y.COM. Specifications Brochure, BRD8011/D.

^{*}This package is inherently Pb-Free.

MUR480E, MUR4100E

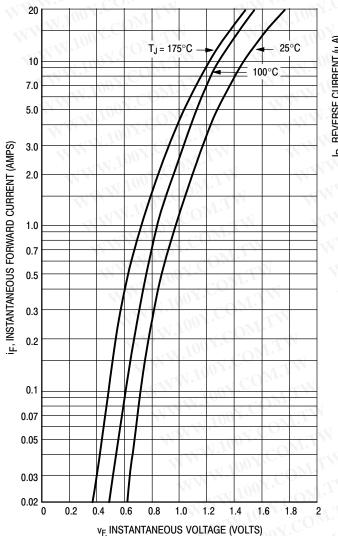


Figure 1. Typical Forward Voltage

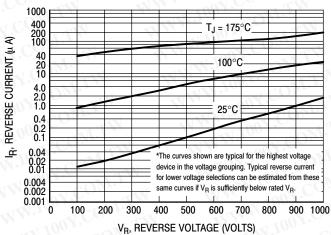


Figure 2. Typical Reverse Current*

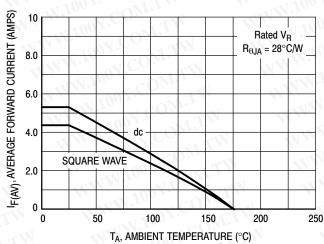


Figure 3. Current Derating (Mounting Method #3 Per Note 2)

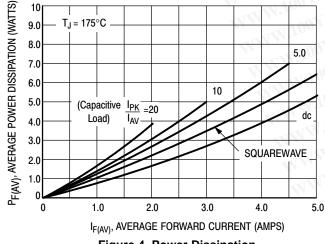


Figure 4. Power Dissipation

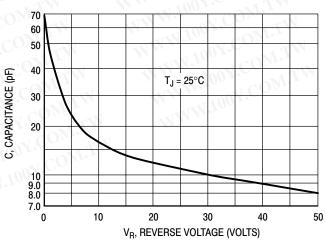


Figure 5. Typical Capacitance

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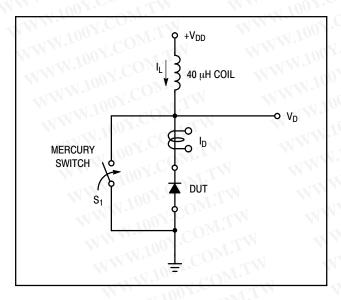


Figure 6. Test Circuit

The unclamped inductive switching circuit shown in Figure 6 was used to demonstrate the controlled avalanche capability of the new "E" series Ultrafast rectifiers. A mercury switch was used instead of an electronic switch to simulate a noisy environment when the switch was being opened.

When S_1 is closed at t_0 the current in the inductor I_L ramps up linearly; and energy is stored in the coil. At t_1 the switch is opened and the voltage across the diode under test begins to rise rapidly, due to di/dt effects, when this induced voltage reaches the breakdown voltage of the diode, it is clamped at BV_{DUT} and the diode begins to conduct the full load current which now starts to decay linearly through the diode, and goes to zero at t_2 .

By solving the loop equation at the point in time when S_1 is opened; and calculating the energy that is transferred to the diode it can be shown that the total energy transferred is equal to the energy stored in the inductor plus a finite amount of energy from the V_{DD} power supply while the diode is in breakdown (from t_1 to t_2) minus any losses due to finite

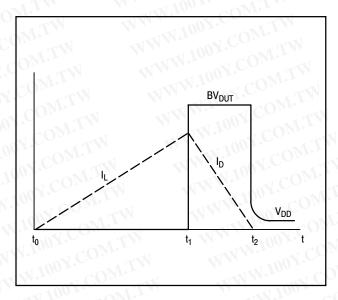


Figure 7. Current-Voltage Waveforms

component resistances. Assuming the component resistive elements are small Equation (1) approximates the total energy transferred to the diode. It can be seen from this equation that if the V_{DD} voltage is low compared to the breakdown voltage of the device, the amount of energy contributed by the supply during breakdown is small and the total energy can be assumed to be nearly equal to the energy stored in the coil during the time when S_1 was closed, Equation (2).

The oscilloscope picture in Figure 8, shows the information obtained for the MUR8100E (similar die construction as the MUR4100E Series) in this test circuit conducting a peak current of one ampere at a breakdown voltage of 1300 V, and using Equation (2) the energy absorbed by the MUR8100E is approximately 20 mjoules.

Although it is not recommended to design for this condition, the new "E" series provides added protection against those unforeseen transient viruses that can produce unexplained random failures in unfriendly environments.

EQUATION (1):

$$W_{AVAL} \approx \frac{1}{2} LI_{LPK}^2 \left(\frac{BV_{DUT}}{BV_{DUT}^{-V}_{DD}} \right)$$

EQUATION (2):

$$W_{AVAL} \approx \frac{1}{2}LI_{LPK}^2$$

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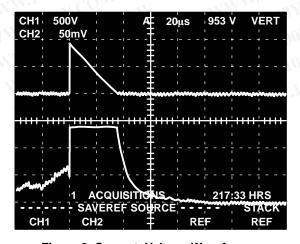


Figure 8. Current-Voltage Waveforms

CHANNEL 2: I_L 0.5 AMPS/DIV

> CHANNEL 1: V_{DUT} 500 VOLTS/DIV.

TIME BASE: 20 μs/DIV.

NOTE 2 – AMBIENT MOUNTING DATA

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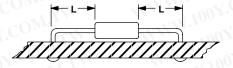
Data shown for thermal resistance junction—to—ambient $(R_{\theta JA})$ for the mountings shown is to be used as typical guideline values for preliminary engineering or in case the tie point temperature cannot be measured.

TYPICAL VALUES FOR $R_{\theta JA}$ IN STILL AIR

Moui	nting	Lea	d Leng	th, L (I	N)	«I
Met	hod	1/8	1/4	1/2	3/4	Units
1/1	41	50	51	53	55	°C/W
2	$R_{\theta JA}$	58	59	61	63	°C/W
3	1	M. A.	1 (2	28	140	°C/W

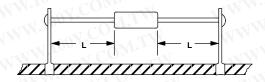
MOUNTING METHOD 1

P.C. Board Where Available Copper Surface area is small.



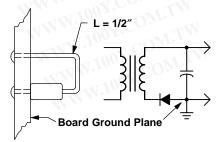
MOUNTING METHOD 2

Vector Push-In Terminals T-28



MOUNTING METHOD 3

P.C. Board with 1–1/2" x 1–1/2" Copper Surface



PACKAGE DIMENSIONS

AXIAL LEAD CASE 267-05 (DO-201AD) **ISSUE G**

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NOTES

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH.

F .	INC	HES	MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.287	0.374	7.30	9.50
В	0.189	0.209	4.80	5.30
D	0.047	0.051	1.20	1.30
K	1.000		25.40	
STYLE	1:			
	1. CATH		LARITY B	AND)
	2. ANOD	ÞΕ		

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