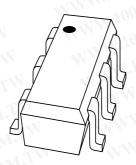
DISCRETE SEMICONDUCTORS

DATA SHEET



勝 特 力 材 料 886-3-5753170 胜特力电子(上海) 86-21-54151736 胜特力电子(深圳) 86-755-83298787 Http://www.100y.com.tw

PUMT1 PNP general purpose double transistor

Product specification Supersedes data of 1999 Apr 14

2001 Dec 19





Philips Semiconductors Product specification

PNP general purpose double transistor

PUMT1

FEATURES

- Low current (max. 100 mA)
- Low voltage (max. 40 V)
- · Reduces number of components and boardspace.

APPLICATIONS

General purpose switching and amplification.

DESCRIPTION

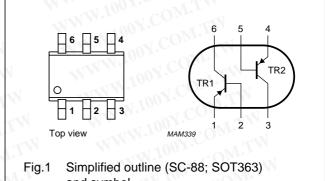
Two independently operating PNP transistors in an SC-88; SOT363 plastic package. NPN complement: PUMX1.

MARKING

TYPE NUMBER	MARKING CODE
PUMT1	FtF _W 100

PINNING

PIN	PIN DESCRIPTION		
1, 4	emitter	TR1; TR2	
2, 5	base	TR1; TR2	
3, 6	collector	TR2; TR1	



and symbol.

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT		
Per transis	tor	WWW.	MMM. Inn COM.				
V _{CBO}	collector-base voltage	open emitter	- WW	-50	V		
V _{CEO}	collector-emitter voltage	open base	7	-40	V		
V _{EBO}	emitter-base voltage	open collector	7/1/1	-5	V		
I _C	collector current (DC)	WWW. CONT.CO. TW	- WW	-100	mA		
I _{CM}	peak collector current	WWW. TOOY. CONT.	- 71	-200	mA		
I _{BM}	peak base current	TNN TOO COM.	- XX	-200	mA		
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	_	200	mW		
T _{stg}	storage temperature	WW 1001.	-65	+150	°C		
Tj	junction temperature	WW. 1007.00	III -	150	°C		
T _{amb}	operating ambient temperature	MAN M. TOON COM	–65	+150	°C		
Per device	MANN TOO COMP.	M.M.M. Took'CO	TW	WW	100		
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	Wr.	300	mW		

Note

1. Device mounted on an FR4 printed-circuit board.

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THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
Per device	WWW.100X.CO.M.TW	WW.100Y.COM	TW	
R _{th j-a}	thermal resistance from junction to ambient	note 1	416	K/W

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Per transis	tor	OO TOM IT WWW.100	COJ	M. I	
I _{CBO} collector cut-off current		$I_E = 0; V_{CB} = -30 \text{ V}$	CC	-100	nA
		$I_E = 0$; $V_{CB} = -30 \text{ V}$; $T_j = 150 ^{\circ}\text{C}$	M. J.	-10	μΑ
I _{EBO}	emitter cut-off current	$I_{C} = 0; V_{EB} = -4 V$	101.C	-100	nA
h _{FE}	DC current gain	$I_C = -1 \text{ mA}; V_{CE} = -6 \text{ V}$	120	75,71	N
V _{CEsat}	collector-emitter saturation voltage	$I_C = -50 \text{ mA}$; $I_B = -5 \text{ mA}$; note 1	N.100Y	-200	mV
C _c	collector capacitance	$I_E = i_e = 0$; $V_{CB} = -12 \text{ V}$; $f = 1 \text{ MHz}$	-XV.100	2.2	pF
f _T	transition frequency	$I_C = -2 \text{ mA}$; $V_{CE} = -12 \text{ V}$; $f = 100 \text{ MHz}$	100	07.00	MHz

1. Pulse test: $t_p \le 300 \ \mu s; \ \delta \le 0.02$. WWW.100Y.COM

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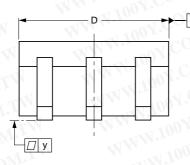
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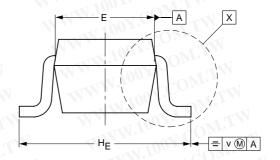
PACKAGE OUTLINE

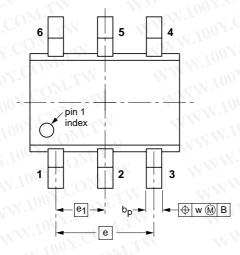
Plastic surface mounted package; 6 leads

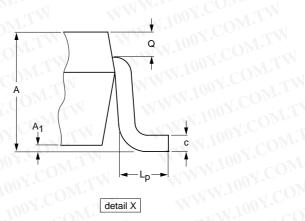
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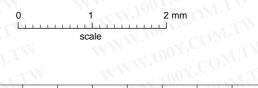
SOT363











DIMENSIONS (mm are the original dimensions)

UNIT	Α	A ₁ max	bp	C	Ď.C	E	e	e ₁	HE	Lp	Q	O	w	Уу
mm	1.1 0.8	0.1	0.30 0.20	0.25 0.10	2.2 1.8	1.35 1.15	1.3	0.65	2.2 2.0	0.45 0.15	0.25 0.15	0.2	0.2	0.1

OUTLINE		REFER	RENCES	EUROPEAN	ICCUE DATE	
VERSION	IEC	JEDEC	EIAJ	INW.Ing	PROJECTION	ISSUE DATE
SOT363	WW	1W.100Y.	SC-88	M. 10		97-02-28

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PNP general purpose double transistor

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DATA SHEET STATUS

DATA SHEET STATUS(1)	PRODUCT STATUS ⁽²⁾	DEFINITIONS				
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.				
Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product.				
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Notes

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- 2. The product status of the device(s) described in this data sheet may have changed since this data sheet was published. The latest information is available on the Internet at URL http://www.semiconductors.philips.com.

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Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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