Composite Transistors

XN04211 (XN4211)

Silicon NPN epitaxial planer transistor

For switching/digital circuits

Features

- Two elements incorporated into one package. • (Transistors with built-in resistor)
- Reduction of the mounting area and assembly cost by one half.

Basic Part Number of Element

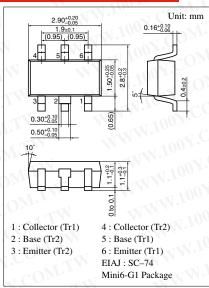
UNR1211(UN1211) \times 2 elements

Parameter		Symbol	Ratings	Unit	
Rating of element	Collector to base voltage	V _{CBO}	50	V	
	Collector to emitter voltage	V _{CEO}	50	v	
	Collector current	I _C	100	mA	
Overall	Total power dissipation	P _T	300	mW	
	Junction temperature	T _j	150	°C	
	Storage temperature	T _{stg}	-55 to +150	°C	

Absolute Maximum Ratings

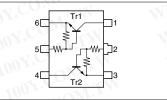


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Marking Symbol: 9V

Internal Connection



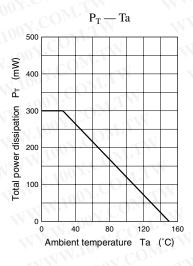
Electrical Characteristics (Ta=25°C)

Parameter	Symbol	Conditions	min	typ	max	Unit
Collector to base voltage	V _{CBO}	$I_{\rm C} = 10 \mu A, I_{\rm E} = 0$	50	J.CO	NI.	V
Collector to emitter voltage	V _{CEO}	$I_C = 2mA, I_B = 0$	50		M.L.	V
Collector cutoff current	I _{CBO}	$V_{CB} = 50V, I_E = 0$	N 10	N.C.	0.1	μΑ
	I _{CEO}	$V_{CE} = 50V, I_B = 0$	N	NOY.C	0.5	μA
Emitter cutoff current	I _{EBO}	$V_{EB} = 6V, I_C = 0$. No	0.5	mA
Forward current transfer ratio	h _{FE}	$V_{CE} = 10V, I_C = 5mA$	35	100		
Collector to emitter saturation voltage	V _{CE(sat)}	$I_{\rm C} = 10 {\rm mA}, I_{\rm B} = 0.3 {\rm mA}$		0.09	0.25	V
Output voltage high level	V _{OH}	$V_{CC} = 5V, V_B = 0.5V, R_L = 1k\Omega$	4.9			V
Output voltage low level	V _{OL}	$V_{CC} = 5V, V_B = 2.5V, R_L = 1k\Omega$			0.2	V
Transition frequency	f _T	$V_{CB} = 10V, I_E = -2mA, f = 200MHz$		150		MHz
Input resistance	R ₁		-30%	10	+30%	kΩ
Resistance ratio	R ₁ /R ₂		0.8	1.0	1.2	

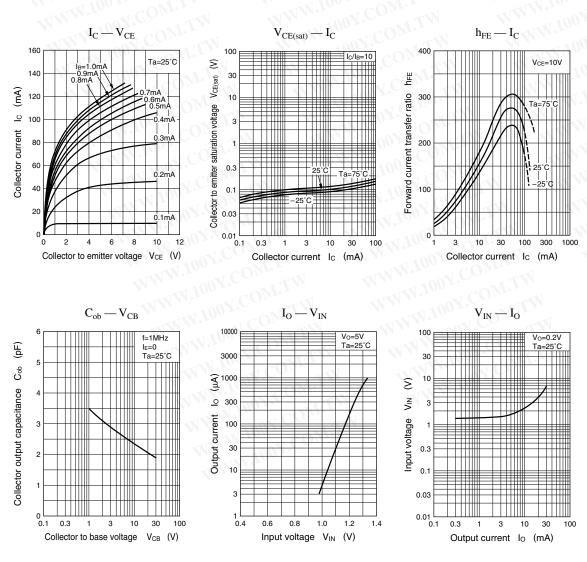
Note) The Part number in the Parenthesis shows conventional part number.

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XN04211



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