Transistors

DTC114EM / DTC114EE / DTC114EUA DTC114EKA / DTC114ESA

100mA / 50V Digital transistors (with built-in resistors) DTC114EM / DTC114EE / DTC114EUA / DTC114EKA / DTC114ESA

Applications

Inverter, Interface, Driver

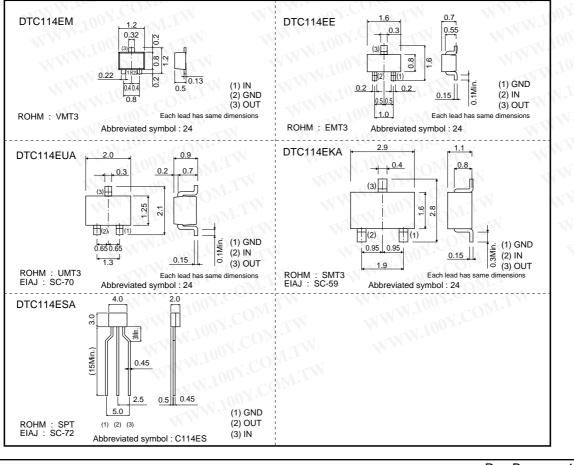
Features

- 1) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 3) Only the on/off conditions need to be set for operation, making the device design easy.

Structure

NPN epitaxial planar silicon transistor (Resistor built-in type)

•Dimensions (Unit : mm)



ROHM

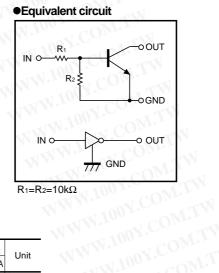
勝特力材料 886-3-5753170 胜特力电子(上海) 86-21-54151736 胜特力电子(深圳) 86-755-83298787 Http://www.100y.com.tw

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Packaging specifications

	Package	VMT3	EMT3	UMT3	SMT3	SPT
	Packaging type	Taping	Taping	Taping	Taping	Taping
	Code	T2L	TL	T106	T146	TP
Part No.	Basic ordering unit (pieces)	8000	3000	3000	3000	5000
DTC114EM		0	N - 2	A.CO.	Vr .	-
DTC114EE		-	0		Wr.	
DTC114EU	JA	- Ź	-71	00	T. in	- N
DTC114EK	ίΑ į		N 17.	OF.C	0	-17-
DTC114ES	A	_	- NET	<u> </u>	-0 <u>7</u> 7.	0



Parameter	Symbol		Limits	DNr.
T drameter	Gymbol	DTC114EM DTC114	EE DTC114EUA DTC114	EKA DTC114ESA
Supply voltage	Vcc	N N	50	
Input voltage	Vin		-10 to +40	CONL
0.1.1.1.0.0.1.	lo		50	AN.
Output current	IC(Max.)	W	100	1.00
Power dissipation	PD	150	200	300
Junction temperature	Ti	T.A.	150	CON
Storage temperature	Tstg	WT.	-55 to +150	NY.C

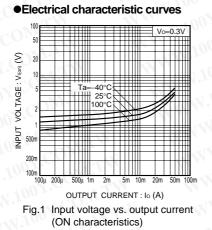
•Electrical characteristics (Ta=25°C)

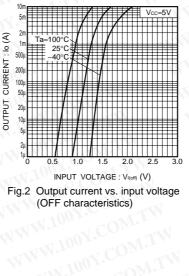
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
put voltage	VI(off)	1 <u></u>		0.5	v	Vcc=5V, Io=100µA	
iput voltage	VI(on)	3	- 1	-		Vo=0.3V, Io=10mA	
utput voltage	VO(on)		0.1	0.3	V	Io/II=10mA/0.5mA	
put current	A C	$0_{\overline{D}}$.		0.88	mA	VI=5V	
utput current	IO(off)	- - N		0.5	μA	Vcc=50V, Vi=0V	
C current gain	G	30		- 1		Vo=5V, Io=5mA	
nput resistance	R1	7	10	13	kΩ	WWW.	
esistance ratio	R2/R1	0.8	1.	1.2	-	M. Lus C	
ansition frequency	ft *	1-1	250	14.7	MHz	Vce=10V, Ie=-5mA, f=100MHz	

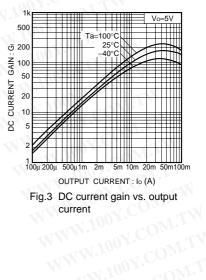
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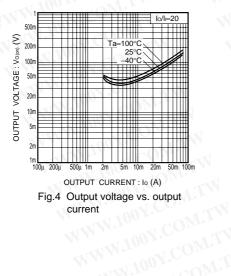
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Appendix

Notes

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