勝 特 力 材 料 886-3-5753170 胜特力电子(上海) 86-21-54151736 胜特力电子(深圳) 86-755-83298787 Http://www.100y.com.tw

SN54LS07, SN74LS07, SN74LS17 HEX BUFFERS/DRIVERS WITH OPEN-COLLECTOR HIGH-VOLTAGE OUTPUTS

SDLS021A, D3517, MAY 1990-REVISED AUGUST 1991

 Converts TTL-Voltage Levels to MOS Levels

- High Sink-Current Capability
- Input Clamping Diodes Simplify System Design
- Open-Collector Driver for Indicator Lamps and Relays
- Package Options Include "Small Outline" Packages, Ceramic Chip Carriers, and Standard and Ceramic 300-mil DIPs

description

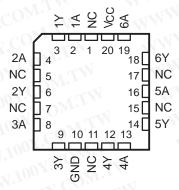
These monolithic hex buffers/drivers feature high-voltage open-collector outputs to interface with high-level circuits or for driving high-current loads. They are also characterized for use as buffers for driving TTL inputs. The 'LS07 has a rated output voltage of 30 V and the 'LS17 has a rated output voltage of 15 V. The maximum sink current is 30 mA for the SN54LS07 and 40 mA for the SN74LS07 and SN74LS17.

These circuits are compatible with most TTL families. Inputs are diode-clamped to minimize transmission-line effects, which simplifies design. Typical power dissipation is 140 mW and average propagation delay time is 12 ns.

SN54LS07 . . . J PACKAGE SN74LS07, SN74LS17 . . . D OR N PACKAGE (T0P VIEW)



SN54LS07 . . . FK PACKAGE (T0P VIEW)



NC - No internal connection

The SN54LS07 is characterized over the full military temperature range of –55°C to 125°C. The SN74LS07 and SN74LS17 are characterized for operation from 0°C to 70°C.

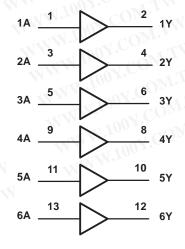
logic symbol†

| 1A 1 | \triangleright \Diamond | 2 1Y |
|--------------|-----------------------------|-------|
| 2A 3 | . * | 4 2Y |
| 3A 5 | | 6 3Y |
| 4A 9 | | 8 4Y |
| | | 10 5Y |
| 5A 11 | | |
| 6A <u>13</u> | | 12 6Y |

[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

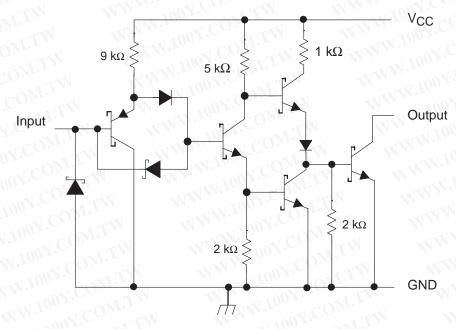
Pin numbers shown are for D, J, and N packages.

logic diagram (positive logic)



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schematic (each gate)



Resistor values shown are nominal.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

| Supply voltage, V _{CC} | P | 7 V |
|--|------------------------|----------------|
| Input voltage, V _I (see Note 1) | M | 5.5 V |
| Output voltage, VO (see Notes 1 and 2 | 2): SN54LS07, SN74LS07 | 30 V |
| WWW.I | SN74LS17 | |
| Operating free-air temperature range: | SN54LS07 | -55°C to 125°C |
| | SN54LS07, SN74LS17 | |
| Storage temperature range | 4.CO | -65°C to 150°C |

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTES: 1. Voltage values are with respect to network ground terminal.

recommended operating conditions

| | | MMM.1007.CO | MITW | SN54LS | 07 | | N74LS0 N74LS1 | 9 - | UNIT |
|-----|--------------------------------|-------------|-----------------------|--------------|-----|------|------------------|------|------|
| | | | MIN | NOM | MAX | MIN | NOM | MAX | |
| VCC | Supply voltage | 4, 100 | 4.5 | 5 | 5.5 | 4.75 | 5 | 5.25 | V |
| VIH | High-level input voltage | 11007: | 2 | // | 1// | 2 | 100 1 | | V |
| VIL | Low-level input voltage | WW. | Com | | 0.8 | MA | | 0.8 | V |
| | | 'LSO | $7 - 0^{M_{\bullet}}$ | - V T | 30 | | | 30 | |
| VOH | High-level output voltage | 'LS1 | 7 | 11. | | | | 15 | V |
| loL | Low-level output current | WWW | 17.00 | | 30 | | | 40 | mA |
| TA | Operating free-air temperature | TINN, 10 | -55 | | 125 | 0 | | 70 | °C |



^{2.} This is the maximum voltage that should be applied to any output when it is in the off state.

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electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

| PARAMETER | TEST CONDI | TIONS† | TW | SN54LS0 | 7 | | N74LS0 N74LS1 | ~7/1.1 | UNIT |
|-----------------|---|-------------------------------|--------|---------|------|------|------------------|--------|------|
| | OY.COMITY I | 1007. | MIN | TYP‡ | MAX | MIN | TYP‡ | MAX | 7.4 |
| VIK | $V_{CC} = MIN$, $I_I = -12 \text{ mA}$ | MALLONICO | TT | | -1.5 | WW T | 1007 | -1.5 | V |
| 1-00/10/ | V MIN W 2V | 'LS07, V _{OH} = 30 V | Mr. | XXI | 0.25 | MW | 201 | 0.25 | - A |
| ІОН | $V_{CC} = MIN, \qquad V_{IH} = 2 V$ | 'LS17, V _{OH} = 15 V | MI | | 0.25 | - V | 1.700. | 0.25 | mA |
| V | V MIN V- 0.0V | I _{OL} = 16 mA | - 3/1 | LW | 0.4 | MAG | -1100 | 0.4 | J.J |
| VOL | $V_{CC} = MIN, V_{IL} = 0.8 V$ | I _{OL} = MAX§ | Oh | TV | 0.7 | WW | 44. | 0.7 | DM V |
| II N | $V_{CC} = MAX$, $V_I = 7 V$ | W.100 | CON | | 1 | -78 | MIL | 1 | mA |
| IH III | $V_{CC} = MAX$, $V_I = 2.4 V$ | 11007 | | V.T.A. | 20 | M. | -xx1 1 | 20 | μΑ |
| I _{IL} | $V_{CC} = MAX$, $V_I = 0.4 V$ | WWW | I.CO | - 17 | -0.2 | W | M. | -0.2 | mA |
| ICCH | V _{CC} = MAX | TAN. Inc | -7 C.C | DINT. | 14 | | NW | 14 | mA |
| ICCL | V _{CC} = MAX | 710 | 27. | ow. | 45 | | -14 | 45 | mA |

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

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switching characteristics, $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$ (see Note 3)

| RAMETER | FROM (INPUT) | TO (OUTPUT) | TEST CONDITIONS | MIN | TYP | MAX | UNIT |
|--------------------|-----------------|------------------------------------|-----------------------|-----|-----|-----|------|
| tPLH | | . CON | B: = 110 0 C: = 15 pE | TV | 6 | 10 | má N |
| t _{PHL} A | M. I. | $R_L = 110 \Omega$, $C_L = 15 pF$ | - 1 | 19 | 30 | ns | |

NOTE 3: Load circuit and voltage waveforms are shown in Section 1 of TTL Logic Data Book, 1988.

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[‡] All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$.

^{\$} IOI = 30 mA for SN54 series parts and 40 mA for SN74 series parts.

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