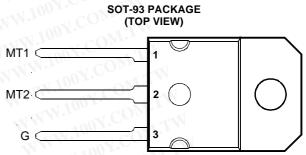
- High Current Triacs
- 25 A RMS
- Glass Passivated Wafer
- 400 V to 800 V Off-State Voltage
- 175 A Peak Current
- Max I<sub>GT</sub> of 50 mA (Quadrants 1 3)



Pin 2 is in electrical contact with the mounting base.

MDC2AD

# absolute maximum ratings over operating case temperature (unless otherwise noted)

RATING	W	SYMBOL	VALUE	UNIT
Repetitive peak off-state voltage (see Note 1)	TIC263D TIC263M TIC263S TIC263N	$V_{DRM}$	400 600 700 800	
Full-cycle RMS on-state current at (or below) 70°C case temperature (see Note 2)			25	Α
Peak on-state surge current full-sine-wave (see Note 3)			175	Α
Peak gate current			±1	Α
Operating case temperature range			-40 to +110	°C
Storage temperature range			-40 to +125	°C
Lead temperature 1.6 mm from case for 10 seconds			230	°C

- NOTES: 1. These values apply bidirectionally for any value of resistance between the gate and Main Terminal 1.
  - 2. This value applies for 50-Hz full-sine-wave operation with resistive load. Above 70°C derate linearly to 110°C case temperature at the rate of 625 mA/°C.
  - 3. This value applies for one 50-Hz full-sine-wave when the device is operating at (or below) the rated value of peak reverse voltage and on-state current. Surge may be repeated after the device has returned to original thermal equilibrium.

# electrical characteristics at 25°C case temperature (unless otherwise noted)

PARAMETER		TEST CONDITIONS			MIN	TYP	MAX	UNIT
I <sub>DRM</sub>	Repetitive peak off-state current	V <sub>D</sub> = Rated V <sub>DRM</sub>	I <sub>G</sub> = 0	T <sub>C</sub> = 110°C	W	NN.	±2	mA
I <sub>GTM</sub>	Peak gate trigger	V <sub>supply</sub> = +12 V†	$R_L = 10 \Omega$	t <sub>p(g)</sub> > 20 μs	10	7	50	COx
		V <sub>supply</sub> = +12 V†	$R_L = 10 \Omega$	t <sub>p(g)</sub> > 20 μs		-15	-50	- a01
	current	$V_{\text{supply}} = -12 \text{ V}\dagger$	$R_L = 10 \Omega$	t <sub>p(g)</sub> > 20 μs	4	-16	-50	mA
		$V_{\text{supply}} = -12 \text{ V}\dagger$	$R_L = 10 \Omega$	t <sub>p(g)</sub> > 20 μs		28	W	
V <sub>GTM</sub>	Peak gate trigger	V <sub>supply</sub> = +12 V†	$R_L = 10 \Omega$	t <sub>p(g)</sub> > 20 μs		0.7	2	-7 C
		V <sub>supply</sub> = +12 V†	$R_L = 10 \Omega$	$t_{p(g)} > 20 \mu s$		-0.7	-2	00 %
	voltage	V <sub>supply</sub> = -12 V†	$R_L = 10 \Omega$	t <sub>p(g)</sub> > 20 μs	V	-0.8	-2	· V
		$V_{\text{supply}} = -12 \text{ V}\dagger$	$R_L = 10 \Omega$	t <sub>p(g)</sub> > 20 μs	-1	0.8	2	
V <sub>TM</sub>	Peak on-state voltage	I <sub>TM</sub> = ±35.2 A	I <sub>G</sub> = 50 mA	(see Note 4)	A.	±1.5	±1.7	V
I <sub>H</sub>	Holding current	V <sub>supply</sub> = +12 V†	$I_G = 0$	Init' $I_{TM} = 100 \text{ mA}$		6	40	A
		V <sub>supply</sub> = -12 V†	$I_G = 0$	Init' $I_{TM} = -100 \text{ mA}$	7	-13	-40	mA

<sup>†</sup> All voltages are with respect to Main Terminal 1.

NOTE 4: This parameter must be measured using pulse techniques, t<sub>p</sub> = ≤ 1 ms, duty cycle ≤ 2 %. Voltage-sensing contacts separate from the current carrying contacts are located within 3.2 mm from the device body.



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### electrical characteristics at 25°C case temperature (unless otherwise noted) (continued)

	PARAMETER TEST CONDITIONS			NIM	TYP	MAX	UNIT	
Jr V	Latching current	$V_{\text{supply}} = +12 \text{ V}^{\dagger}$ $V_{\text{supply}} = -12 \text{ V}^{\dagger}$	(see Note 5)	W.100Y.COM.	W.	20 -20		mA
dv/dt	Critical rate of rise of off-state voltage	V <sub>D</sub> = Rated V <sub>D</sub>	I <sub>G</sub> = 0	T <sub>C</sub> = 110°C	LM	±450		V/µs
dv/dt <sub>(c)</sub>	Critical rise of commutation voltage	$V_D$ = Rated $V_D$ di/dt = 0.5 $I_{T(RMS)}$ /ms	CIW V	$T_{C} = 80^{\circ}C$ $I_{T} = 1.4 I_{T(RMS)}$	M.TV	±1		V/µs
di/dt	Critical rate of rise of on -state current	$V_D$ = Rated $V_D$ $di_G/dt = 50 \text{ mA/}\mu\text{s}$	I <sub>GT</sub> = 50 mA	T <sub>C</sub> = 110°C	$_{ m DM.T}$	±200		A/µs

<sup>†</sup> All voltages are with respect to Main Terminal 1.

NOTE 5: The triacs are triggered by a 15-V (open-circuit amplitude) pulse supplied by a generator with the following characteristics:  $R_G$  = 100  $\Omega$ ,  $t_{p(g)}$  = 20  $\mu s$ ,  $t_r$  =  $\leq$  15 ns, f = 1 kHz.

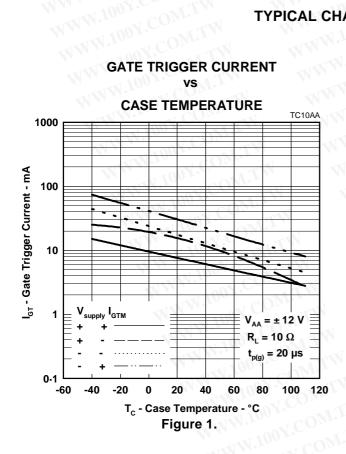
### thermal characteristics

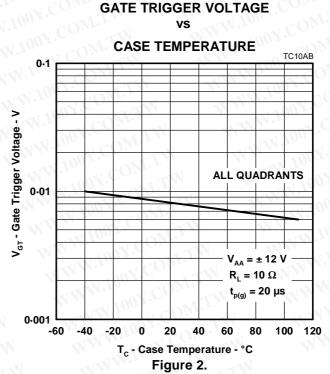
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W.Inc	PARAMETER	MIN	TYP	MAX	UNIT
$R_{\theta JC}$	Junction to case thermal resistance	100 -	$OM_{ij}$	1.52	°C/W
$R_{\theta JA}$	Junction to free air thermal resistance	11007.	- 11	36	°C/W

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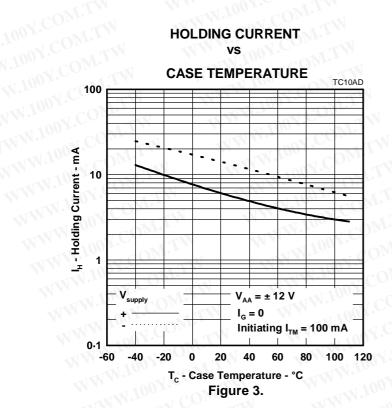


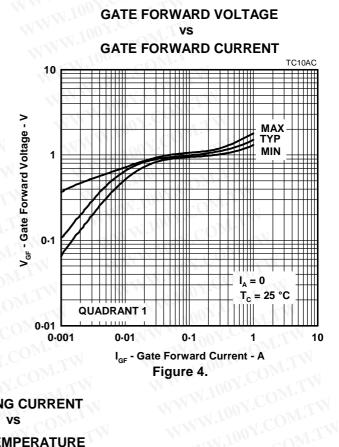


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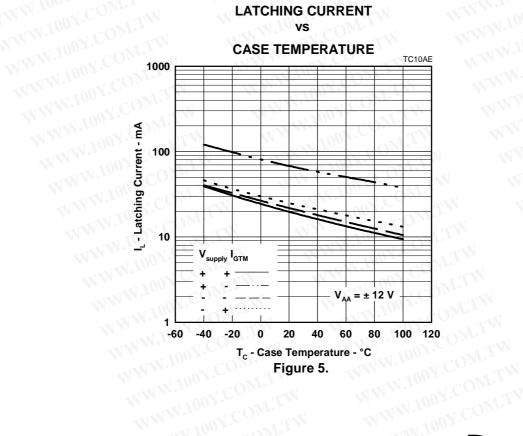
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### TYPICAL CHARACTERISTICS





# **LATCHING CURRENT**



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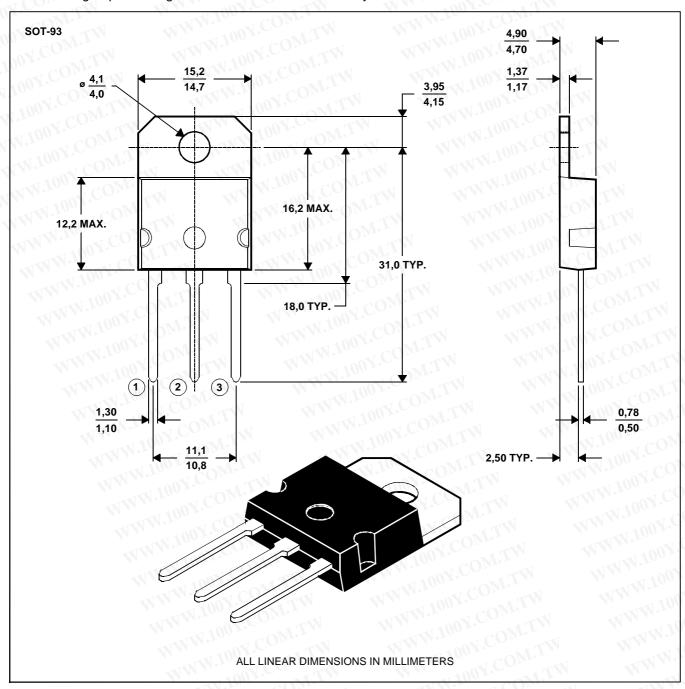
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### **MECHANICAL DATA**

### **SOT-93**

## 3-pin plastic flange-mount package

This single-in-line package consists of a circuit mounted on a lead frame and encapsulated within a plastic compound. The compound will withstand soldering temperature with no deformation, and circuit performance characteristics will remain stable when operated in high humidity conditions. Leads require no additional cleaning or processing when used in soldered assembly.



NOTE A: The centre pin is in electrical contact with the mounting tab.

**MDXXAW** 

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