TOSHIBA Photocoupler GaAs Ired & Photo-Thyristor

## **TLP541G,TLP542G**

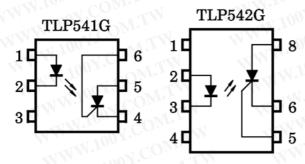
Programmable Controllers
AC-Output Module
Solid State Relay

The TOSHIBA TLP541G consists of a photo-thyristor optically coupled to a gallium arsenide infrared emitting diode in a six lead plastic DIP package.

The TOSHIBA TLP542G consists of a photo–thyristor optically coupled to a gallium arsenide infrared emitting diode in a seven lead plastic DIP package.

- Peak off-state voltage: 400 V (min.)
- Trigger LED current: 7 mA (max.)
- On-state current: 150 mA (max.)
- Isolation voltage: 2500 V<sub>rms</sub> (min.)
- UL recognized: UL1577, file no. E67349

## Pin Configuration (top view)



1: ANODE

2: CATHODE

3: N.C.

4: CATHODE

5: ANODE

6: GATE

1 : N.C.

2: ANODE

3: CATHODE

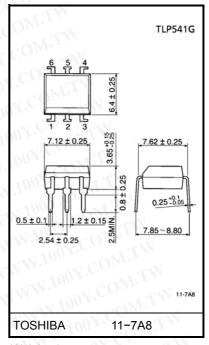
4 : N.C.

5: GATE

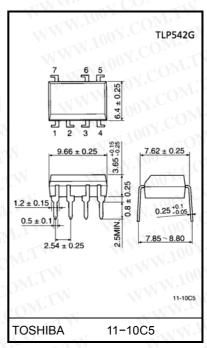
6: CATHODE

7: ANODE

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Weight: 0.4 g



Weight: 0.53 g



## Maximum Ratings (Ta = 25°C)

	Characteristic	Symbol	Rating	Unit
-7	Forward current	IF	70	mA
	Forward current derating (Ta ≥ 25°C)	ΔI <sub>F</sub> / °C	-0.7	mA / °C
LED	Peak forward current (100 µs pulse, 100 pps)	I <sub>FP</sub>	W.100 1	- A
	Reverse voltage	V <sub>R</sub>	5,00	VM
	Junction temperature	Tj	125	°C
TA-	Peak forward voltage (R <sub>GK</sub> = 27kΩ)	$V_{DRM}$	400	V
	Peak reverse voltage (R <sub>GK</sub> = 27kΩ)	$V_{RRM}$	400	OVV
C <sub>V</sub>	On-state current	I <sub>T</sub> (RMS)	150	mA
Detector	On–state current derating (Ta ≥ 25°C)	ΔI <sub>T</sub> / °C	-2.0	mA / °C
ă	Peak one cycle surge current	I <sub>TSM</sub>	2	Α
	Peak reverse gate voltage	$V_{GM}$	-5	V
17.0	Junction temperature	$OMT_j$	100	°C
Stora	ge temperature range	T <sub>stg</sub>	-55~125	°C
Opera	ating temperature range	T <sub>opr</sub>	-30~100	°C
Lead	soldering temperature (10 s)	T <sub>sol</sub>	260	°C
Isolati	on voltage (AC, 1 min., R.H. ≤ 60%) (Note)	BVs	2500	V <sub>rms</sub>

(Note) Device considered a two terminal device: LED side pins shorted together and detector side pins shorted WWW.100Y.COM.T together.

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Characteristic	Symbol	Min.	Typ.	Max.	Uni
Supply voltage	V <sub>AC</sub>	00¥.C		120	Va
Forward current	1F	10	16	25	m/
Operating temperature	T <sub>opr</sub>	-30	$C_{\mathbf{O}_{M}}$	85	°C
Gate to cathode resistance	R <sub>GK</sub>	1.100	27	33	kΩ
Gate to cathode capacity	C <sub>GK</sub>	W Jan	0.01	0.1	μΕ

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## **Individual Electrical Characteristics (Ta = 25°C)**

	Characteristic	Symbol	Test Cond	dition	Min.	Тур.	Max.	Unit
LED	Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 10 mA	COM.	1.0	1.15	1.3	V
	Reverse current	I <sub>R</sub>	V <sub>R</sub> = 5 V		- XX	_	10	μΑ
ŢŴ	Capacitance	C <sub>T</sub>	V = 0, f = 1 MHz	OM	1 7	30	_	pF
M.TV	Off-state current I <sub>DRM</sub> V	Y.C. M.T.W	V <sub>AK</sub> = 400 V	Ta = 25°C	$\Gamma_{IA}$	10	5000	nA
		$R_{GK} = 27 \text{ k}\Omega$	Ta = 100°C	N-TV	1	100	μA	
	N. WWW.	ON COM	V <sub>KA</sub> = 400 V	Ta = 25°C	7.7	10	5000	nA
Detector	Reverse current	Reverse current $I_{RRM}$ $R_{GK} = 27 \text{ k}\Omega$	Ta = 100°C	7 × 1	1	100	μA	
	On-state voltage	V <sub>TM</sub>	I <sub>TM</sub> = 100 mA		Oh	0.9	1.3	V
	Holding current	V. JUN JIM	R <sub>GK</sub> = 27 kΩ		$C_{\mathbf{O}_{N_i}}$	0.2	1	mA
	Off-state dv/dt	dv/dt	$V_{AK}$ = 280 V, $R_{GK}$ = 27 k $\Omega$		5	10	i –	V/µs
	0	W. 00	V = 0, f = 1 MHz A	Anode to gate	70	20	_	
	Capacitance C <sub>j</sub>	Gate to cathode			350	_	pF	

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# Coupled Characteristics (Ta = 25°C)

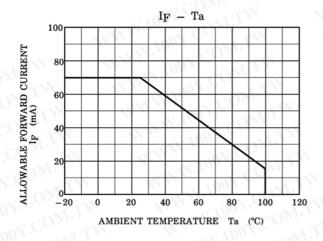
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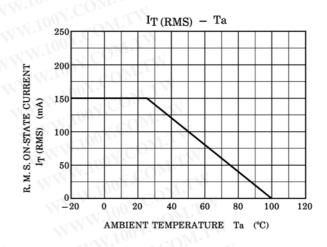
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Trigger LED current	VIFT	$V_{AK} = 6 \text{ V}, R_{GK} = 27 \text{ k}\Omega$	11	40	7	mA
Turn-on time	t <sub>on</sub>	$I_F$ = 50 mA, $R_{GK}$ = 27 kΩ	NA'L	10	$O_{Mr}$	μs
Capacitance (input to output)	Cs	V <sub>S</sub> = 0, f = 1 MHz	MAN.	0.8	$C\overline{O}_{NI}$	pF
Isolation resistance	R <sub>S</sub>	V <sub>S</sub> = 500 V, R.H. ≤ 60%	$M_{\overline{M},M}$	10 <sup>11</sup>	CO	Ω
Isolation voltage	BVS	AC, 1 minute	2500	1.5	N.CU	V <sub>rms</sub>

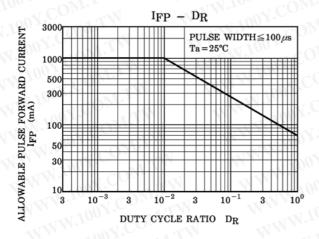
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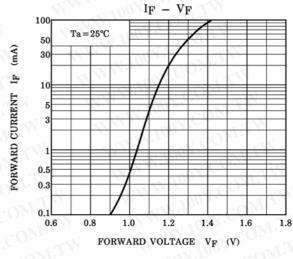
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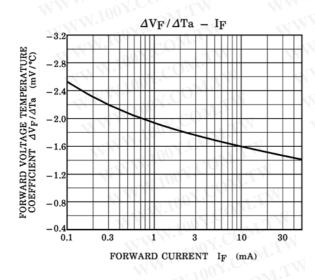
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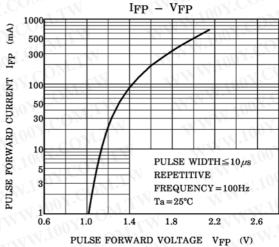






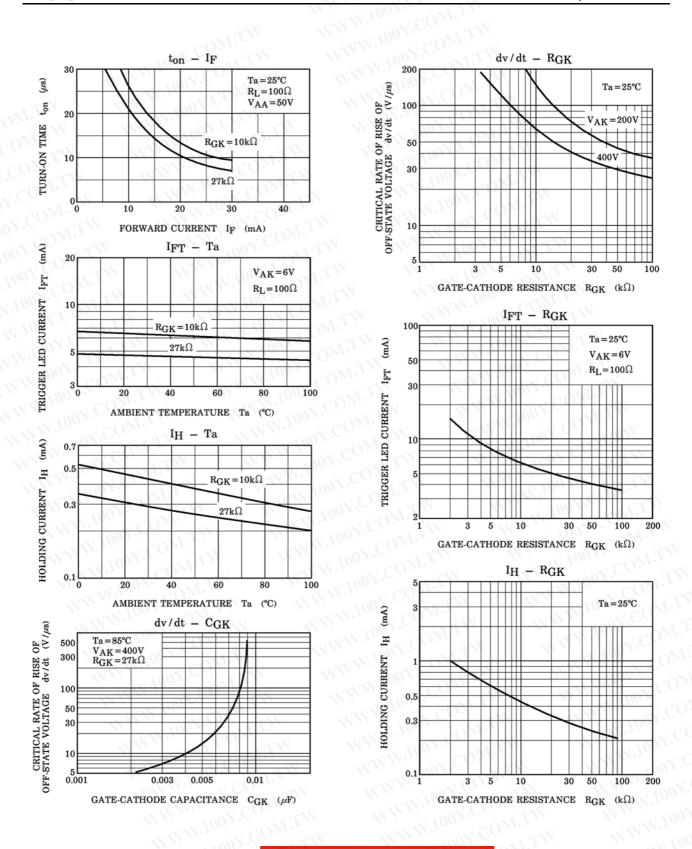






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