

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (U-MOSII)

**TPC6001**

Notebook PC Applications

Portable Equipment Applications

- Low drain-source ON resistance:  $R_{DS(ON)} = 22 \text{ m}\Omega$  (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 15 \text{ S}$  (typ.)
- Low leakage current:  $I_{DSS} = 10 \mu\text{A}$  (max) ( $V_{DS} = 20 \text{ V}$ )
- Enhancement mode:  $V_{th} = 0.5$  to  $1.2 \text{ V}$  ( $V_{DS} = 10 \text{ V}$ ,  $I_D = 200 \mu\text{A}$ )

**Maximum Ratings (Ta = 25°C)**

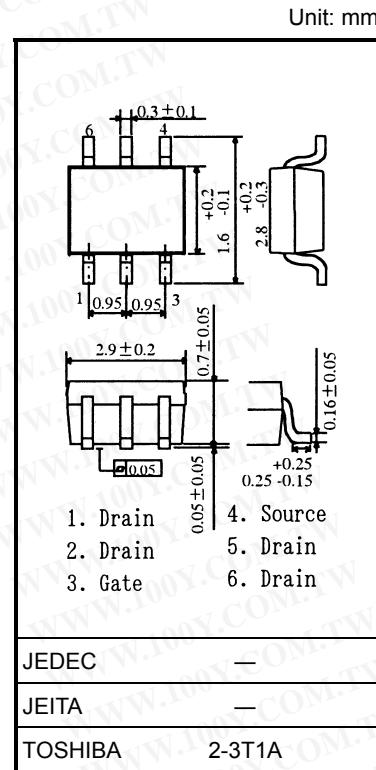
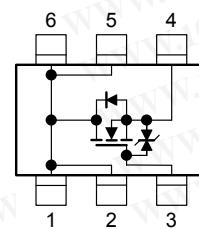
Characteristics		Symbol	Rating	Unit
Drain-source voltage		$V_{DSS}$	20	V
Drain-gate voltage ( $R_{GS} = 20 \text{ k}\Omega$ )		$V_{DGR}$	20	V
Gate-source voltage		$V_{GSS}$	$\pm 12$	V
Drain current	DC (Note 1)	$I_D$	6	A
	Pulse (Note 1)	$I_{DP}$	24	
Drain power dissipation (t = 5 s) (Note 2a)		$P_D$	2.2	W
Drain power dissipation (t = 5 s) (Note 2b)		$P_D$	0.7	W
Single pulse avalanche energy (Note 3)		$E_{AS}$	5.8	mJ
Avalanche current		$I_{AR}$	3	A
Repetitive avalanche energy (Note 4)		$E_{AR}$	0.22	mJ
Channel temperature		$T_{ch}$	150	°C
Storage temperature range		$T_{stg}$	-55 to 150	°C

**Thermal Characteristics**

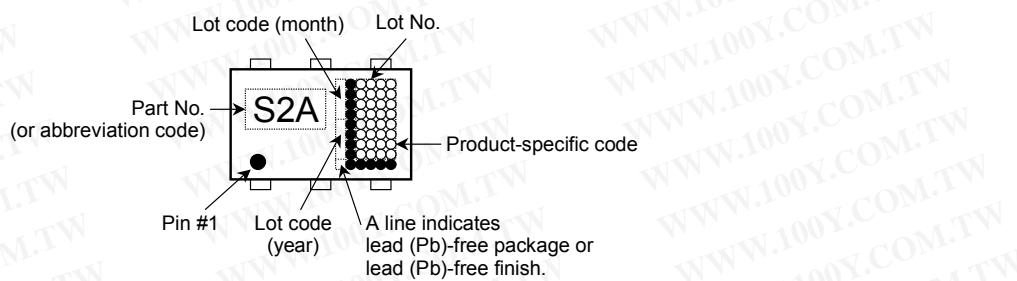
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient (t = 5 s)	$R_{th}(ch-a)$	56.8	°C/W
Thermal resistance, channel to ambient (t = 5 s)	$R_{th}(ch-a)$	178.5	°C/W

Note 1, Note 2, Note 3, Note 4 and Note 5: See the next page.

This transistor is an electrostatic-sensitive device. Please handle it with caution.

**Circuit Configuration**

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胜特力电子(上海) 86-21-54151736  
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**Marking (Note 5)****Electrical Characteristics ( $T_a = 25^\circ\text{C}$ )**

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current	$I_{GSS}$	$V_{GS} = \pm 10\text{ V}, V_{DS} = 0\text{ V}$	—	—	$\pm 10$	$\mu\text{A}$
Drain cut-OFF current	$I_{DSS}$	$V_{DS} = 20\text{ V}, V_{GS} = 0\text{ V}$	—	—	10	$\mu\text{A}$
Drain-source breakdown voltage	$V_{(\text{BR})\text{ DSS}}$	$I_D = 10\text{ mA}, V_{GS} = 0\text{ V}$	20	—	—	$\text{V}$
	$V_{(\text{BR})\text{ DSX}}$	$I_D = 10\text{ mA}, V_{GS} = -12\text{ V}$	8	—	—	
Gate threshold voltage	$V_{th}$	$V_{DS} = 10\text{ V}, I_D = 200\text{ }\mu\text{A}$	0.5	—	1.2	$\text{V}$
Drain-source ON resistance	$R_{DS(\text{ON})}$	$V_{GS} = 2.0\text{ V}, I_D = 3\text{ A}$	—	35	60	$\text{m}\Omega$
	$R_{DS(\text{ON})}$	$V_{GS} = 2.5\text{ V}, I_D = 3\text{ A}$	—	28	45	
	$R_{DS(\text{ON})}$	$V_{GS} = 4.5\text{ V}, I_D = 3\text{ A}$	—	22	30	
Forward transfer admittance	$ Y_{fs} $	$V_{DS} = 10\text{ V}, I_D = 3\text{ A}$	7.5	15	—	$\text{S}$
Input capacitance	$C_{iss}$	$V_{DS} = 10\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$	—	755	—	$\text{pF}$
Reverse transfer capacitance	$C_{rss}$		—	172	—	
Output capacitance	$C_{oss}$		—	222	—	
Switching time	Rise time	$t_r$	 $V_{GS}$ 5 V $0\text{ V}$ $I_D = 3\text{ A}$ $V_{OUT}$ $4.7\text{ }\Omega$ $3.3\text{ }\Omega$ $V_{DD} \approx 10\text{ V}$ $Duty \leq 1\%, t_w = 10\text{ }\mu\text{s}$	—	6	—
	Turn-ON time	$t_{on}$		—	11	—
	Fall time	$t_f$		—	32	—
	Turn-OFF time	$t_{off}$		—	64	—
Total gate charge (gate-source plus gate-drain)	$Q_g$	$V_{DD} \approx 16\text{ V}, V_{GS} = 5\text{ V}, I_D = 6\text{ A}$	—	15	—	$\text{nC}$
Gate-source charge	$Q_{gs}$		—	10	—	
Gate-drain ("miller") charge	$Q_{gd}$		—	5	—	

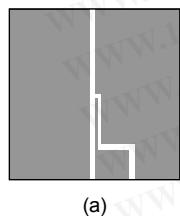
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## Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Pulse drain reverse current (Note 1)	$I_{DRP}$	—	—	—	24	A
Forward voltage (diode)	$V_{DSF}$	$I_{DR} = 6 \text{ A}, V_{GS} = 0 \text{ V}$	—	—	-1.2	V

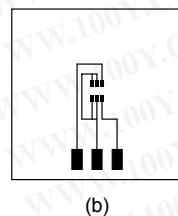
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a) (b) Device mounted on a glass-epoxy board (b)



(a)

FR-4  
25.4 × 25.4 × 0.8  
Unit: (mm)



(b)

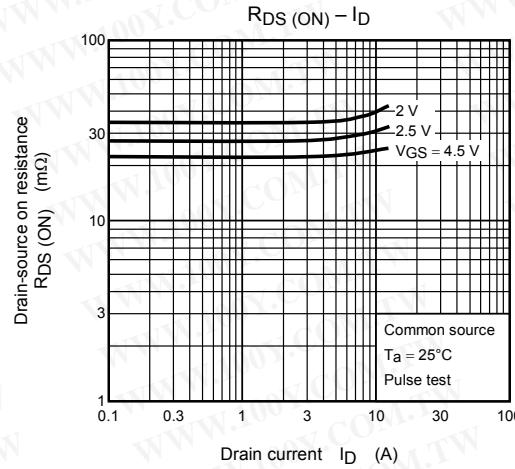
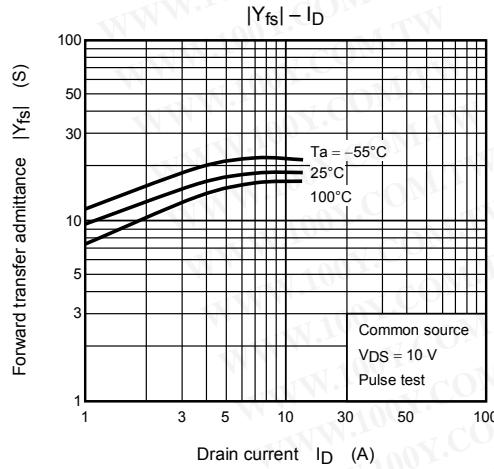
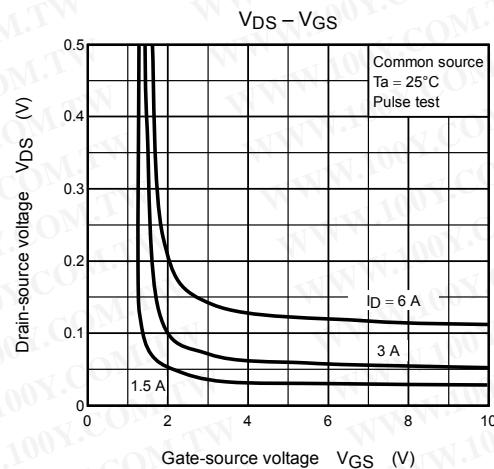
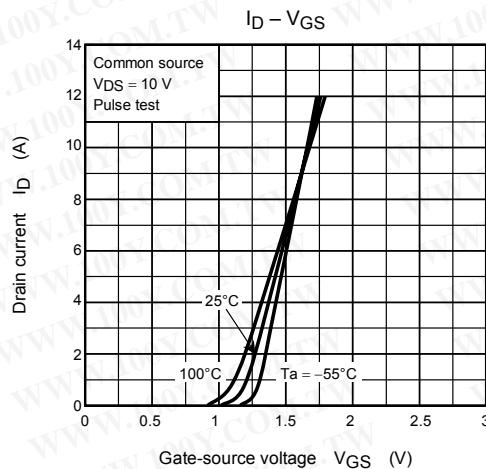
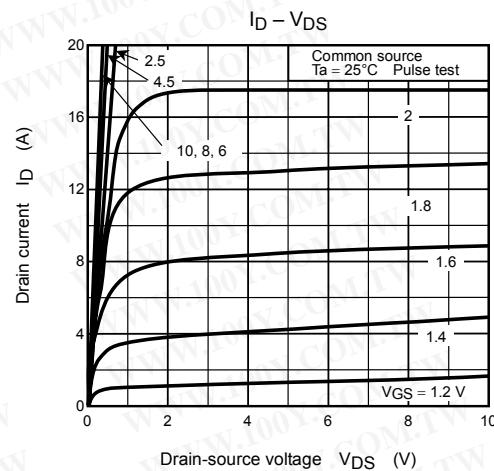
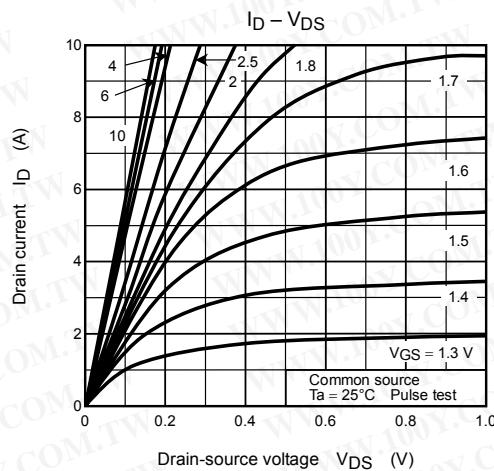
FR-4  
25.4 × 25.4 × 0.8  
Unit: (mm)

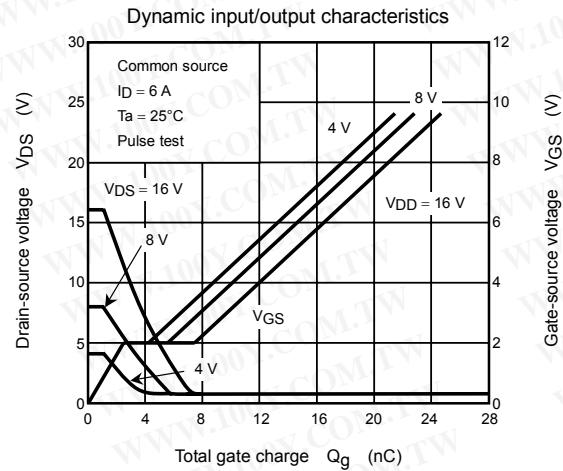
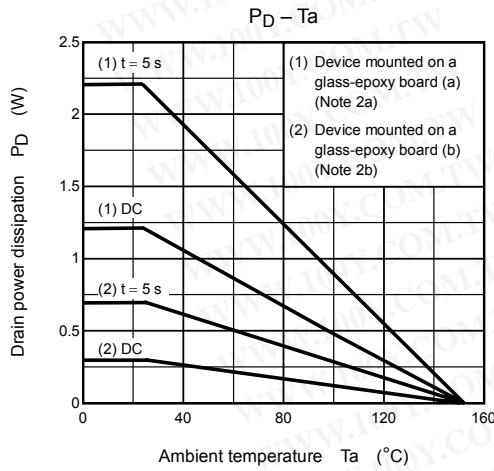
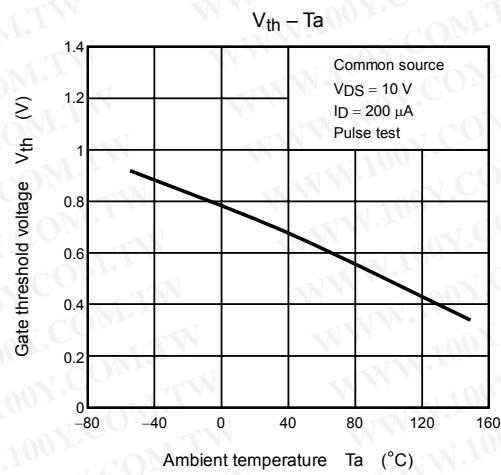
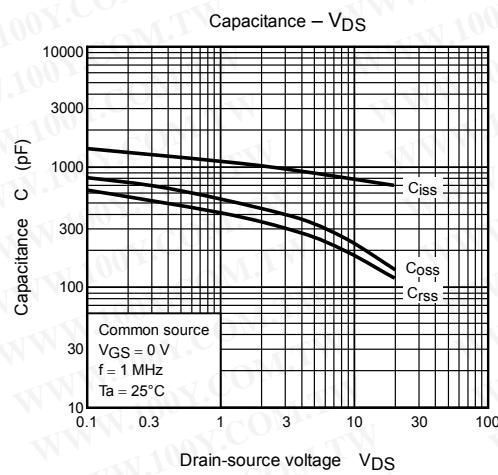
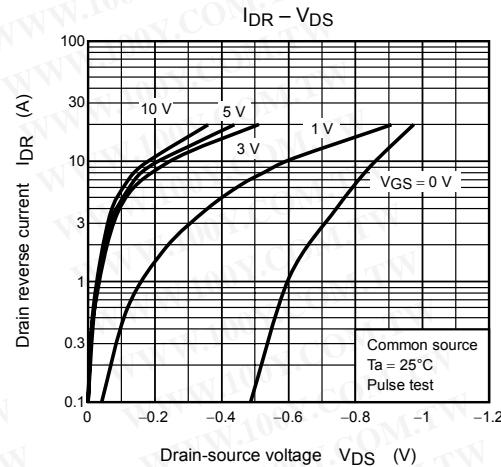
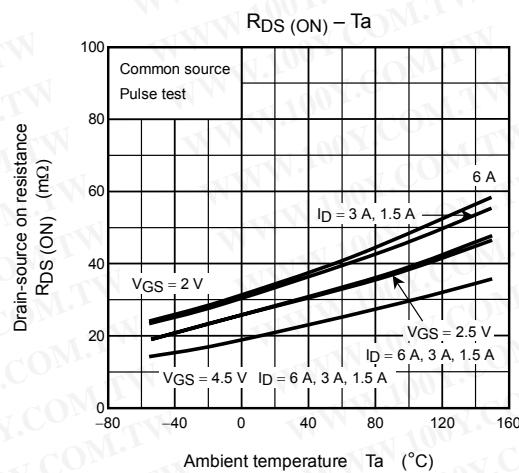
Note 3:  $V_{DD} = 16 \text{ V}$ ,  $T_{ch} = 25^\circ\text{C}$  (initial),  $L = 0.5 \text{ mH}$ ,  $R_G = 25 \Omega$ ,  $I_{AR} = 3.0 \text{ A}$

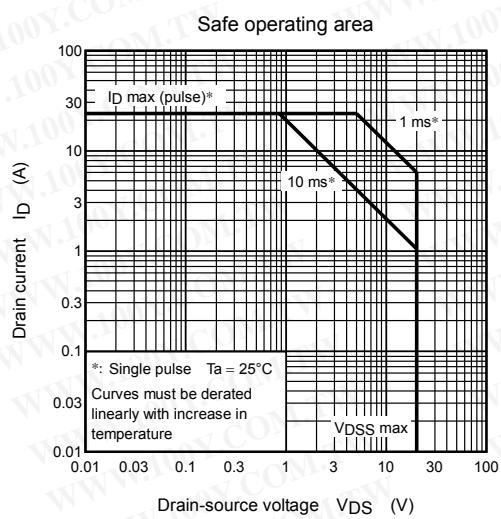
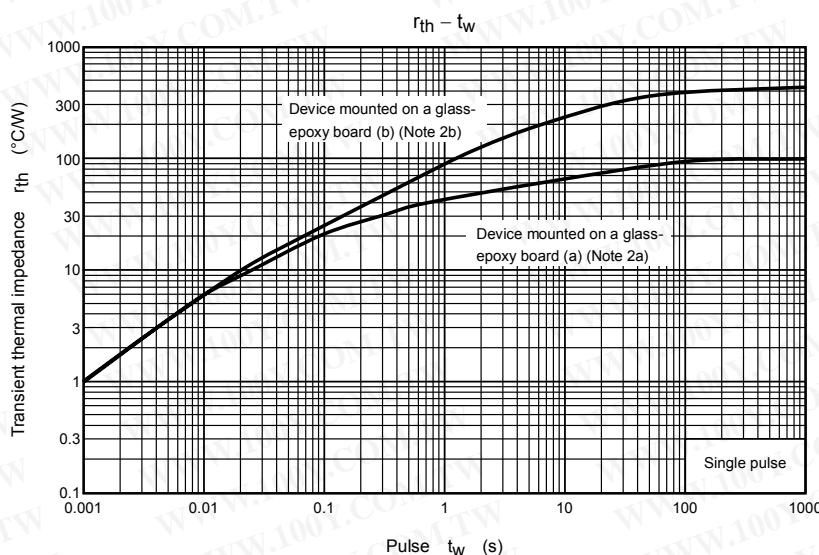
Note 4: Repetitive rating: pulse width limited by maximum channel temperature

Note 5: . • on lower left of the marking indicates Pin 1.

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