DATA SHEET

NEC

BIPOLAR ANALOG INTEGRATED CIRCUIT

μPC29M33A,μPC29M05A

THREE-TERMINAL LOW DROPOUT VOLTAGE REGULATOR

DESCRIPTION

The μPC29M33A, μPC29M05A of low dropout voltage three terminal positive regulators is constructed with PNP output transistor. The μPC29M33A, μPC29M05A feature the ability to source 0.5 A of output current with a low dropout voltage of typically 0.5 V.

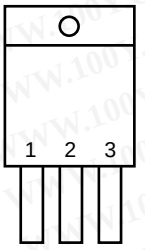
The power dissipation of the μPC29M33A, μPC29M05A can be drastically reduced compared with the conventional three terminal positive voltage regulators that is constructed with NPN output transistor. Also, this series corresponds to the low voltage output (3 V, 3.3 V) which is not in the conventional low dropout regulators (μPC24M00A series).

FEATURES

- Output current in excess of 0.5 A
- Low dropout voltage V_{DIF} = 0.5 V TYP. (at I_O = 0.5 A)
- On-chip overcurrent and thermal protection circuit
- On-chip output transistor safe area protection circuit

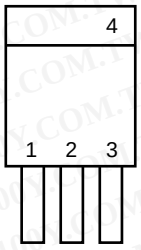
PIN CONFIGURATION (Marking Side)

μPC29M33AHF, μPC29M05AHF: MP-45G



1: INPUT
2: GND
3: OUTPUT

μPC29M33AHB, μPC29M05AHB: MP-3
μPC29M33A, μPC29M05AT: MP-3Z



1: INPUT
2: GND
3: OUTPUT
4: GND (Fin)

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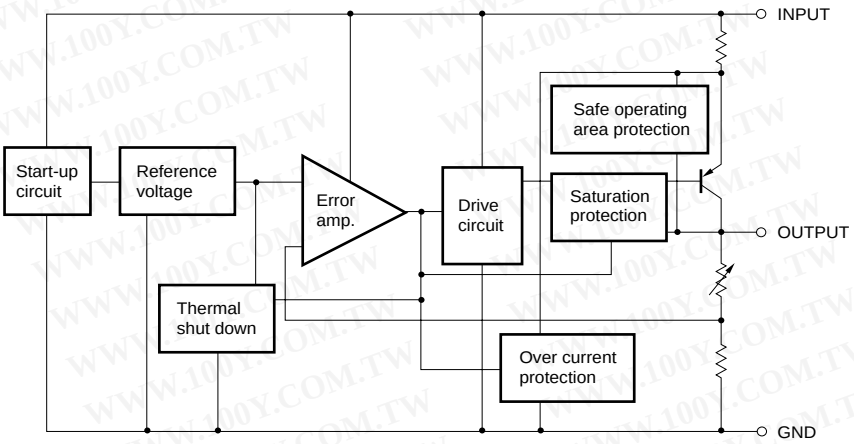
BLOCK DIAGRAM



ORDERING INFORMATION

Part Number	Package	Output Voltage	Marking	Package Type
μPC29M33AHF	MP-45G (Isolated TO-220)	3.3 V	29M33A	• Packed in envelope
μPC29M33AHB	MP-3 (SC-64)	3.3 V	29M33A	• Packed in envelope
μPC29M33AT	MP-3Z (SC-63)	3.3 V	29M33A	• Packed in envelope
μPC29M33AT-E1	MP-3Z (SC-63)	3.3 V	29M33A	• 16 mm wide embossed taping • Pin 1 on drawout side • 2000 pcs/reel
μPC29M33AT -E2	MP-3Z (SC-63)	3.3 V	29M33A	• 16 mm width embossed taping • Pin 1 at takeup side • 2000 pcs/reel
μPC29M33AT -T1	MP-3Z (SC-63)	3.3 V	29M33A	• 32 mm wide adhesive taping • Pin 1 at drawout side • 1500 pcs/reel
μPC29M33AT -T2	MP-3Z (SC-63)	3.3 V	29M33A	• 32 mm wide adhesive taping • Pin 1 at takeup side • 1500 pcs/reel
μPC29M05AHF	MP-45G (Isolated TO-220)	5.0 V	29M05A	• Packed in envelope
μPC29M05AHB	MP-3 (SC-64)	5.0 V	29M05A	• Packed in envelope
μPC29M05AT	MP-3Z (SC-63)	5.0 V	29M05A	• Packed in envelope
μPC2905AT-E1	MP-3Z (SC-63)	5.0 V	29M05A	• 16 mm wide embossed taping • Pin 1 at drawout side • 2000 pcs/reel
μPC2905AT-E2	MP-3Z (SC-63)	5.0 V	29M05A	• 16 mm wide embossed taping • Pin 1 at takeup side • 2000 pcs/reel
μPC2905AT-T1	MP-3Z (SC-63)	5.0 V	29M05A	• 32 mm wide adhesive taping • Pin 1 at drawout side • 1500 pcs/reel
μPC2905AT-T2	MP-3Z (SC-63)	5.0 V	29M05A	• 32 mm wide adhesive taping • Pin 1 at takeup side • 1500 pcs/reel

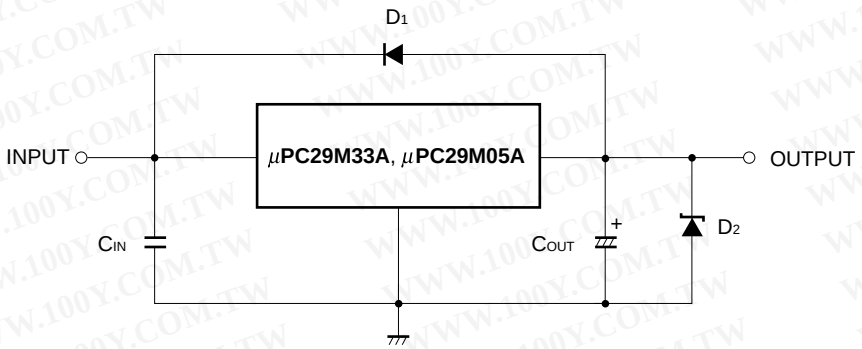
ABSOLUTE MAXIMUM RATINGS (T_A = 25°C unless otherwise specified)

Parameter	Symbol	Rating		Unit
		μPC29M33AHF, μPC29M05AHF	μPC29M33AHB, μPC29M05AHB μPC29M33AT, μPC29M05AT	
Input Voltage	V_{IN}	20		V
Internal Power Dissipation Note (T_C = 25°C)	P_T	15	10	W
Operating Ambient Temperature	T_A	–30 to +85		°C
Operating Junction Temperature	T_J	–30 to +150		°C
Storage Temperature	T_{stg}	–55 to +150		°C
Thermal Resistance (junction to case)	$R_{th(J-C)}$	7	12.5	°C/W
Thermal Resistance (junction to ambient)	$R_{th(J-A)}$	65	125	°C/W

Note Internally limited. When the operating junction temperature rises over 150°C, the internal circuit shuts down the output voltage.

Caution If the absolute maximum rating of any of the above parameters is exceeded even momentarily, the quality of the product may be degraded. In other words, absolute maximum ratings specify the values exceeding which the product may be physically damaged. Be sure to use the product with these ratings never exceeded.

STANDARD CONNECTION



C_{IN}: 0.1 μF or higher. Set this value according to the length of the line between the regulator and INPUT pin. Be sure to connect C_{IN} to prevent parasitic oscillation. Use of a film capacitor or other capacitor with excellent voltage and temperature characteristics is recommended. If using a laminated ceramic capacitor, it is necessary to ensure that C_{IN} is 0.1 μF or higher for the voltage and temperature range to be used.

C_{OUT}: 47 μF or higher. Be sure to connect C_{OUT} to prevent oscillation and improve excessive load regulation. Place C_{IN} and C_{OUT} as close as possible to the IC pins (within 2 cm). Also, use an electrolytic capacitor with low impedance characteristics if considering use at sub-zero temperatures.

D_1: If the OUTPUT pin has a higher voltage than the INPUT pin, connect a diode.

D_2: If the OUTPUT pin has a lower voltage than the GND pin, connect a Schottky barrier diode.

Caution Make sure that no voltage is applied to the OUTPUT pin from external.

RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	Type Number	MIN.	TYP.	MAX.	Unit
Input Voltage	V_{IN}	μPC29M33A	4.3		16	V
		μPC29M05A	6		16	
Output Current	I_O	All	0		0.5	A
Operating Ambient Temperature	T_A	All	–30		+85	°C
Operating Junction Temperature	T_J	All	–30		+125	°C

ELECTRICAL CHARACTERISTICS

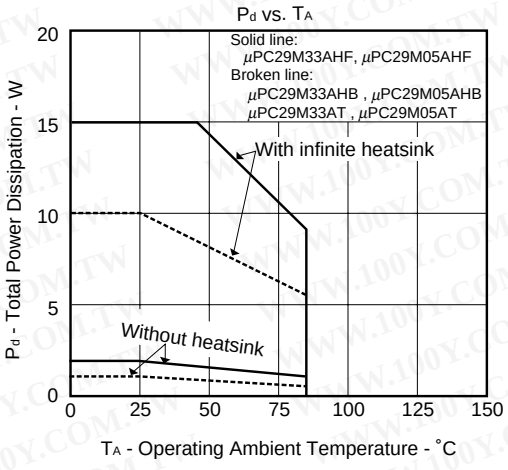
μPC29M33A (T_J = 25°C, V_{IN} = 5 V, I_O = 350 mA, C_{IN} = 0.22 μF, C_{OUT} = 47 μF, unless otherwise specified)

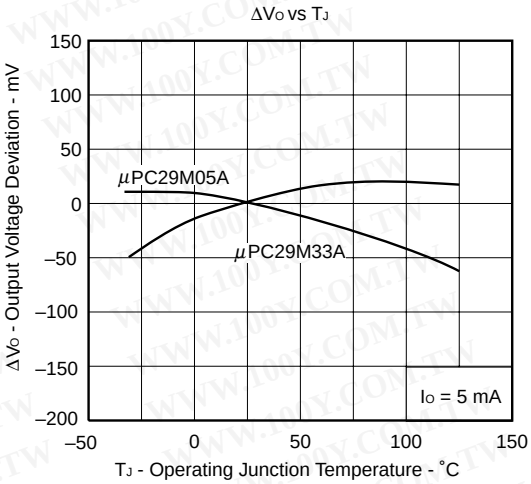
Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output Voltage	V_O		3.18	3.3	3.42	V
		0°C ≤ T_J ≤ 125°C, 4.3 V ≤ V_{IN} ≤ 16 V, 0 A ≤ I_O ≤ 350 mA	3.14		3.46	
		0°C ≤ T_J ≤ 125°C, 0 A ≤ I_O ≤ 0.5 A				
Line Regulation	REG_{IN}	4.3 V ≤ V_{IN} ≤ 16 V		8	33	mV
Load Regulation	REG_L	0 A ≤ I_O ≤ 0.5 A		10	33	
Quiescent Current	I_{BIAS}	I_O = 0 A		1.8	3.0	mA
		I_O = 0.5 A		15	20	
Startup Quiescent Current	$I_{BIAS\ (s)}$	V_{IN} = 3.1 V, I_O = 0 A		9	20	mA
		V_{IN} = 3.1 V, I_O = 0.5 A			50	
Quiescent Current Change	ΔI_{BIAS}	0°C ≤ T_J ≤ 125°C, 4.3 V ≤ V_{IN} ≤ 16 V		2.9	15	mA
Output Noise Voltage	V_n	10 Hz ≤ f ≤ 100 kHz		56		$\mu V_{r.m.s.}$
Ripple Rejection	R•R	4.3 V ≤ V_{IN} ≤ 16 V, f = 120 Hz	48	64		dB
Dropout Voltage	V_{DIF}	0°C ≤ T_J ≤ 125°C, I_O = 0.5 A		0.5	1.0	V
Short Circuit Current	I_{Opeak}	V_{IN} = 4.5 V	0.7	1.1	1.5	A
		V_{IN} = 16 V		0.6		
Peak Output Current	I_{Opeak}	V_{IN} = 4.5 V	0.7	1.2	1.5	A
		V_{IN} = 16 V	0.6	1.0	1.5	
Temperature Coefficient of Output Voltage	$\Delta V_O / \Delta T$	0°C ≤ T_J ≤ 125°C, I_O = 5 mA		–0.4		mV/°C

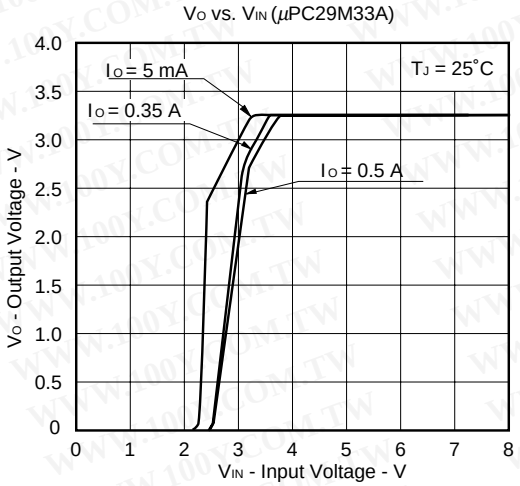
μPC29M05A (T_J = 25°C, V_{IN} = 8 V, I_O = 350 mA, C_{IN} = 0.22 μF, C_{OUT} = 47 μF, unless otherwise specified)

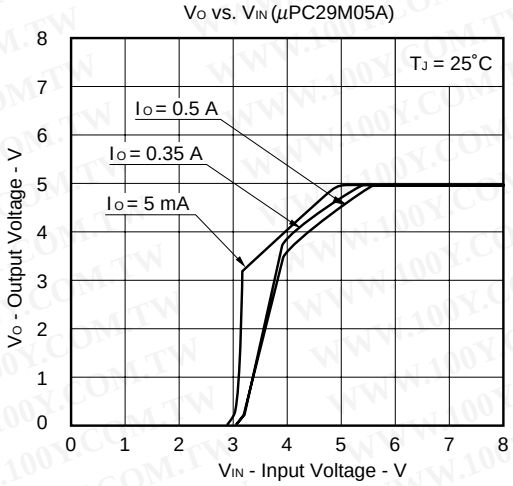
Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output Voltage	V_O		4.83	5.0	5.18	V
		0°C ≤ T_J ≤ 125°C, 6 V ≤ V_{IN} ≤ 16 V, 0 A ≤ I_O ≤ 350 mA	4.75		5.25	
		0°C ≤ T_J ≤ 125°C, 0 A ≤ I_O ≤ 0.5 A				
Line Regulation	REG_{IN}	6 V ≤ V_{IN} ≤ 16 V		26	50	mV
Load Regulation	REG_L	0 A ≤ I_O ≤ 0.5 A		17	50	mV
Quiescent Current	I_{BIAS}	I_O = 0 A		1.9	4.0	mA
		I_O = 0.5 A		15	20	
Startup Quiescent Current	$I_{BIAS\ (s)}$	V_{IN} = 4.5 V, I_O = 0 A		10	20	mA
		V_{IN} = 4.5 V, I_O = 0.5 A			50	
Quiescent Current Change	ΔI_{BIAS}	0°C ≤ T_J ≤ 125°C, 6 V ≤ V_{IN} ≤ 16 V		2.4	15	mA
Output Noise Voltage	V_n	10 Hz ≤ f ≤ 100 kHz		87		$\mu V_{r.m.s.}$
Ripple Rejection	R•R	6 V ≤ V_{IN} ≤ 16 V, f = 120 Hz	46	60		dB
Dropout Voltage	V_{DIF}	0°C ≤ T_J ≤ 125°C, I_O = 0.5 A		0.5	1.0	V
Short Circuit Current	I_{Opeak}	V_{IN} = 6.5 V	0.65	1.1	1.5	A
		V_{IN} = 16 V		0.6		
Peak Output Current	I_{Opeak}	V_{IN} = 6.5 V	0.7	1.2	1.5	A
		V_{IN} = 16 V	0.6	1.1	1.5	
Temperature Coefficient of Output Voltage	$\Delta V_O / \Delta T$	0°C ≤ T_J ≤ 125°C, I_O = 5 mA		0.7		mV/°C

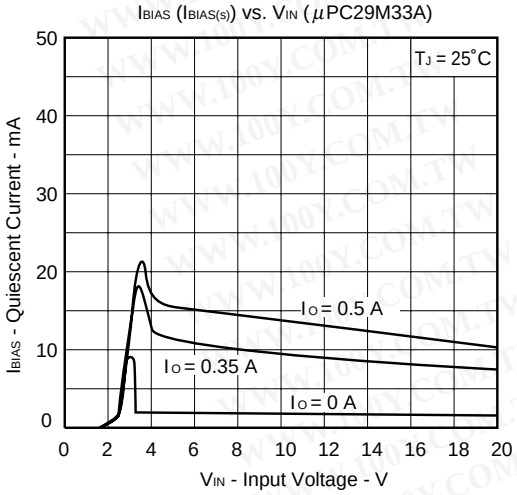
TYPICAL CHARACTERISTICS (Reference Values)

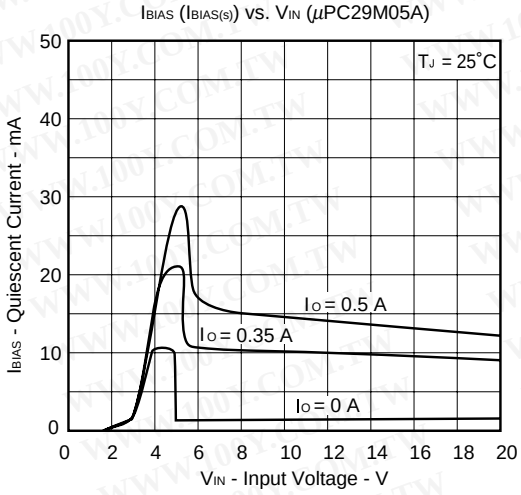




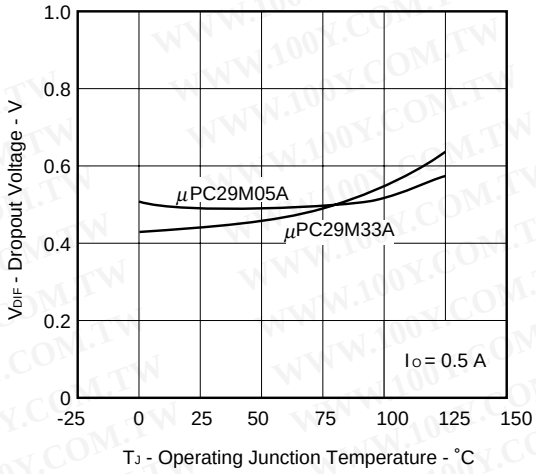




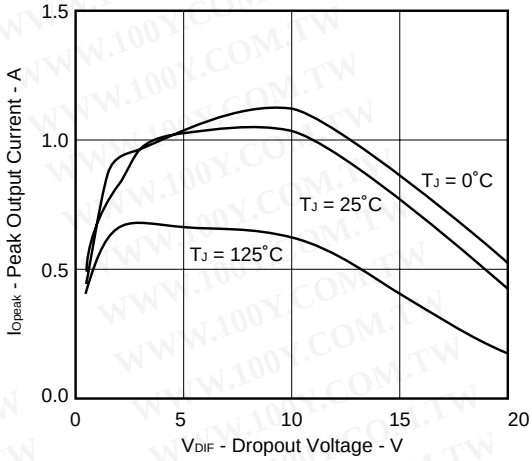




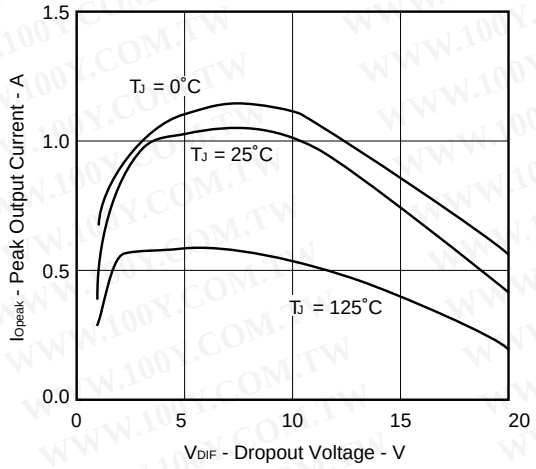
VDIF vs. TJ



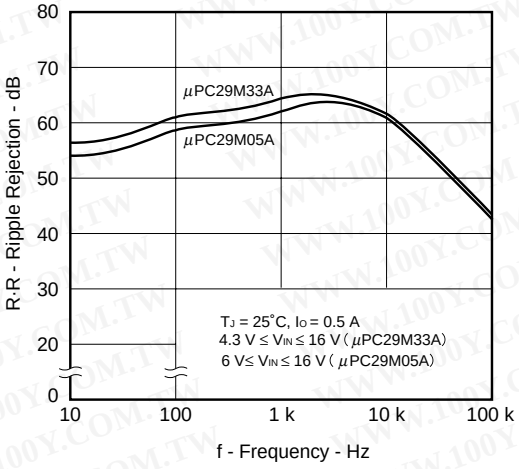
IOpeak vs. VDIF (μPC29M33A)



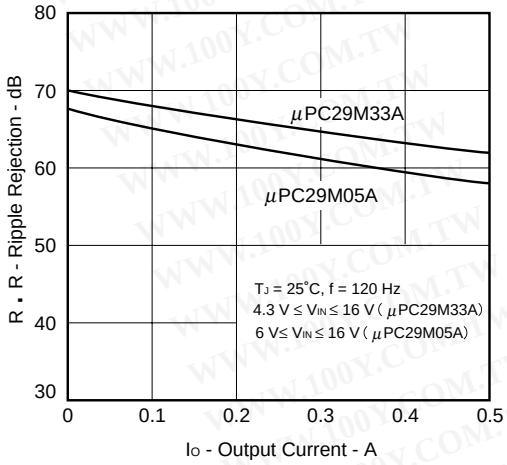
IOpeak vs. VDIF (μPC2905A)



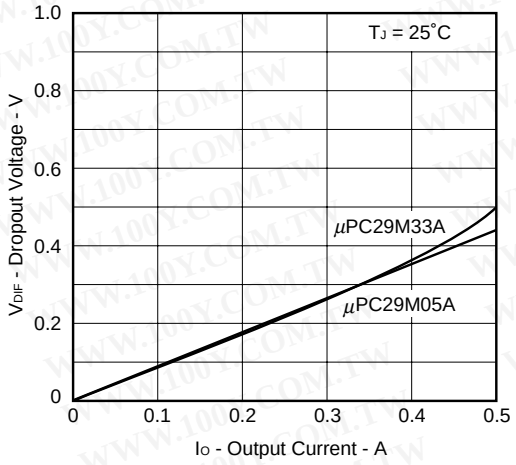
R·R vs f

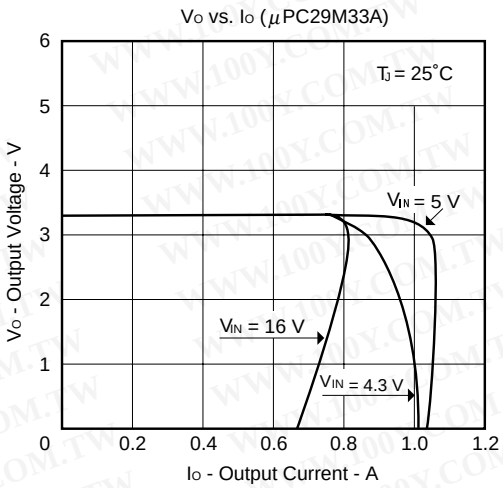


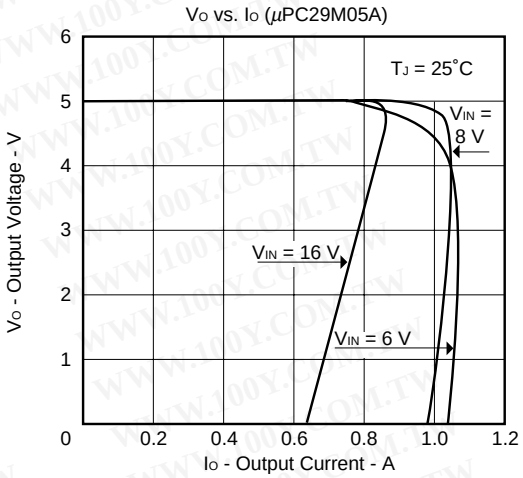
R • R vs. IO



VDIF vs. IO



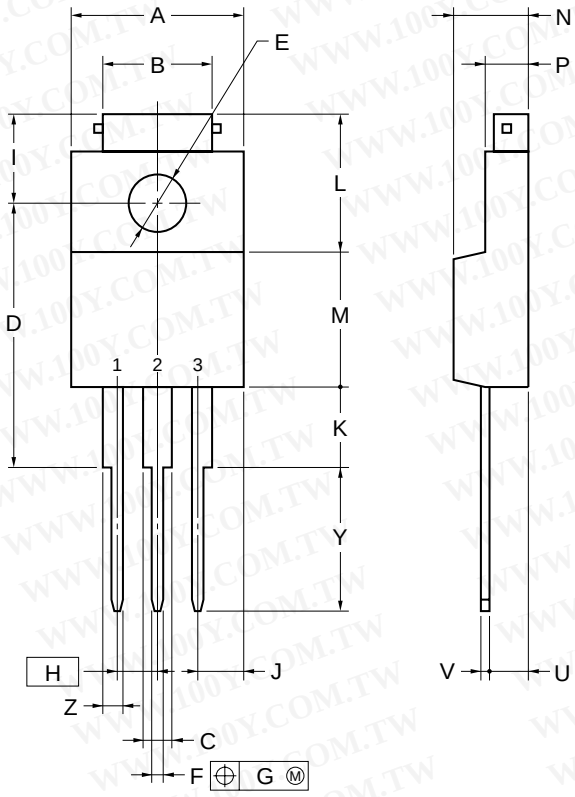




PACKAGE DRAWINGS

μPC29M33AHF, μPC29M05AHF

3PIN PLASTIC SIP (MP-45G)



NOTE

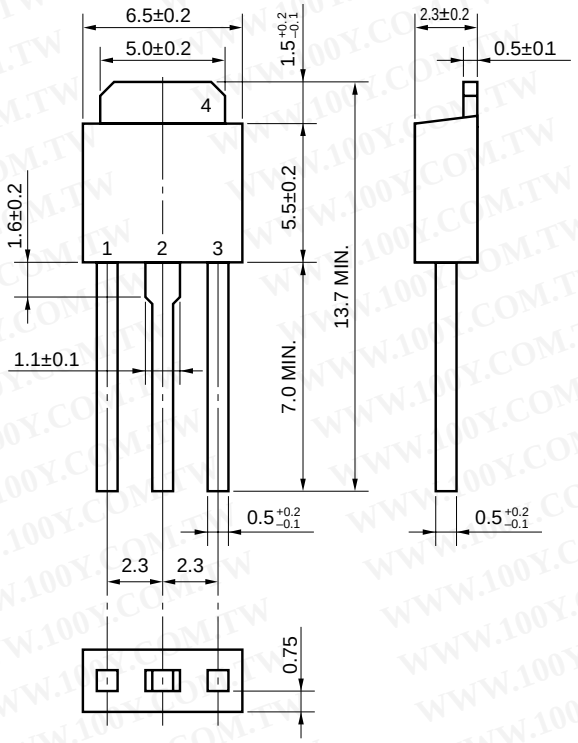
Each lead centerline is located within 0.25 mm of its true position (T.P.) at maximum material condition.

ITEM	MILLIMETERS
A	10.0±0.2
B	7.0±0.2
C	1.50±0.2
D	17.0±0.3
E	φ3.3±0.2
F	0.75±0.10
G	0.25
H	2.54 (T.P.)
I	5.0±0.3
J	2.46±0.2
K	5.0±0.2
L	8.5±0.2
M	8.5±0.2
N	4.5±0.2
P	2.8±0.2
U	2.4±0.5
V	0.65±0.10
Y	8.9±0.7
Z	1.30±0.2

P3HF-254B-4

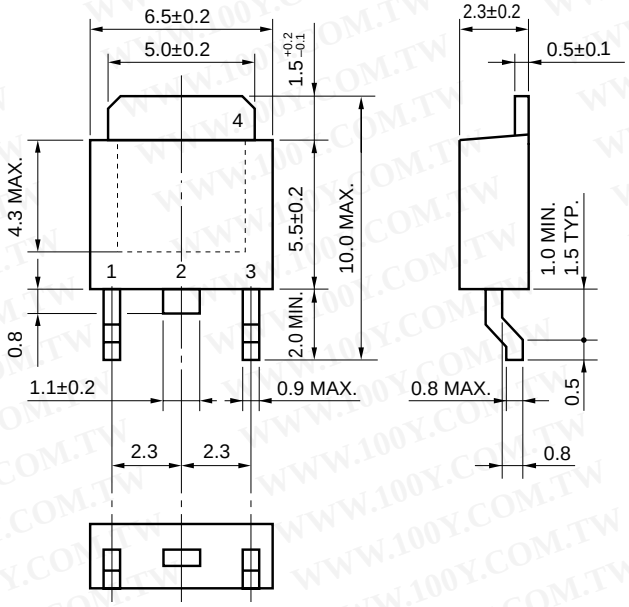
μPC29M33AHB, μPC29M05AHB

MP-3(SC-64) (Unit: mm)



μPC29M33AT, μPC29M05AT

MP-3Z (SC-63) (Unit: mm)



RECOMMENDED SOLDERING CONDITIONS

When soldering this product, it is highly recommended to observe the conditions as shown below. If other soldering processes are used, or if the soldering is perfomed under different condition, please make sure to consult with our sales offices.

For more details, refer to our document **"SEMICONDUCTOR DEVICE MOUNTING TECHNOLOGY MANUAL" (C10535E)**.

Surface Mount Device

μPC29M33AT, μPC29M05AT: MP-3Z (SC-63)

Process	Conditions	Symbol
Infrared Ray Reflow	Peak temperature: 235°C or below (Package surface temperature), Reflow time: 30 seconds or less (at 210°C or higher), Maximum number of reflow processes: 2 times or less.	IR35-00-2
Vapor Phase Soldering	Peak temperature: 215°C or below (Package surface temperature), Reflow time: 40 seconds or less (at 200°C or higher), Maximum number of reflow processes: 2 times or less.	VP15-00-2
Wave Soldering	Solder temperature: 260°C or below, Flow time: 10 seconds or less, Maximum number of flow processes: 1 time, Pre-heating temperature: 120°C or below (Package surface temperature).	WS60-00-1
Partial Heating Method	Pin temperature: 300°C or below, Heat time: 3 seconds or less (Per each side of the device).	–

Caution Apply only one kind of soldering condition to a device, except for "partial heating method", or the device will be damaged by heat stress.

Through-hole devices

μPC29M33AHF, μPC29M05AHF: MP-45G
μPC29M33AHB, μPC29M05AHB: MP-3

Process	Conditions
Wave soldering (only to leads)	Solder temperature: 260°C or below, Flow time: 10 seconds or less.
Partial heating method	Pin temperature: 300°C or below, Heat time: 3 seconds or less (Per each pin).

Caution For through-hole device, the wave soldering process must be applied only to leads, and make sure that the package body does not get jet soldered.

NOTES ON USE

When the μPC29M33A, μPC29M05A are used with an input voltage that is lower than the value indicated in the recommended operating conditions, a large quiescent current flows through the device due to saturation of the transistor of the output stage. (Refer to the I_{BIAS} ($I_{BIAS(S)}$) vs. V_{IN} curves in **TYPICAL CHARACTERISTICS**).

These products have saturation protector, but a current of up to 80 mA MAX. may flow through the device. Thus the power supply on the input side must have sufficient capacity to allow this quiescent current to pass when the device starts up.

REFERENCE DOCUMENTS

Document Name	Document No.
QUALITY GRADE ON NEC SEMICONDUCTOR DEVICES	C11531E
SEMICONDUCTOR DEVICE MOUNTING TECHNOLOGY MANUAL	C10535E
VOLTAGE REGULATOR OF SMD	G11872E
SEMICONDUCTOR SELECTION GUIDE – PRODUCTS AND PACKAGES	X13769E

[MEMO]

[MEMO]

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