

## FEATURE

1. Small size and light weight
2. High reliability and stability
3. Reduced size of final equipment
4. Lower assembly costs
5. Higher component and equipment reliability
6. Lead free product is available

勝特力材料 886-3-5753170  
勝特力电子(上海) 86-21-54151736  
勝特力电子(深圳) 86-755-83298787  
[Http://www.100y.com.tw](http://www.100y.com.tw)

## APPLICATION

- Mobile phone
- PDA
- Camcorders
- Pagers
- Palmtop computers

## DESCRIPTION

The resistors are constructed in a high grade ceramic body (aluminum oxide). Internal metal electrodes are added at each end and connected by a resistive paste that is applied to the top surface of the substrate. The composition of the paste is adjusted to give the approximate resistance required and the value is trimmed to nominated value within tolerance which controlled by laser trimming of this resistive layer.

The resistive layer is covered with a protective coat. Finally, the two external end terminations are added. For ease of soldering the outer layer of these end terminations is a Lead-tin or Tin (Lead free) alloy.

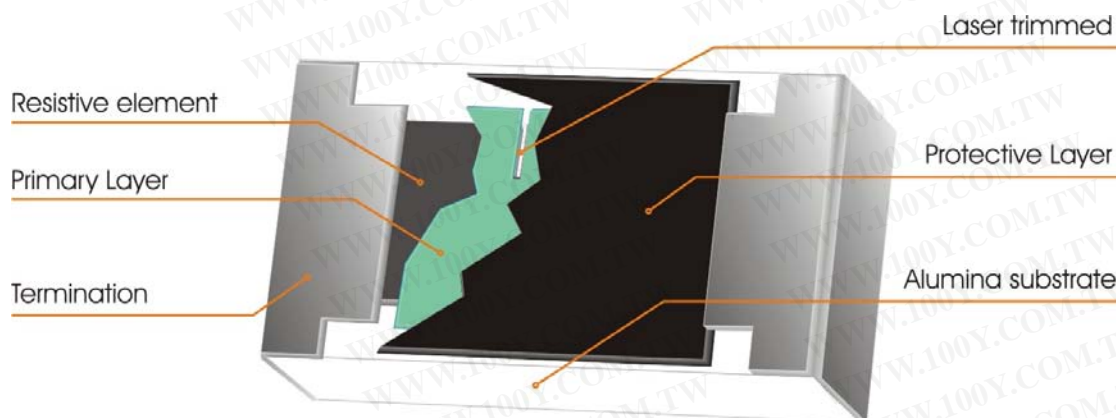


Fig 1. Construction of Chip-R WR04

## QUICK REFERENCE DATA

Item	General Specification		
Series No.	WR04X		
Size code	0402 ( 1005 )		
Resistance Tolerance	±5% (E24 series)	±1% (E96 series),	
Resistance Range	1Ω ~ 10MΩ ( ±5% ), Jumper (0Ω)	10Ω ~ 1MΩ ( ±1% )	
TCR (ppm/°C) R ≥ 1MΩ 1MΩ > R ≥10Ω R < 10Ω	-300ppm/°C ~ +500 ppm/°C ≤ ± 200 ppm/°C -300ppm/°C ~ +500 ppm/°C	≤ ± 200 ppm/°C	
Max. dissipation at T <sub>amb</sub> =70°C	1/16 W		
Max. Operation Voltage (DC or RMS)	50V		
Max. Overload Voltage (DC or RMS)	100V		
Climatic category (IEC 60068)	55/155/56		

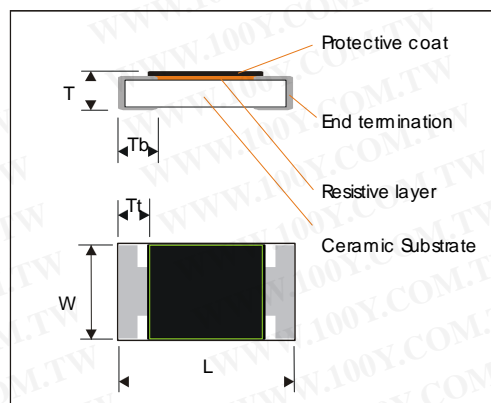
Note :

1. This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115-8"
2. Max. Operation Voltage : So called RCWV (Rated Continuous Working Voltage) is determined by  

$$RCWV = \sqrt{\text{Rated Power} \times \text{Resistance Value}}$$
or Max. RCWV listed above, whichever is lower.
3. For Resistance range 1Ω~10Ω and 1MΩ~10MΩ of tolerance ±1% series No. is WR04W, please refer to specification respectively.

## Dimensions:

	WR04X
<b>L</b>	1.00 ± 0.05
<b>W</b>	0.50 ± 0.05
<b>T</b>	0.35 ± 0.05
<b>Tb</b>	0.25 ± 0.10
<b>Tt</b>	0.20 ± 0.10



## MARKING

WR04 has no marking on the product overcoat for both 5% &amp; 1%.

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## FUNCTIONAL DESCRIPTION

### Product characterization

Standard values of nominal resistance are taken from the E24 series for resistors with a tolerance of  $\pm 5\%$ , and E96 series for resistors with a tolerance of  $\pm 1\%$ . The values of the E24/E96 series are in accordance with "IEC publication 60063"

### Derating

The power that the resistor can dissipate depends on the operating temperature; see Fig.2

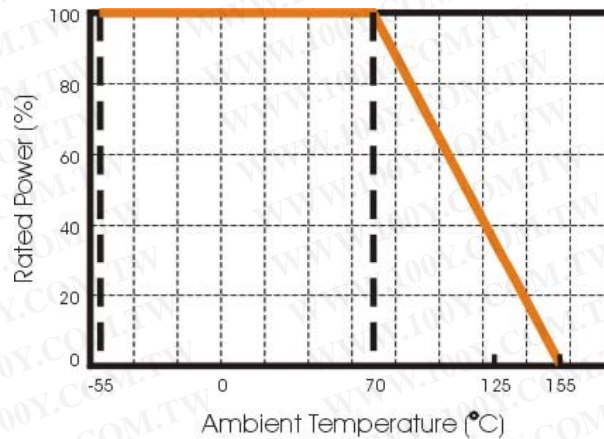


Figure 2. Maximum dissipation in percentage of rated power  
 As a function of the ambient temperature

## CATALOGUE NUMBERS

The resistors have a catalogue number starting with :

WR04	X	3742	J	T	—
Size code	Type code	Resistance code	Tolerance	Packaging code	Termination code
WR04 : 0402	X : Normal W : Out of resistance range (1% of 1Ω ~ 10Ω, 1MΩ~ 10MΩ)	E24 : 2 significant digits followed by no. of zeros and a blank 4.7Ω =4R7_ 10Ω =100_ 220Ω =221_ Jumper =000_ ("—" means a blank) E96 : 3 significant digits followed by no. of zeros 102Ω =1020 37.4KΩ =3742	F : $\pm 1\%$ J : $\pm 5\%$ P : Jumper	T : 7" Reeled taping B : Bulk	— = SnPb base ("—" means a blank) L = Sn base (lead free)

1. Reeled tape packaging : 8mm width paper taping 10,000pcs per reel.
2. Bulk packaging : 10,000pcs per poly-bag

## MOUNTING

Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems.

Chip placement can be on ceramic substrates and printed-circuit boards (PCBs).

Electrical connection to the circuit is by individual soldering condition.

The end terminations guarantee a reliable contact.

## SOLDERING CONDITION

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for one minute. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 230°C during 2 seconds. The test condition for no leaching is 260°C for 60 seconds. Typical examples of soldering processes that provide reliable joints without any damage are given in Fig 3.

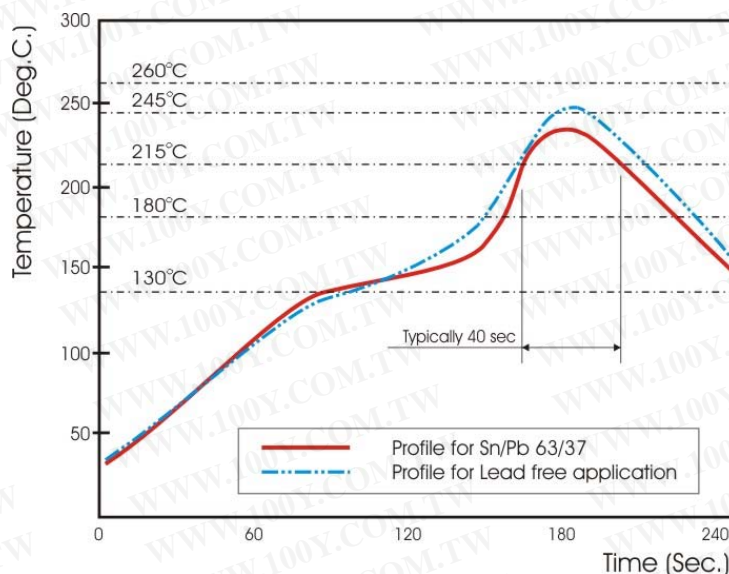


Fig 3. Infrared soldering profile for Chip Resistors WR04X

## TEST CONDITION FOR JUMPER (0 Ω)

Item	WR04X
Power Rating At 70°C	1/16W
Resistance	MAX. 50mΩ
Rated Current	1A
Peak Current	1.5A
Operating Temperature	-55~155°C

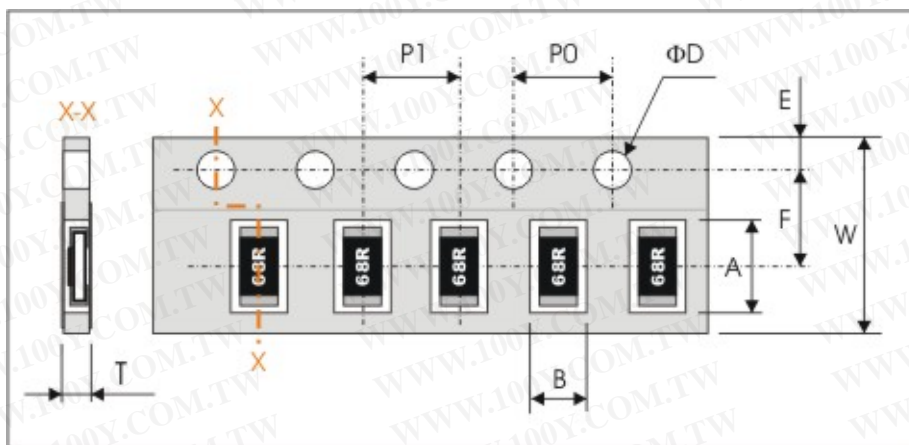


## TEST AND REQUIREMENTS

TEST	PROCEDURE	REQUIREMENT	
		Resistor	Jumper
DC resistance	DC resistance values measured at the test voltages specified below : $<10\Omega @ 0.1V$ , $<100\Omega @ 0.3V$ , $<1K\Omega @ 1.0V$ , $<10K\Omega @ 3V$ , $<100K\Omega @ 10V$ , $<1M\Omega @ 25V$ , $<10M\Omega @ 30V$	Within the specified tolerance	$< 50m\Omega$
Temperature Coefficient of Resistance ( TCR )	Natural resistance change per change in degree centigrade. $\frac{R_2 - R_1}{R_1(t_2 - t_1)} \times 10^6 \text{ (ppm/}^\circ\text{C)}$ $R_1$ : Resistance at reference temperature $R_2$ : Resistance at test temperature $t_1$ : $25^\circ\text{C}$	Test temperature $-55 \sim +155^\circ\text{C}$ $\geq 1M\Omega$ , $-300 \sim +500\text{ppm/}^\circ\text{C}$ $\geq 10\Omega$ , $\leq \pm 200\text{ppm/}^\circ\text{C}$ $< 10\Omega$ , $-300 \sim +500\text{ppm/}^\circ\text{C}$	N/a
Short time overload ( STOL )	Permanent resistance change after a 5second application of a voltage 2.5 times RCWV or the maximum overload voltage specified in the above list, whichever is less.	$\Delta R/R$ max. $\pm(2\%+0.10\Omega)$	$< 50m\Omega$
Resistance to soldering heat	Unmounted chips $10 \pm 1$ seconds, $270 \pm 5^\circ\text{C}$	no visible damage $\Delta R/R$ max. $\pm(1\%+0.10\Omega)$	no visible damage, $< 50m\Omega$
Solderability	Termination SnPb base : Unmounted chips completely immersed for $2 \pm 0.5$ sec. in a solder bath at $230 \pm 5^\circ\text{C}$  Termination Sn base (lead free) : Unmounted chip completely immersed in a lead free solder bath, $245^\circ\text{C} \pm 5^\circ\text{C}$ , $3 \pm 1$ sec	good tinning ( $>95\%$ covered) no visible damage	
Temperature cycling	1. 30 minutes at $-55^\circ\text{C} \pm 3^\circ\text{C}$ , 2. 2~3 minutes at room temperature, 3. 30 minutes at $+155^\circ\text{C} \pm 3^\circ\text{C}$ , 4. 2~3 minutes at room temperature, Total 5 continuous cycles	no visible damage $\Delta R/R$ max. $\pm(1\%+0.10\Omega)$	no visible damage, $< 50m\Omega$
Load life (endurance)	$70 \pm 2^\circ\text{C}$ , 1000 hours, loaded with RCWV or $V_{\text{max}}$ , 1.5 hours on and 0.5 hours off	$10\Omega \sim 1M\Omega$ $\pm(3\%+0.1\Omega)$ $< 10\Omega$ or $\geq 1M\Omega$ $\pm(5\%+0.1\Omega)$	$< 50m\Omega$
Load life in Humidity	1000 hours, at rated continuous working voltage in humidity chamber controller at $40^\circ\text{C} \pm 2^\circ\text{C}$ and 90~95% relative humidity, 1.5hours on and 0.5 hours off	$10\Omega \sim 1M\Omega$ $\pm(3\%+0.1\Omega)$ $< 10\Omega$ or $\geq 1M\Omega$ $\pm(5\%+0.1\Omega)$	$< 50m\Omega$
Bending and Termination strength	Resistors mounted on a 90mm glass epoxy resin PCB(FR4); bending : 5 mm, once for 10seconds Pulling test : 500grams	no visible damage $\Delta R/R$ max. $\pm(1\%+0.10\Omega)$	no visible damage, $< 50m\Omega$

## PACKAGING

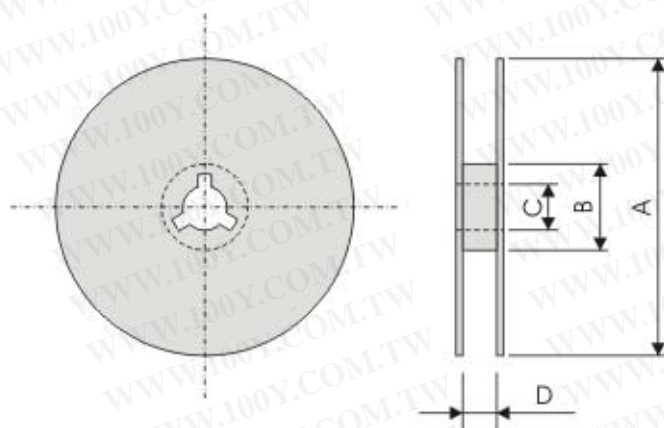
### Paper Tape specifications (unit :mm)



Series No.	A	B	W	F	E
WR04X	1.20±0.10	0.7±0.10	8.00±0.20	3.50±0.05	1.75±0.10

Series No.	P1	P0	ΦD	T
WR04X	2.00±0.10	4.00±0.10	Φ1.50 <sup>+0.1</sup> <sub>-0.0</sub>	0.40±0.05

### Reel dimensions



Symbol	A	B	C	D
(unit : mm)	Φ178.0±2.0	Φ60.0±1.0	13.0±0.2	9.0±0.5

### Taping quantity and Tape material

- Chip resistors 10,000 pcs/reel, Paper tape.