

# DATA SHEET

## LOW OHMIC CHIP RESISTORS

RL series (Pb Free) 5%, 1% sizes 0402/0603/0805/1206/ 1210/1218/2010/2512



勝 特 力 材 料 886-3-5753170 胜特力电子(上海) 86-21-54151736 胜特力电子(深圳) 86-755-83298787 Http://www.100y.com.tw

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### SCOPE

This specification describes RL0402 to RL2512 low ohmic chip resistors with lead-free terminations made by thick film process.

#### ORDERING INFORMATION

Part number is identified by the series, size, tolerance, packing type, temperature coefficient, taping reel and resistance value.

### PHYCOMP ORDERING CODE

### 12NC CODE

#### 2350 / 2390 / 2322 XXX XXXXX L

(2) (4) (I) (3) EMBOSSED<sup>(2)</sup> PAPER/PE<sup>(2)</sup> START TOL. RESISTANCE SIZE TYPE TAPE ON REEL TAPE ON REEL (units) IN <sup>(1)</sup> (%) RANGE 4,000 5,000 0402 LRC31 2350 ±5% 0.1 to I Ω 513 20xxx LRC32 2350  $\pm 1\%$  0.1 to 1  $\Omega$ 513 22xxx 0603 LRC21 2350 ±5% 0.01 to I Ω 512 10xxx LRC22 2350 ±1% 0.01 to I Ω 512 12xxx 0805 LRCII 2350 ±5% 0.01 to I Ω 511 10xxx LRC12 2350  $\pm$  1% 0.01 to 1  $\Omega$ 511 12xxx 1206 LRC01 2350 ±5% 0.01 to I Ω 510 10xxx LRC02 2350  $\pm$  1% 0.01 to 1  $\Omega$ 510 12xxx 1210 LPRC101 2390  $\pm 5\%$  0.01 to 0.0976  $\Omega$ 735 90xxx LPRC101 2390 ±5% 0.1 to I Ω 735 60xxx LPRC102 2390  $\pm$  1% 0.01 to 1  $\Omega$ 735 3xxxx 1218 LPRC201 2322 ±5% 0.01 to I Ω 735 64xxx LPRC201 2322  $\pm$  1% 0.01 to 1  $\Omega$ 735 7xxxx 2010 LPRCIII 2322 ±5% 0.01 to 0.0976 Ω 760 90xxx LPRCIII 2322 ±5% 0.1 to 1 Ω 760 60xxx LPRCIII 2322 ±1% 0.01 to 0.0976 Ω 761 90xxx LPRCIII 2322  $\pm$  1% 0.1 to 1  $\Omega$ 761 6xxxx 2512 LPRC221 2322 ±5% 0.01 to 0.0976 Ω 762 90xxx LPRC221 2322 ±5% 0.1 to I Ω 762 60xxx LPRC221 2322 ±1% 0.01 to 0.0976 Ω 763 90xxx LPRC221 2322  $\pm$  1% 0.1 to 1  $\Omega$ 763 6xxxx

- (1) The resistors have a 12-digit ordering code starting with 2350/2390/2322.
- (2) The subsequent 4 or 5 digits indicate the resistor tolerance and packaging.

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- (3) The remaining 4 or 3 digits represent the resistance value with the last digit indicating the multiplier as shown in the table of "Last digit of I2NC".
- (4) "L" means lead-free terminations.

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#### **ORDERING EXAMPLE**

The ordering code of a RL0805 resistor, value 0.56  $\Omega$  with ±1% tolerance, supplied in tape of 5,000 units per reel is: 235051112567L.

Last digit of I2N	С
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10,000

Last up	git of 1214	C	
Resistance	decade <sup>(3</sup>	;)	Last digit
0.01 to 0.0	)976 Ω		0
0.1 to 0.97	76 Ω		7
l to 9.76 9	2		8
10 to 97.6	Ω		0 9
100 to 976			COM-
l to 9.76 l	Ω		<b>CON</b> 2
10 to 97.6	kΩ		3
100 to 976	6 kΩ		4
l to 9.76 l	MΩ		5
10 to 97.6	MΩ	1.10	6
Example:	0.02 Ω	đ.,	0200 or 200
	0.3 Ω	1 III	3007 or 307
	ΙΩ	(F)	1008 or 108
	33 kΩ	Ē	3303 or 333
	10 MΩ		1006 or 106

### NOTE

- I. The "L" at the end of the code is only for ordering. On the reel label, the standard CTC or I2NC will be mentioned an additional stamp "LFP"= lead free production.
- 2. Products with lead in terminations fulfil the same requirements as mentioned in this datasheet.
- 3. Products with lead in terminations will be phased out in the coming months (before July 1st, 2006)

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### CTC CODE

RL XXXX X X X XX XXXX L (2) (3) (4) (5) (6) (7) (1)

(I) SIZE	WWW.LOOV.COM. TW
0402	WWW.Inv.COM.
0603	
0805	
1206	
1210	
1218	
2010	
2512	
(2) TO FRANCE	

### **ORDERING EXAMPLE**

The ordering code of a RL0805 chip resistor, value 0.56  $\Omega$  with ±1% tolerance, supplied in 7-inch tape reel is: RL0805FR-070R56L.

### NOTE

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- 1. The "L" at the end of the code is only for ordering. On the reel label, the standard CTC will be mentioned an additional stamp "LFP"= lead free production.
- 2. Products with lead in terminations fulfil the same requirements as mentioned in this datasheet.
- 3. Products with lead in terminations will be phased out in the coming months (before July 1st, 2006) WWW.100Y.COM.T

### (Z)

 $F = \pm 1\%$ 

 $| = \pm 5\%$ 

### (3) PACKAGING TYPE

R = Paper/PE taping reel

K = Embossed taping reel

### (4) TEMPERATURE COEFFICIENT OF RESISTANCE

- = Base on spec

#### (5) TAPING REEL

07 = 7 inch dia. Reel

### (6) RESISTANCE VALUE

0R01, 0R056, 0R56, 0R91 of E24 series (E48/96 on request).

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### (7) RESISTOR TERMINATIONS

L = Lead free terminations (matte tin)

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Chip Resistor Surface Mount RL SERIES 0402 to 2512 (Pb Free)

Product specification

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RD2D	E-24 series: 4 digits
Fig. I Value = 20 m $\Omega$	The "R" is used as a decimal point; the other 3 digits are significant.
)603: R≥100 mΩ IN E-24	SERIES, R = 10/20/30/40/50/60 mΩ
Y.CO. M.TW	3 digits
R22	The "R" is used as a decimal point; the other 2 digits are significant.
Fig. 2 Value = $22 \text{ m}\Omega$	The K is used as a decimal point, the other 2 digits are significant.

For marking codes, please see EIA-marking code rules in data sheet "Chip resistors marking".

### **CONSTRUCTION**

The resistors are constructed out of a high-grade ceramic body. Internal metal electrodes are added at each end and connected by a resistive paste. The composition of the paste is adjusted to give the approximate required resistance and laser cutting of this resistive layer that achieves tolerance trims the value. The resistive layer is covered with a protective coat and printed with the resistance value. Finally, the two external terminations (matte tin) are added. See fig. 4.

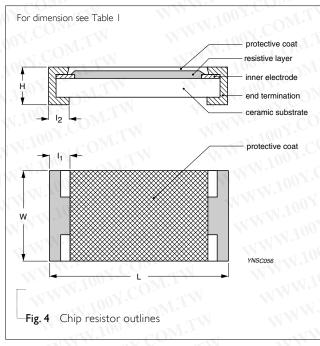
### **CONSTRUCTION**

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Table	I For out	lines see fig	. 4		
TYPE	L (mm)	W (mm)	H (mm)	lı (mm)	<sup>™</sup> l₂ (mm)
RL0402	1.00 ±0.10	0.50 ±0.05	0.35 ±0.05	0.20 ±0.10	0.25 ±0.10
RL0603	1.60 ±0.10	0.80 ±0.10	0.45 ±0.10	0.25 ±0.15	0.25 ±0.15
RL0805	2.00 ±0.10	1.25 ±0.10	0.50 ±0.10	0.35 ±0.20	0.35 ±0.20
RL1206	3.10 ±0.10	1.60 ±0.10	0.55 ±0.10	0.45 ±0.20	0.40 ±0.20
RL1210	3.10 ±0.10	2.60 ±0.15	0.55 ±0.10	0.50 ±0.20	0.50 ±0.20
RL1218	3.05 ±0.15	4.60 ±0.20	0.55 ±0.10	0.45 ±0.25	0.50 ±0.25
RL2010	5.00 ±0.10	2.50 ±0.15	0.55 ±0.10	0.60 ±0.20	0.50 ±0.20
RL2512	6.35 ±0.10	3.20 ±0.15	0.55 ±0.10	0.60 ±0.20	0.50 ±0.20

### OUTLINES



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### ELECTRICAL CHARACTERISTICS

	RESISTANCE RANGE	WWW.100	Y.COM	M.TW			
<b>RL0402</b> I00mΩ≤R <iω< th=""><th>W</th><th>or. COM.T</th><th>100r</th><th>nΩ≤R<iω< th=""><th>ON.IT</th><th></th></iω<></th></iω<>		W	or. COM.T	100r	nΩ≤R <iω< th=""><th>ON.IT</th><th></th></iω<>	ON.IT	
		±800 ppm/°C					
BL 0402		10mΩ≤R≤36	mΩ 36	mΩ <r≤91mω< th=""><th>91mΩ&lt;</th><th>R≤500mΩ</th><th>500mΩ<r<iω< th=""></r<iω<></th></r≤91mω<>	91mΩ<	R≤500mΩ	500mΩ <r<iω< th=""></r<iω<>
<b>RL0603</b> 10mΩ≤R<1Ω		±1,500 ppm	/°C ±	1,200 ppm/°C	±800	ppm/°C	±300 ppm/°C
N.	COMPT	I0mΩ≤R≤I8mΩ	I8mΩ <r≤47mω< td=""><td>47mΩ<r≤91mω< td=""><td>2 91mΩ<r≤360mω< td=""><td>360mΩ<r≤500mω< td=""><td>500mΩ<r<iω< td=""></r<iω<></td></r≤500mω<></td></r≤360mω<></td></r≤91mω<></td></r≤47mω<>	47mΩ <r≤91mω< td=""><td>2 91mΩ<r≤360mω< td=""><td>360mΩ<r≤500mω< td=""><td>500mΩ<r<iω< td=""></r<iω<></td></r≤500mω<></td></r≤360mω<></td></r≤91mω<>	2 91mΩ <r≤360mω< td=""><td>360mΩ<r≤500mω< td=""><td>500mΩ<r<iω< td=""></r<iω<></td></r≤500mω<></td></r≤360mω<>	360mΩ <r≤500mω< td=""><td>500mΩ<r<iω< td=""></r<iω<></td></r≤500mω<>	500mΩ <r<iω< td=""></r<iω<>
RL0805		±1,500 ppm/°C	±1,200 ppm/°C	±1,000 ppm/°C	±600 ppm/°C	±300 ppm/°C	±200 ppm/°C
RL1206		±1,500 ppm/°C	±1,200 ppm/°C	±1,000 ppm/°C	±600 ppm/°C	±300 ppm/°C	€±200 ppm/°C
RL1210	I0mΩ≤R <iω< td=""><td>±1,500 ppm/°C</td><td>±1,000 ppm/°C</td><td>±800 ppm/°C</td><td>±600 ppm/°C</td><td>±300 ppm/°C</td><td>±200 ppm/°C</td></iω<>	±1,500 ppm/°C	±1,000 ppm/°C	±800 ppm/°C	±600 ppm/°C	±300 ppm/°C	±200 ppm/°C
RL2010		±1,500 ppm/°C	±1,200 ppm/°C	±1,000 ppm/°C	±600 ppm/°C	±300 ppm/°C	±200 ppm/°C
RL2512		±1,500 ppm/°C	±1,200 ppm/°C	±800 ppm/°C	±600 ppm/°C	±300 ppm/°C	±200 ppm/°C
011210		I0mΩ≤R≤30mΩ	30mΩ <r≤56< td=""><td></td><td>≤180mΩ</td><td>180mΩ<r<1ω< td=""><td>2</td></r<1ω<></td></r≤56<>		≤180mΩ	180mΩ <r<1ω< td=""><td>2</td></r<1ω<>	2
RLIZI8	l0mΩ≤R <lω< td=""><td>±2,000 ppm/°C</td><td>±1,000 ppm</td><td>/°C ±700 p</td><td>pm/°C</td><td>±250 ppm/°C</td><td>MT.IN</td></lω<>	±2,000 ppm/°C	±1,000 ppm	/°C ±700 p	pm/°C	±250 ppm/°C	MT.IN

### FOOTPRINT AND SOLDERING PROFILES

For recommended footprint and soldering profiles, please see the special data sheet "Chip resistors mounting".

### ENVIRONMENTAL DATA

For material declaration information (IMDS-data) of the products, please see the separated info WWW.100Y.CC WWW.100Y.COM.TW "Environmental data". W.100Y.COM.

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### PACKING STYLE AND PACKAGING QUANTITY

PACKING STYLE	REEL DIMENSION	RL0402	RL0603 RL08	805 RLI20	6 RLI210	RL1218	RL2010	RL2512
Paper/PE taping reel (R)	7" (178 mm)	10,000	5,000 5,	000 5,00	0 5,000		NN.IO	J CO
Embossed taping reel (K)	7" (178 mm)		WW	101.0	NTN.	4.000	4.000	4,000

#### NOTE

1. For Paper/PE/Embossed tape and reel specification/dimensions, please see the special data sheet "Packing" document. WWW.100Y.C WWW.100Y.C

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RL 0402 to 2512 (Pb Free) **Chip Resistor Surface Mount** SERIES

Product specification

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### FUNCTIONAL DESCRIPTION

### **OPERATINGTEMPERATURE RANGE**

Range: -55°C to +125°C

### **POWER RATING**

Each type rated power at 70°C: RL0402=1/16 W; RL0603=1/10 W; RL0805=1/8 W; RL1206=1/4 W; RL1210=1/2 W; RL1218=1 W; RL2010=3/4 W; RL2512=1 W.

### **RATED VOLTAGE**

The DC or AC (rms) continuous working voltage corresponding to the rated power is determined by the following formula:

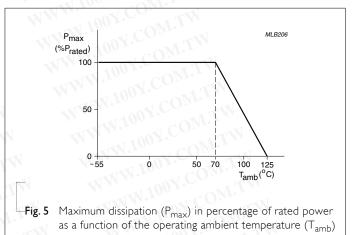
$$V = \sqrt{(P \times R)}$$

Where

V = Continuous rated DC or AC (rms) working voltage (V)WWW.100Y.COM.TW

P = Rated power (W)

 $R = Resistance value (\Omega)$ 



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# 100X.COM.TW TESTS AND REQUIREMENTS

TEST	TEST METHOD	PROCEDURE	REQUIREMENTS
Temperature	MIL-STD-202F-method 304;	At +25/–55 °C and +25/+125 °C	Refer to table 2
Coefficient of Resistance	JIS C 5202-4.8	Formula:	
(T.C.R.)			
		T.C.R= $\frac{R_2-R_1}{R_1(t_2-t_1)} \times 10^6 \text{ (ppm/°C)}$	
		Where t <sub>1</sub> =+25 °C or specified room temperature	
		$t_2 = -55$ °C or +125 °C test temperature	
		R <sub>I</sub> =resistance at reference temperature in ohms	
		R <sub>2</sub> =resistance at test temperature in ohms	
N.ICON.CO	WWW WT	TODY.COM TW WWW	OOY.CO. MITW
Thermal Shock	MIL-STD-202F-method 107G;	At -65 (+0/-10) °C for 2 minutes and at +125	±1.0%
	IEC 60115-1 4.19	(+10/–0) °C for 2 minutes; 25 cycles	
WWW.100			
Low Temperature	MIL-R-55342D-Para 4.7.4	At -65 (+0/-5) °C for I hour; RCWV applied for 45 (+5/-0) minutes	±1.0%
Operation			No visible damage
Short Time	MIL-R-55342D-Para 4.7.5;	2.5 × RCWV applied for 5 seconds at room	±1.0% for 1% tol.
Overload	IEC 60115-1 4.13	temperature	±2.0% for 5% tol.
	100Y. M.TW	WWW.100Y.COM.TW	No visible damage
Insulation	MIL-STD-202F-method 302;	One DC voltage (V) applied for 1 minute	≥10 GΩ
Resistance	IEC 60115-1 4.6.1.1	Details see below table 5	
Dielectric	MIL-STD-202F-method 301;	One AC voltage (V <sub>rms</sub> ) applied for 1 minute	No breakdown or flashove
Withstand	IEC 60115-1 4.6.1.1	Details see below table 5	
Voltage		WWWWWWWWWWWWW	
	WWW. LOOY.COM	TW WWW. 100Y.COM.TY	N WW.10
Resistance to Soldering	MIL-STD-202F-method 210C;	Unmounted chips; 260 $\pm$ 5 °C for 10 $\pm$ 1 seconds	±1.0%
Heat	IEC 60115-1 4.18	seconds	No visible damage
Life	MIL-STD-202F-method 108A;	At 70±2 °C for 1,000 hours; RCWV applied for	±2% for 1% tol.
	IEC 60115-1 4.25.1	1.5 hours on and 0.5 hour off	±3% for 5% tol.
			± 5% 101 5% tol.
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	WWW.100		
	WWW	券特力材料 886-3-5753170 +性力由子(1-約) 86-91-54151796	
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**Chip Resistor Surface Mount** RL SERIES 0402 to 2512 (Pb Free) WWW.100Y. WWW.100Y.COM.T WWW.100Y.COM.T WWW.100Y

8 Product specification

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Table 4	Test condition, procedure and requirements (cor	ntinued)

TEST	TEST METHOD	PROCEDURE	REQUIREMENTS
Solderability	MIL-STD-202F-method 208A;	Solder bath at 245±3 °C	Well tinned (≥95% covered
	IEC 60115-1 4.17	Dipping time: 2±0.5 seconds	No visible damage
Resistance to	MIL-STD-202F-method 215;	lsopropylalcohol (C <sub>3</sub> H <sub>7</sub> OH) or dichloromethane	No smeared
Solvent	IEC 60115-1 4.29	(CH <sub>2</sub> Cl <sub>2</sub> ) followed by brushing	
Humidity	JIS C 5202 7.5;	I,000 hours; 40±2 °C; 93(+2/−3)% RH	±2.0%
(steady state)	IEC 60115-8 4.24.8	RCWV applied for 1.5 hours on and 0.5 hour off	CONTRACT
Leaching	EIA/IS 4.13B;	Solder bath at 260±5 °C	No visible damage
	IEC 60115-8 4.18	Dipping time: 30±1 seconds	

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TYPE	RL0402	RL0603	RL0805	RL1206	RL1210	RL1218	RL2010	RL2512
Voltage (DC/unit: V); (AC/ unit: V <sub>rms</sub> )	100	100	300	500	500	500	500	500

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<u>REVISION HISTORY</u>	
REVISION DATE CHANGE NOTIFICATIO	DN DESCRIPTION
Version I Apr I 5, 2005 -	<ul> <li>New datasheet for low ohmic chip resistors sizes of 0402/0603/0805/1206/1210/1218/2010/2512 1% and 5% with lead-fr terminations</li> </ul>
	- Replace the 0603 to 2512 parts of pdf files: LRC01_5_12, LRC02_1_4 LRC11_5_4, LRC12_1_3, LRC21_22_51_4, LPRC111_1_6, LPRC111 LPRC221_1_PbFree_L_0, LPRC221_5_6, P_RL1218_51_PbFree_L_0 combine into a document.
	- Test method and procedure updated
	- PE tape added (paper tape will be replaced by PE tape)
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