

DATA SHEET

ARRAY CHIP RESISTORS

YC122 (4Pin/2R; Pb Free)

5%, 1%

sizes 2 × 0402



勝 特 力 材 料 886-3-5753170 胜特力电子(上海) 86-21-54151736 胜特力电子(深圳) 86-755-83298787 Http://www.100y.com.tw





Phicomp

Chip Resistor Surface Mount

C SERIES

122 (Pb Free)

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<u>SCOPE</u>

This specification describes YC122 series chip resistor arrays with lead-free terminations made by thick film process.

ORDERING INFORMATION

Part number is identified by the series, size, tolerance, packing type, temperature coefficient, taping reel and resistance value.

PHYCOMP ORDERING CODE

12NC CODE

2350	XXX >	XXXX	L
(I) (O)	(2)	(3)	(4)

			RESISTANCE	PAPER / PE TA	APE ON REEL (units) (2)
2×0402	IN (I)	(%)	RANGE	10,000	50,000
ARV32I	2350	±5%	10 to 1 MΩ	013 11xxx	013 12xxx
ARV322	2350	±1%	10 to 1 $M\Omega$	013 2xxxx	013 3xxxx
Jumper	2350		0 Ω	013 91001	ON.TW

- (1) The resistors have a 12-digit ordering code starting with 2350.
- (2) The subsequent 4 or 5 digits indicate the resistor tolerance and packaging.
- (3) The remaining 4 or 3 digits represent the resistance value with the last digit indicating the multiplier as shown in the table of "Last digit of 12NC".
- (4) "L" means lead-free terminations.

ORDERING EXAMPLE

The ordering code of an ARV321 convex chip resistor array, value 1,000 Ω with ±5% tolerance, supplied in tape of 10,000 units per reel is: 235001311102L.

Last dig	it of I2N	C	
Resistance	decade (3) _I .C	Last digit
0.01 to 0.0	976 Ω	ov.	COM
0.1 to 0.97	6 Ω		COM. 7
I to 9.76 S	2		4 COM 8
10 to 97.6	Ω		
100 to 976	Ω		ON.
I to 9.76 k	Ω		00 ^y .
10 to 97.6	kΩ		100 Y. CU3
100 to 976	kΩ		Y.C4
I to 9.76 N	1Ω		5.00
10 to 97.6	ΜΩ	N W	W.100 6
Example:	0.02 Ω	-E-1	0200 or 200
	0.3 Ω	=	3007 or 307
	٧		

100 -

YC122	- <u>X</u>	X	X	XX	XXXX	L
	(1)	(2)	(3)	(4)	(5)	(6)

(I) TOLERANCE	
F = ±1%	TW
$J = \pm 5\%$	

(2) PACKAGING TYPE

R = Paper/PE taping reel

(3) TEMPERATURE COEFFICIENT OF RESISTANCE

- = Base on spec

(4) TAPING REEL

07 = 7 inch dia. Reel 13 = 13 inch dia. Reel

(5) RESISTANCE VALUE

56R, 560R, 5K6, 56K, 1M 0R = Jumper

(6) RESISTOR TERMINATIONS

L = Lead free terminations (pure Tin)

ORDERING EXAMPLE

The ordering code of a YC122 convex chip resistor array, value $1,000~\Omega$ with $\pm 5\%$ tolerance, supplied in 7-inch tape reel is: YC122-JR-071KL.

NOTE

1. The "L" at the end of the code is only for ordering. On the reel label, the standard CTC or 12NC will be mentioned an additional stamp "LFP"= lead free production.

ΙΩ

1008 or 108

3303 or 333

1006 or 106

- 2. Products with lead in terminations fulfil the same requirements as mentioned in this datasheet.
- 3. Products with lead in terminations will be phased out in the coming months (before July 1st, 2006)





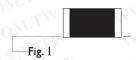
122 (Pb Free)

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MARKING

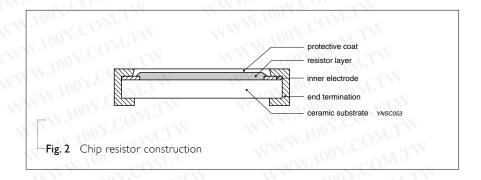
YC122



No marking

CONSTRUCTION

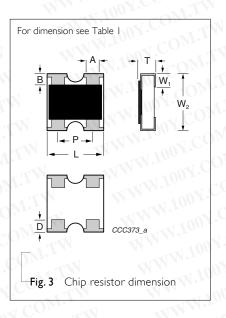
The resistors are constructed out of a high-grade ceramic body. Internal metal electrodes are added at each end and connected by a resistive paste. The composition of the paste is adjusted to give the approximate required resistance and laser cutting of this resistive layer that achieves tolerance trims the value. The resistive layer is covered with a protective coat.



Finally, the four external terminations (pure Tin) are added. See fig. 2.

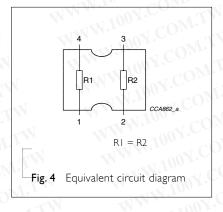
DIMENSIONS

Table I	
TYPE	YC122
A (mm)	0.21 +0.10/-0.05
B (mm)	0.20 ±0.10
P (mm)	0.67 ±0.05
L (mm)	1.00 ±0.10
T (mm)	0.35 ±0.10
W _I (mm)	0.25 ±0.10
W ₂ (mm)	1.00 ±0.10
D (mm)	0.20 ±0.10



WW.100Y.COA

SCHEMATIC



Chip Resistor Surface Mount YC SERIES 122 (Pb Free)

ELECTRICAL CHARACTERISTICS

1/16 W
+125 °C
50 V
100 V
100 V
2
to I MΩ
to I MΩ
< 0.05 Ω
ppm/°C
1.0 A

FOOTPRINT AND SOLDERING PROFILES

For recommended footprint and soldering profiles, please see the special data sheet "Chip resistors mounting".

ENVIRONMENTAL DATA

For material declaration information (IMDS-data) of the products, please see the separated info "Environmental data" conformed to EU RoHS.

PACKING STYLE AND PACKAGING QUANTITY

Table 3 Packing style and packaging quantity

PRODUCT TYPE	PACKING STYLE	REEL DIMENSION	QUANTITY PER REEL
YCI22	Paper / PE Taping Reel (R)	7" (178 mm)	10,000 units
MM.Ino	A COM.	13" (330 mm)	50,000 units

NOTE

1. For Paper/PE tape and reel specification/dimensions, please see the special data sheet "Packing" document.

FUNCTIONAL DESCRIPTION

POWER RATING

YC122 rated power at 70°C is 1/16 W

RATED VOLTAGE

The DC or AC (rms) continuous working voltage corresponding to the rated power is determined by the following formula:

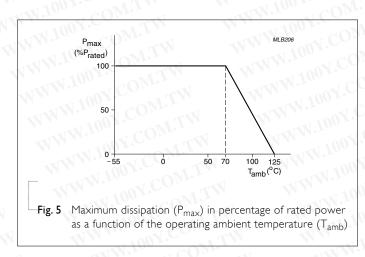
$$V = \sqrt{(P \times R)}$$

Where

V = Continuous rated DC or AC (rms) working voltage (V)

P = Rated power (W)

 $R = Resistance value (\Omega)$



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TESTS AND REQUIREMENTS

Table 4 Table 1 Table 4 Test condition, procedure and requirements

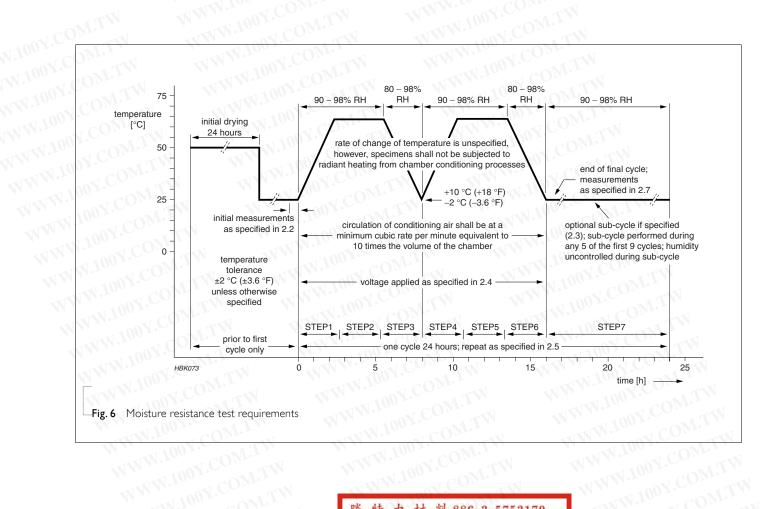
Temperature Coefficient of Resistance (T.C.R.) MIL-STD-202F-method 304; $At +25/-55$ °C and $+25/+125$ °C Refer to table 2 $IIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIII$	Table 4 Test co	ondition, procedure and require	ements	
Coefficient of Resistance (T.C.R.)	TEST	TEST METHOD	PROCEDURE	REQUIREMENTS
Formula: T.C.R. R ₂ -R ₁ x 106 (ppm/°C) 持力 材 料 886-3-5753170 特力 地子(深刻 86-21-54151736 t = +25 °C or specified room temperature t = +25	- 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		At +25/–55 °C and +25/+125 °C	Refer to table 2
特力 村 邦 886-3-5753170 特力 坦子(上脚 86-21-54151736 特力 坦子(上脚 86-21-54151736 特力 坦子(上脚 86-21-54151736 比特力 坦子(比脚 86-755-83298787 Http://www.100y.com.tw R ₂ = resistance at reference temperature R ₂ = resistance at test temperature in ohms R ₂ = resistance at test temperature in ohms R ₃ = resistance at test temperature in ohms R ₄ = resistance at reference temperature in ohms R ₅ = resistance at reference temperature in ohms R ₇ = resistance at reference temperature in ohms R ₈ = resistance at reference temperature in ohms R ₇ = resistance at reference temperature in ohms R ₈ = resistance at reference temperature in ohms R ₇ = resistance at reference temperature in ohms R ₇ = resistance at reference temperature in ohms R ₈ = resistance at reference temperature in ohms R ₈ = resistance at reference temperature in ohms R ₈ = resistance at reference temperature in ohms R ₈ = resistance at reference temperature in ohms R ₇ = resistance at reference temperature in ohms R ₈ = resistance at reference temperature in ohms R ₈ = resistance at reference temperature in ohms R ₈ = resistance at reference temperature in ohms R ₈ = resistance at reference temperature in ohms R ₈ = resistance at reference temperature in ohms R ₈ = resistance at reference temperature in ohms R ₈ = resistance at reference temperature in ohms R ₈ = resistance at reference temperature in ohms R ₁ = resistance at reference temperature in ohms R ₁ = resistance at reference temperature in ohms R ₁ = resistance at reference temperature in ohms R ₁ = resistance at reference temperature in ohms R ₁ = resistance at reference temperature in ohms R ₁ = resistance at reference temperature in ohms R ₁ = resistance at reference temperature in ohms R ₂ = resistance at reference temperature in ohms R ₂ = resistance at reference temperature in ohms R ₂ = resistance at test temperature in ohms R ₂ = resistance at test temperature in ohms R ₂ = resistance at test temperature tent (10% +0.05 Ω) for 1% tol. No visible damage The resist		MMM.100X;		
# 力电子に謝 86-21-54151736 ## 力电子に謝 86-755-83298787 ## Http://www. 100y. com. tw Thermal Shock MilL-STD-202F-method 107G; EC 60115-1 4.19 At -65 (+0/-10) °C for 2 minutes and at +125 ±(0.5% +0.05 \Omega) for 1% tol. ±(1.0% +0.05 \Omega) for 1% tol. ±(1.0% +0.05 \Omega) for 5% tol. No visible damage Short Time Overload MilL-STD-202F-method 302; EC 60115-1 4.13 EC 60115-1 4.6.1.1 Type YC122 Voltage (DC) 100 \V Dielectric Withstand Voltage MilL-STD-202F-method 210C; EC 60115-1 4.18 EC 60115-1 4.18 Life MilL-STD-202F-method 108A; At 70 ±2 °C for 1,000 hours; RCWV applied for ±(0.5% +0.05 \Omega) for 1% tol. ±(1.0% +0.05 \Omega) for 5% tol. No visible damage Life MilL-STD-202F-method 108A; At 70 ±2 °C for 1,000 hours; RCWV applied for ±(0.5% +0.05 \Omega) for 1% tol. 1 (1.0% +0.05 \Omega) for 5% tol. No visible damage Life MilL-STD-202F-method 210C; Unmounted chips; 260 ±5 °C for 10 ±1 seconds ±(0.5% +0.05 \Omega) for 1% tol. 1 (1.0% +0.05 \Omega) for 5% tol. No visible damage Life MilL-STD-202F-method 210C; Unmounted chips; 260 ±5 °C for 10 ±1 seconds ±(0.5% +0.05 \Omega) for 1% tol. 1 (1.0% +0.05 \Omega) for 5% tol. No visible damage Life MilL-STD-202F-method 108A; At 70 ±2 °C for 1,000 hours; RCWV applied for ±(1.0% +0.05 \Omega) for 1% tol. 1 (1.0% +0.05 \Omega) for 1% tol. ±(1.0% +0.05 \Omega) for 5% tol. No visible damage ±(1.0% +0.05 \Omega) for 1% tol. ±(1.0% +0.05 \Omega) fo	OOY.CO.	N WWW.1005	T.C.R = $\frac{ t_2 - t_1 }{ R_1(t_2 - t_1) } \times 10^6 \text{ (ppm/°C)}$	
### ### ### ### ### ### ### ### ### ##				
R ₁ = resistance at reference temperature in ohms R ₂ = resistance at test temperature in ohms Etc (+0.05 \((1.0% +0.05 \(\Omega \)) for 1% tol. R ₂ + 0.05 \(\Omega \) for 5% tol. No visible damage Etc (+0.05 \(\Omega \) for 5% tol. No visible damage R ₂ = resistance at test temperature and at +125 ±(0.5% +0.05 \(\Omega \) for 1% tol. Etc (+0.05 \(\Omega \) for 1% tol. R ₂ = resistance at test temperature and at +125 ±(0.5% +0.05 \(\Omega \) for 1% tol. R ₂ = resistance at test temperature and at +125 ±(0.5% +0.05 \(\Omega \) for 1% tol. R ₂ = resistance at test temperature and at +125 ±(0.5% +0.05 \(\Omega \) for 1% tol. R ₂ = resistance at test temperature and at +125 ±(0.5% +0.05 \(\Omega \) for 1% tol. R ₂ = resistance at test temperature and at +125 ±(0.5% +0.05 \(\Omega \) for 1% tol. R ₂ = resistance at test temperature and at +125 ±(0.5% +0.05 \(\Omega \) for 1% tol. R ₂ = resistance at test temperature and at +125 ±(0.5% +0.05 \(\Omega \) for 1% tol. R ₂ = resistance at test temp		and the second s		
R ₂ = resistance at test temperature in ohms		1.		
Thermal Shock MIL-STD-202F-method 107G; IEC 60115-1 4.19 At −65 (+0/−10) °C for 2 minutes and at +125 ±(0.5% +0.05 Ω) for 1% tol. ±(1.0% +0.05 Ω) for 5% tol. Low Temperature Operation MIL-R-55342D-Para 4.7.4 At −65 (+0/−5) °C for 1 hour; RCWV applied for ±(0.5% +0.05 Ω) for 1% tol. ±(1.0% +0.05 Ω) for 5% tol. No visible damage Short Time Overload MIL-R-55342D-Para 4.7.5; IEC 60115-1 4.13 2.5 × RCWV applied for 5 seconds at room temperature ±(1.0% +0.05 Ω) for 1% tol. ±(2.0% +0.05 Ω) for 1% tol. No visible damage Insulation Resistance MIL-STD-202F-method 302; IEC 60115-1 4.6.1.1 RCOV for 1 minute ≥10 GΩ Dielectric Withstand Voltage MIL-STD-202F-method 301; IEC 60115-1 4.6.1.1 Maximum voltage (V _{mms}) applied for 1 minute No breakdown or flashover Resistance to Soldering Heat MIL-STD-202F-method 210C; IEC 60115-1 4.18 Unmounted chips: 260 ±5 °C for 10 ±1 seconds ±(0.5% +0.05 Ω) for 1% tol. No visible damage Life MIL-STD-202F-method 108A; At 70 ±2 °C for 1,000 hours; RCWV applied for ±(1/2 +0.05 Ω) for 1% tol. No visible damage	Http://www.1	100y. com. tw		
Low Temperature Operation Temperature Operation Temperature Operation MIL-R-55342D-Para 4.7.4 At -65 (+0/-5) °C for 1 hour; RCWV applied for ±(0.5% +0.05 Ω) for 1% tol. hovisible damage ±(1.0% +0.05 Ω) for 1% tol. hovisible damage ±(1.0% +0.05 Ω) for 1% tol. hovisible damage ±(1.0% +0.05 Ω) for 5% tol. hovisible damage ±(1.0% +0.05 Ω) for 1% tol. hovisible damage ±(1.0% +0.05 Ω) for 5% tol. hovisible damage ±(1.0% +0.05 Ω) for 1% tol. hovisible damage ±(1.0% +0.05 Ω	MAI 100 X CC	OM.TW WWW	R_2 = resistance at test temperature in ohms	ON.COM.TW
Low Temperature Operation MIL-R-55342D-Para 4.7.4 At −65 (+0/−5) °C for I hour; RCWV applied for ±(0.5% +0.05 Ω) for 1% tol. ±(1.0% +0.05 Ω) for 5% tol. No visible damage Short Time Overload MIL-R-55342D-Para 4.7.5; IEC 60115-1 4.13 2.5 × RCWV applied for 5 seconds at room temperature ±(1.0% +0.05 Ω) for 1% tol. ±(2.0% +0.05 Ω) for 1% tol. No visible damage Insulation Resistance MIL-STD-202F-method 302; IEC 60115-1 4.6.1.1 RCOV for I minute ≥10 GΩ Dielectric Withstand Voltage MIL-STD-202F-method 301; IEC 60115-1 4.6.1.1 Maximum voltage (V _{rms}) applied for I minute No breakdown or flashover Type YC122 Voltage (AC) Voltage (AC) 100 V _{rms} ±(0.5% +0.05 Ω) for 1% tol. ±(1.0% +0.05 Ω) for 5% tol. No visible damage Life MIL-STD-202F-method 108A; At 70 ±2 °C for 1,000 hours; RCWV applied for ±(1% +0.05 Ω) for 1% tol.	Thermal Shock	MIL-STD-202F-method 107G;		$\pm (0.5\%$ +0.05 $\Omega)$ for 1% tol.
Temperature Operation	MAM. TOOX.	IEC 60115-1 4.19	(+10/–0) °C for 2 minutes; 25 cycles	$\pm (1.0\% + 0.05 \Omega)$ for 5% tol.
Operation E(100 of Most 2) for 30 cm. No visible damage Short Time Overload MIL-STD-202F-para 4.7.5; IEC 60115-1 4.13 2.5 × RCWV applied for 5 seconds at room temperature ± (1.0% ±0.05 Ω) for 1% tol. ± (2.0% ±0.05 Ω) for 5% tol. No visible damage Insulation Resistance MIL-STD-202F-method 302; IEC 60115-1 4.6.1.1 RCOV for 1 minute ≥ 10 GΩ Dielectric Withstand Voltage MIL-STD-202F-method 301; IEC 60115-1 4.6.1.1 Maximum voltage (V _{rms}) applied for 1 minute No breakdown or flashover Type YC122 Voltage (AC) 100 V _{rms} Resistance to Soldering Heat MIL-STD-202F-method 210C; IEC 60115-1 4.18 Unmounted chips; 260 ±5 °C for 10 ±1 seconds ± (0.5% ±0.05 Ω) for 1% tol. No visible damage Life MIL-STD-202F-method 108A; At 70 ±2 °C for 1,000 hours; RCWV applied for ± (1% ±0.05 Ω) for 1% tol.		MIL-R-55342D-Para 4.7.4	- 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	$\pm (0.5\%$ +0.05 $\Omega)$ for 1% tol .
Short Time MilL-R-55342D-Para 4.7.5; 2.5 × RCWV applied for 5 seconds at room ±(1.0% ±0.05 Ω) for 1% tol. ±(2.0% ±0.05 Ω) for 5% tol. No visible damage ±(1.0% ±0.05 Ω) for 5% tol. No visible damage ±(1.0% ±0.05 Ω) for 5% tol. No visible damage ±(1.0% ±0.05 Ω) for 5% tol. No visible damage ±(1.0% ±0.05 Ω) for 5% tol. Type YC122 Voltage (DC) 100 V Voltage (DC) 100 V Voltage EC 60115-1 4.6.1.1 Type YC122 Voltage (AC) 100 V _{ms} No breakdown or flashover Type YC122 Voltage (AC) 100 V _{ms} EC 60115-1 4.18 ±(1.0% ±0.05 Ω) for 1% tol. 1EC 60115-1 4.18 ±(1.0% ±0.05 Ω) for 5% tol. No visible damage ±(1.0% ±0.05 Ω) for 5% tol. No visible damage ±(1.0% ±0.05 Ω) for 1% tol. 150 ±0.05 Ω 100 ±0.05	- 1 T		45 (+5/–0) minutes	$\pm (1.0\% +0.05 \Omega)$ for 5% tol.
Overload IEC 60115-1 4.13 temperature ± (2.0% +0.05 Ω) for 5% tol. No visible damage Insulation Resistance MIL-STD-202F-method 302; IEC 60115-1 4.6.1.1 RCOV for 1 minute ≥10 GΩ Dielectric Withstand Voltage MIL-STD-202F-method 301; IEC 60115-1 4.6.1.1 Maximum voltage (V _{rms}) applied for 1 minute No breakdown or flashover Type YC122 Voltage (AC) 100 V _{rms} YC122 Voltage (AC) 100 V _{rms} No breakdown or flashover Resistance to Soldering Heat MIL-STD-202F-method 210C; IEC 60115-1 4.18 Unmounted chips; 260 ±5 °C for 10 ±1 seconds ± (1.0% +0.05 Ω) for 1% tol. No visible damage Life MIL-STD-202F-method 108A; At 70 ±2 °C for 1,000 hours; RCWW applied for ± (1% +0.05 Ω) for 1% tol.	Operation			No visible damage
Insulation Resistance IEC 60115-1 4.6.1.1 Type YC122 Voltage (DC) 100 V	Short Time	MIL-R-55342D-Para 4.7.5;	2.5 × RCWV applied for 5 seconds at room	$\pm (1.0\% +0.05 \Omega)$ for 1% tol.
Insulation Resistance MIL-STD-202F-method 302; IEC 60115-1 4.6.1.1 RCOV for 1 minute Type YC122 $≥10 GΩ$ Dielectric Withstand Voltage MIL-STD-202F-method 301; IEC 60115-1 4.6.1.1 Maximum voltage (V _{ms}) applied for 1 minute Type YC122 No breakdown or flashover Voltage (AC) 100 V _{ms} Resistance to Soldering Heat MIL-STD-202F-method 210C; IEC 60115-1 4.18 Unmounted chips; 260 ±5 °C for 10 ±1 seconds ± (0.5% +0.05 Ω) for 1% tol. + (1.0% +0.05 Ω) for 5% tol. No visible damage Life MIL-STD-202F-method 108A; At 70 ±2 °C for 1,000 hours; RCWV applied for ± (1% +0.05 Ω) for 1% tol.	Overload	IEC 60115-1 4.13	temperature	$\pm (2.0\% + 0.05 \ \Omega)$ for 5% tol.
Type YC122 Yoltage (DC) 100 V				No visible damage
	_			≥10 GΩ
Dielectric Withstand VoltageMIL-STD-202F-method 301; IEC 60115-1 4.6.1.1Maximum voltage (V _{rms}) applied for 1 minute Type YC122 Voltage (AC)No breakdown or flashoverResistance to Soldering HeatMIL-STD-202F-method 210C; IEC 60115-1 4.18Unmounted chips; 260 ± 5 °C for 10 ± 1 seconds $\pm (0.5\% \pm 0.05 \Omega)$ for 1% tol. No visible damageLifeMIL-STD-202F-method 108A;At 70 ± 2 °C for 1,000 hours; RCWV applied for $\pm (1\% \pm 0.05 \Omega)$ for 1% tol.	Resistance	IEC 60115-1 4.6.1.1	Type YC122	
Withstand Voltage IEC 60115-1 4.6.1.1 Type YC122 $\overline{Voltage (AC)}$ $\overline{I00 V_{rms}}$ $\overline{I00 V_{rms}}$ $\overline{IEC 60115-1 4.6.1.1}$ \overline{IVpe} $IVpe$			Voltage (DC)	
Voltage (AC) 100 \vee_{ms} Resistance to Soldering Heat MIL-STD-202F-method 210C; Unmounted chips; 260 ±5 °C for 10 ±1 seconds ±(0.5% +0.05 Ω) for 1% tol. \pm (1.0% +0.05 Ω) for 5% tol. No visible damage			Maximum voltage (V _{ms}) applied for 1 minute	No breakdown or flashover
Resistance to Soldering Heat STD-202F-method 210C; Unmounted chips; 260 ± 5 °C for 10 ± 1 seconds $\pm (0.5\% \pm 0.05 \Omega)$ for 1% tol. No visible damage		IEC 60115-1 4.6.1.1	Type YC122	
Soldering Heat $\pm (1.0\% \pm 0.05 \ \Omega)$ for 5% tol. No visible damage $\pm (1.0\% \pm 0.05 \ \Omega)$ for 5% tol. No visible damage	Voltage		Voltage (AC) 100 V _{rms}	
Heat No visible damage Life MIL-STD-202F-method I08A; At 70 ± 2 °C for I,000 hours; RCWV applied for $\pm (1\% + 0.05 \Omega)$ for 1% tol.	Resistance to	MIL-STD-202F-method 210C;	Unmounted chips; 260 ±5 °C for 10 ±1 seconds	$\pm (0.5\% + 0.05 \ \Omega)$ for 1% tol.
Life MIL-STD-202F-method I08A; At 70 ± 2 °C for 1,000 hours; RCWV applied for $\pm (1\% \pm 0.05 \ \Omega)$ for 1% tol.	•	IEC 60115-1 4.18		$\pm (1.0\% + 0.05 \Omega)$ for 5% tol.
	Heat			No visible damage
IEC 60115-1 4.25.1 I.5 hours on and 0.5 hour off $\pm (3\% + 0.05 \Omega)$ for 5% tol.	Life	MIL-STD-202F-method 108A;		$\pm (1\% + 0.05 \Omega)$ for 1% tol.
WWW.		IEC 60115-1 4.25.1	1.5 hours on and 0.5 hour off	$\pm (3\% + 0.05 \Omega)$ for 5% tol.
		WWW.	OOX.COM.TW WWW.	

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TEST	TEST METHOD	PROCEDURE	REQUIREMENTS
Solderability	MIL-STD-202F-method 208A;	Solder bath at 245 ±3 °C	Well tinned (≥95% covered)
	IEC 60115-1 4.17	Dipping time: 2 ±0.5 seconds	No visible damage
Bending	JIS C 5202.6.14;	Resistors mounted on a 90 mm glass epoxy	$\pm (1.0\%$ +0.05 $\Omega)$ for 1% tol.
Strength	IEC 60115-1 4.15	resin PCB (FR4)	\pm (1.0% +0.05 Ω) for 5% tol.
		Bending: I mm	No visible damage
Resistance to Solvent	MIL-STD-202F-method 215; IEC 60115-1 4.29	Isopropylalcohol (C_3H_7OH) or dichloromethane (CH_2Cl_2) followed by brushing	No smeared
Noise	JIS C 5202 5.9;	Maximum voltage (V _{rms}) applied.	Resistors range Value
	IEC 60115-1 4.12		R < 100 Ω 10 dB
			$\frac{100 \Omega \leq R < 1 K\Omega}{20 dB}$
	勝特力材料886	22.5752170	$1 \text{ K}\Omega \leq R < 10 \text{ K}\Omega \qquad 30 \text{ dB}$
	胜特力电子(上海) 86-		$\frac{10 \text{ K}\Omega \leq \text{R} < 100 \text{ K}\Omega}{40 \text{ dB}}$
	胜特力电子(深圳) 86-		$\frac{100 \text{ K}\Omega \leq \text{R} < 1 \text{ M}\Omega}{46 \text{ dB}}$
	Http://www.100		$\frac{1 \text{ M}\Omega \leq R \leq 22 \text{ M}\Omega}{1 \text{ M}\Omega \leq R \leq 22 \text{ M}\Omega} \qquad 48 \text{ dB}$
Humidity	JIS C 5202 7.5;	1,000 hours; 40 ±2 °C; 93(+2/–3)% RH	WWW.100Y.COM.TW
(steady state)	IEC 60115-8 4.24.8	RCWV applied for 1.5 hours on and 0.5 hour off	$\pm (0.5\% + 0.05 \Omega)$ for 1% tol.
		WWW.Ito COM. TW	$\pm (2.0\% + 0.05 \Omega)$ for 5% tol.
	EIA/IS 4.13B;	Solder bath at 260 ±5 °C	No visible damage
Leaching	IEC 60115-8 4.18	Dipping time: 30 ± 1 seconds	INO VISIDIE UdiTiage
Leaching		Dipping time. 30 ±1 3ccond3	
Leaching			
Intermittent	JIS C 5202 5.8	At room temperature; 2.5 × RCWV applied for	$\pm (1.0\% +0.05 \Omega)$ for 1% tol.
WW	MM:100 TCOM:1	At room temperature; 2.5 × RCWV applied for I second on and 25 seconds off; total 10,000 cycles	$\pm (1.0\% + 0.05 \Omega)$ for 1% tol. $\pm (2.0\% + 0.05 \Omega)$ for 5% tol.
Intermittent Overload Resistance to	MM:100 TCOM:1	I second on and 25 seconds off; total 10,000	, M, , , 100 p,
Intermittent Overload	JIS C 5202 5.8	I second on and 25 seconds off; total 10,000 cycles	, M, , , 100 p,
Intermittent Overload Resistance to	JIS C 5202 5.8	I second on and 25 seconds off; total 10,000 cycles	, , , , , , , , , , , , , , , , , , , ,
Intermittent Overload Resistance to Vibration Moisture Resistance	JIS C 5202 5.8 On request	I second on and 25 seconds off; total 10,000 cycles On request	$\pm (2.0\% + 0.05 \Omega)$ for 5% tol.
Intermittent Overload Resistance to Vibration Moisture	JIS C 5202 5.8 On request MIL-STD-202F-method 106F;	I second on and 25 seconds off; total 10,000 cycles On request 42 cycles; total 1,000 hours	$\pm (2.0\% + 0.05 \ \Omega)$ for 5% tol. $\pm (0.5\% + 0.05 \Omega)$ for 1% tol.

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W.100Y.COM.TW REVISION HISTORY

REVISION	DATE	CHANGE NOTIFICATION	DESCRIPTION
Version I	Dec 21, 2004	MAM.100X.COWILA	- New datasheet for 2 × 0402 thick film 1% and 5% with lead-fr terminations
			- Replace the 2 \times 0402 part of pdf files: ARV321_322_51_PbFn
ON COM	W	MAM. TOON COM	- Test method and procedure updated
Version 0	Dec 05, 2003	- MAN, TOO COM.	WWW.roov.COM.

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